SPEC® CINT2006 Result

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Acer Incorporated

(Test Sponsor: Intel Corporation)

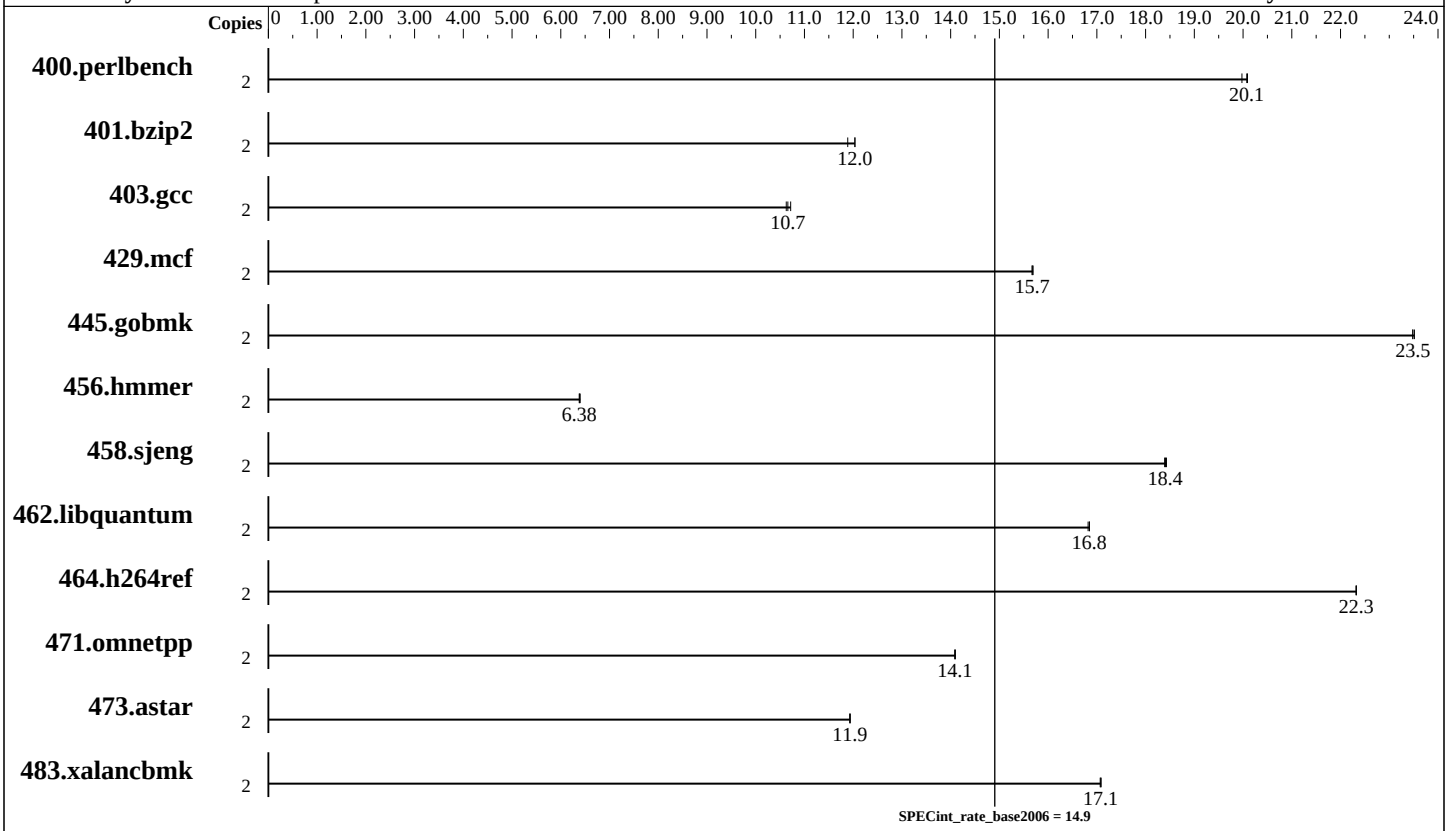
Acer Ferrari 5000 (AMD Turion 64 X2 TL-60)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 14.9

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007



Hardware

CPU Name:	AMD Turion 64 X2 TL-60
CPU Characteristics:	2.0 GHz 2x512KB L2
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Samsung DDR2-667 CL5)
Disk Subsystem:	Hitachi 160GB 5400rpm SATA HDD
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**973**	**20.1**	978	20.0	973	20.1							
401.bzip2	2	1623	11.9	1604	12.0	**1604**	**12.0**							
403.gcc	2	**1510**	**10.7**	1515	10.6	1502	10.7							
429.mcf	2	**1164**	**15.7**	1164	15.7	1162	15.7							
445.gobmk	2	**893**	**23.5**	892	23.5	893	23.5							
456.hmmer	2	2916	6.40	**2924**	**6.38**	2924	6.38							
458.sjeng	2	**1315**	**18.4**	1316	18.4	1313	18.4							
462.libquantum	2	2460	16.8	**2460**	**16.8**	2464	16.8							
464.h264ref	2	1983	22.3	**1983**	**22.3**	1983	22.3							
471.omnetpp	2	887	14.1	887	14.1	**887**	**14.1**							
473.astar	2	1176	11.9	1177	11.9	**1176**	**11.9**							
483.xalancbmk	2	809	17.1	808	17.1	**808**	**17.1**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-arch:SSE2 -Qprec-div- -O3 -Qipo /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F512000000
shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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