SPEC® CINT2006 Result

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ASUS Computer International

(Test Sponsor: Intel Corporation)

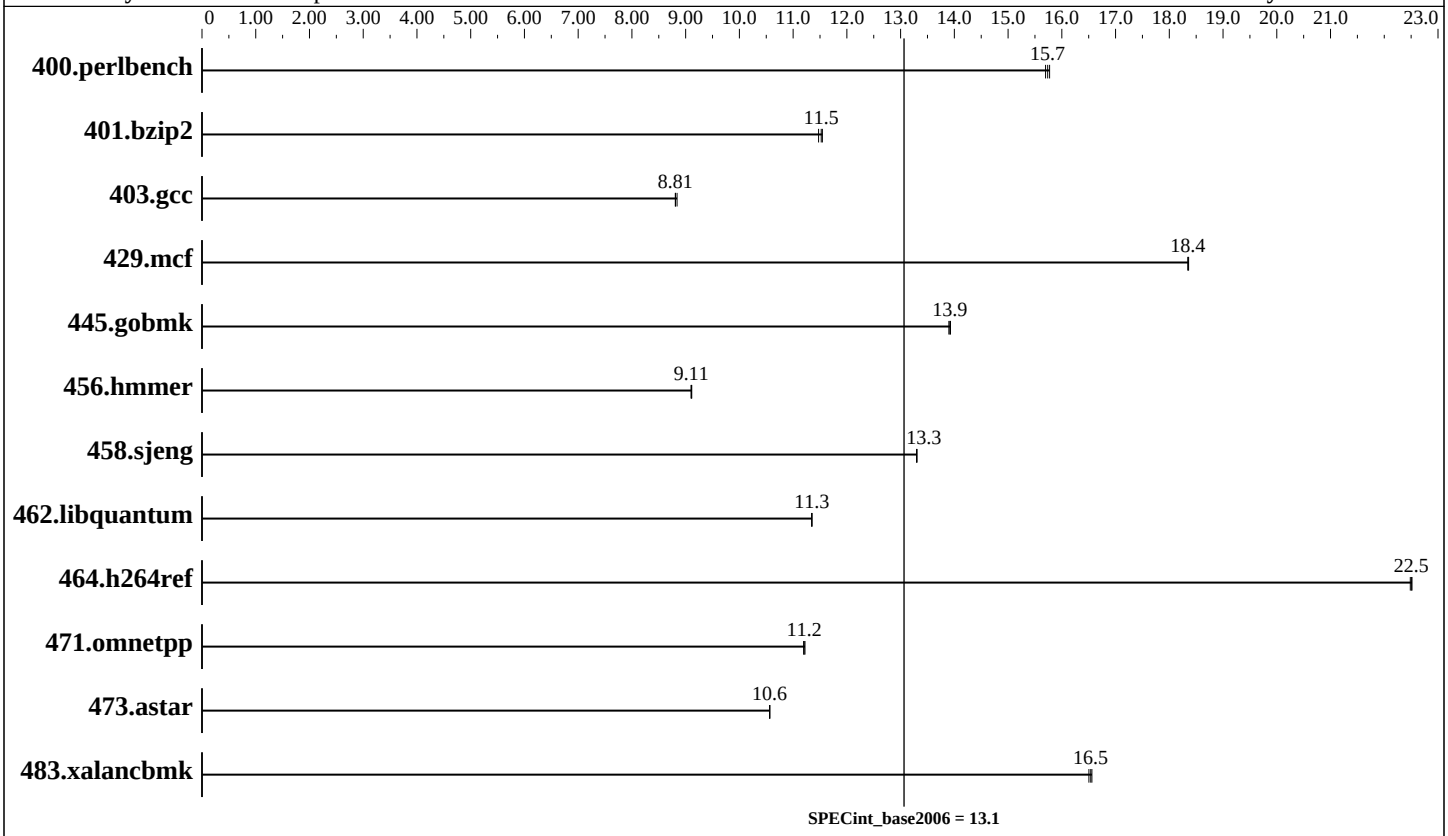
ASUS Lamborghini (Intel Core 2 Duo T7400)

SPECint®2006 = Not Run

SPECint_base2006 = 13.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Core 2 Duo T7400
CPU Characteristics:	2.16 GHz 4 MB L2
CPU MHz:	2167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Nanya DDR2-667 CL5)
Disk Subsystem:	Hitachi 160GB 5400rpm SATA HDD
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	619	15.8	**621**	**15.7**	622	15.7						
401.bzip2	836	11.5	841	11.5	**837**	**11.5**						
403.gcc	911	8.84	**914**	**8.81**	914	8.81						
429.mcf	497	18.4	497	18.3	**497**	**18.4**						
445.gobmk	753	13.9	755	13.9	**754**	**13.9**						
456.hmmer	**1024**	**9.11**	1025	9.10	1024	9.11						
458.sjeng	909	13.3	910	13.3	**910**	**13.3**						
462.libquantum	1826	11.3	**1826**	**11.3**	1826	11.4						
464.h264ref	**983**	**22.5**	983	22.5	984	22.5						
471.omnetpp	557	11.2	558	11.2	**558**	**11.2**						
473.astar	665	10.6	**665**	**10.6**	664	10.6						
483.xalancbmk	417	16.6	418	16.5	**417**	**16.5**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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