SPEC® CINT2006 Result

ASUS Computer International

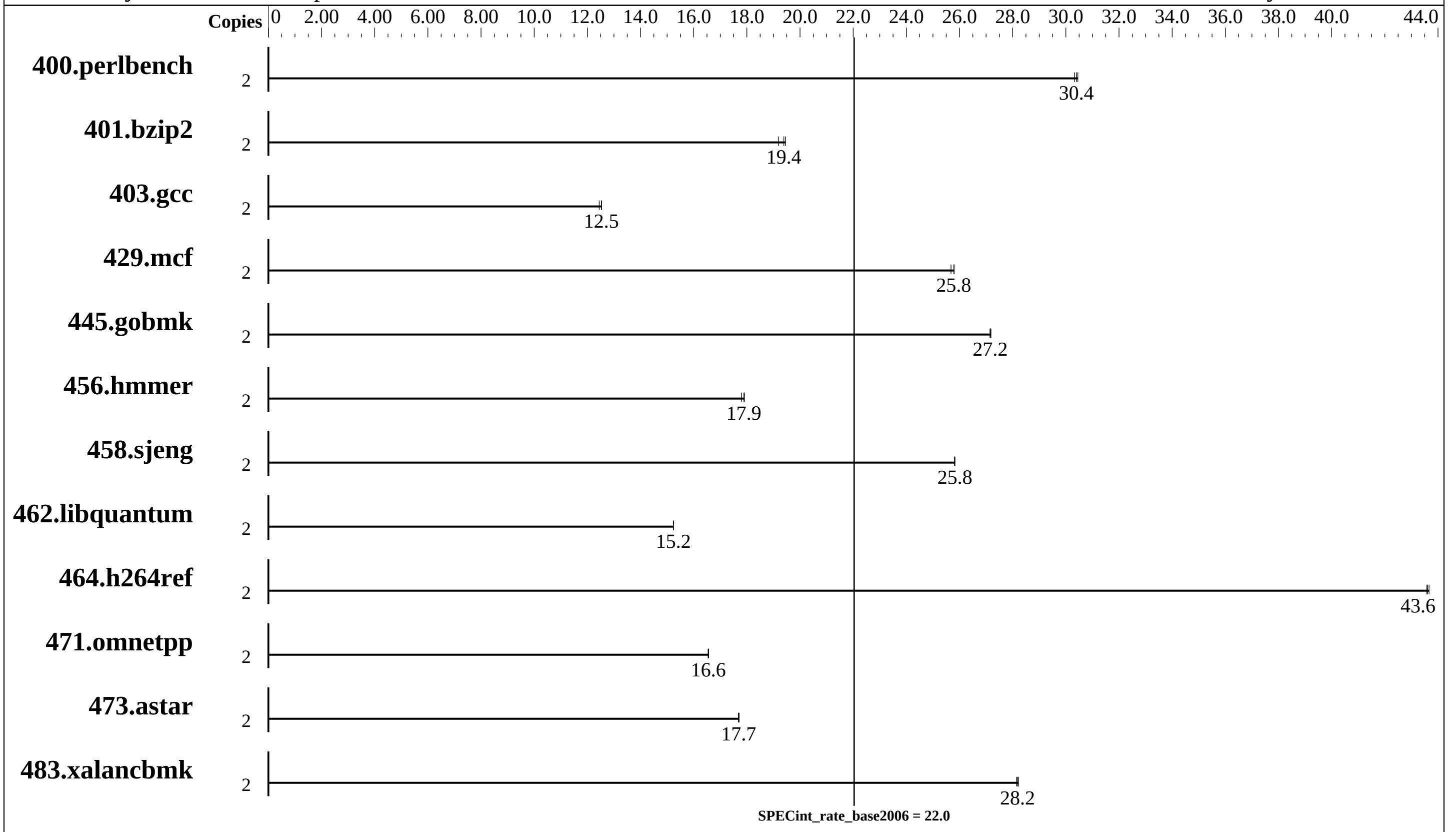
(Test Sponsor: Intel Corporation)

SPECint®_rate2006 = Not Run

ASUS Lamborghini (Intel Core 2 Duo T7400)

SPECint_rate_base2006 = 22.0

CPU2006 license: 13		**Test date:**	Jun-2007
Test sponsor:	Intel Corporation	**Hardware Availability:**	Jun-2007
Tested by:	Intel Corporation	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Core 2 Duo T7400
CPU Characteristics:	2.16 GHz 4 MB L2
CPU MHz:	2167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Nanya DDR2-667 CL5)
Disk Subsystem:	Hitachi 160GB 5400rpm SATA HDD
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**643**	**30.4**	644	30.3	642	30.5							
401.bzip2	2	1006	19.2	992	19.5	**995**	**19.4**							
403.gcc	2	**1285**	**12.5**	1284	12.5	1294	12.4							
429.mcf	2	710	25.7	**707**	**25.8**	707	25.8							
445.gobmk	2	772	27.2	773	27.1	**773**	**27.2**							
456.hmmer	2	1042	17.9	1049	17.8	**1043**	**17.9**							
458.sjeng	2	**937**	**25.8**	937	25.8	938	25.8							
462.libquantum	2	2719	15.2	**2720**	**15.2**	2720	15.2							
464.h264ref	2	**1015**	**43.6**	1014	43.7	1016	43.6							
471.omnetpp	2	755	16.6	**755**	**16.6**	756	16.5							
473.astar	2	793	17.7	**793**	**17.7**	794	17.7							
483.xalancbmk	2	489	28.2	490	28.1	**490**	**28.2**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
      -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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