



SPEC® CINT2006 Result

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HITACHI

SPECint®2006 = 15.8

BladeSymphony BS320 (Intel Xeon E5345)

SPECint_base2006 = 15.1

CPU2006 license: 872

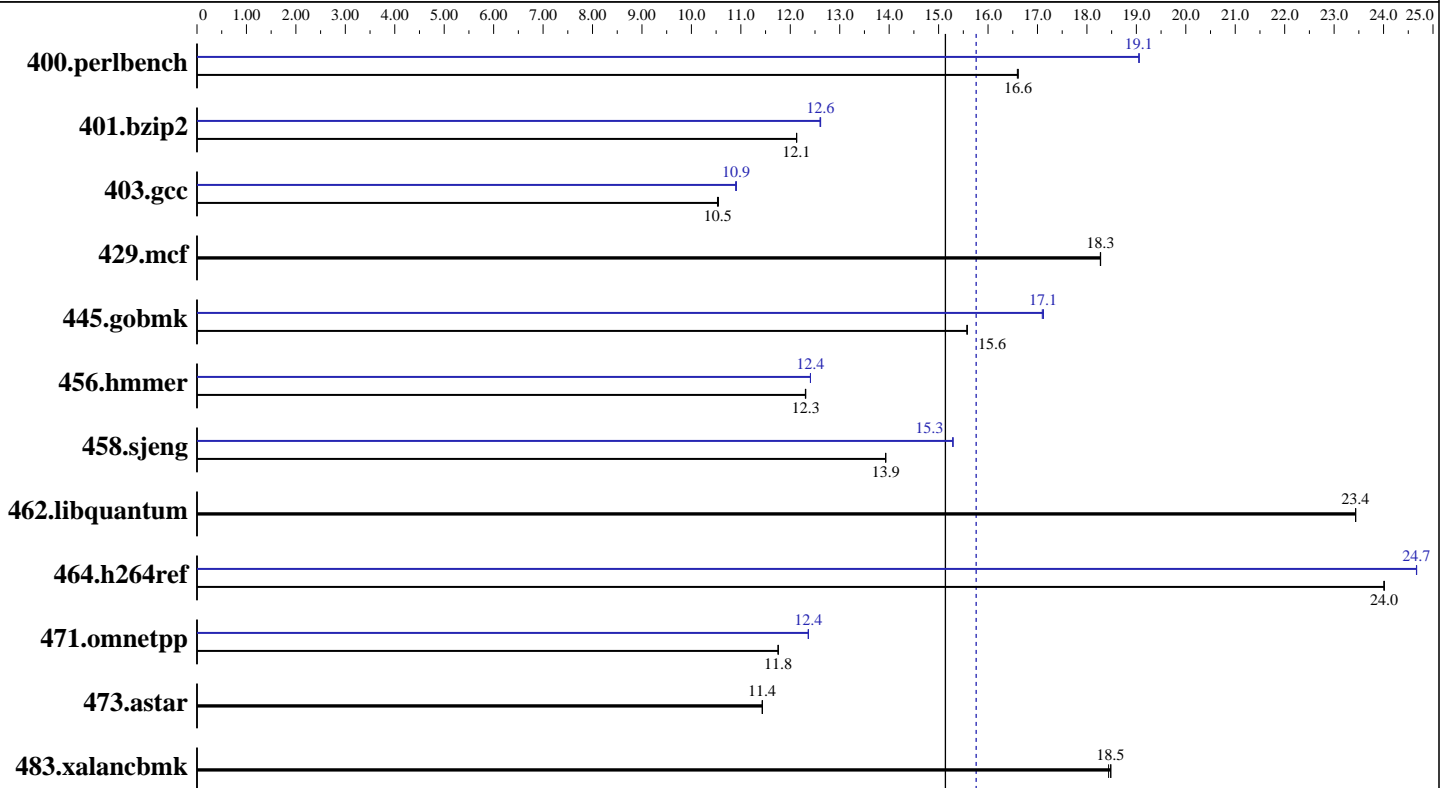
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Jul-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007



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Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB(4 x 2 GB PC2-5300F CAS 5-5-5)
 Disk Subsystem: 2 x 73 GB 10000rpm SAS
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 R2, Enterprise x64 Edition
 Compiler: Intel C++ Compiler for IA32 version 10.0 Build 20070426
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library, Version 8.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	588	16.6	<u>588</u>	<u>16.6</u>	589	16.6	<u>513</u>	<u>19.1</u>	513	19.1	513	19.1
401.bzip2	796	12.1	796	12.1	<u>796</u>	<u>12.1</u>	<u>765</u>	<u>12.6</u>	766	12.6	765	12.6
403.gcc	<u>764</u>	<u>10.5</u>	763	10.5	765	10.5	738	10.9	<u>738</u>	<u>10.9</u>	739	10.9
429.mcf	499	18.3	<u>499</u>	<u>18.3</u>	499	18.3	499	18.3	<u>499</u>	<u>18.3</u>	499	18.3
445.gobmk	<u>673</u>	<u>15.6</u>	674	15.6	673	15.6	614	17.1	<u>613</u>	<u>17.1</u>	613	17.1
456.hammer	758	12.3	758	12.3	<u>758</u>	<u>12.3</u>	<u>752</u>	<u>12.4</u>	752	12.4	752	12.4
458.sjeng	869	13.9	<u>869</u>	<u>13.9</u>	869	13.9	791	15.3	791	15.3	<u>791</u>	<u>15.3</u>
462.libquantum	884	23.4	884	23.4	<u>884</u>	<u>23.4</u>	884	23.4	884	23.4	<u>884</u>	<u>23.4</u>
464.h264ref	<u>922</u>	<u>24.0</u>	922	24.0	922	24.0	897	24.7	897	24.7	<u>897</u>	<u>24.7</u>
471.omnetpp	532	11.8	532	11.8	<u>532</u>	<u>11.8</u>	<u>505</u>	<u>12.4</u>	506	12.4	505	12.4
473.astar	614	11.4	614	11.4	<u>614</u>	<u>11.4</u>	614	11.4	614	11.4	<u>614</u>	<u>11.4</u>
483.xalancbmk	373	18.5	<u>373</u>	<u>18.5</u>	374	18.4	373	18.5	<u>373</u>	<u>18.5</u>	374	18.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
473.astar: -DSPEC_CPU_LITTLE_ENDIAN

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



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Peak Optimization Flags

C benchmarks:

400.perlbench: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/ic100.xml.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/ic100.xml.xml>



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