SPEC® CINT2006 Result

HITACHI

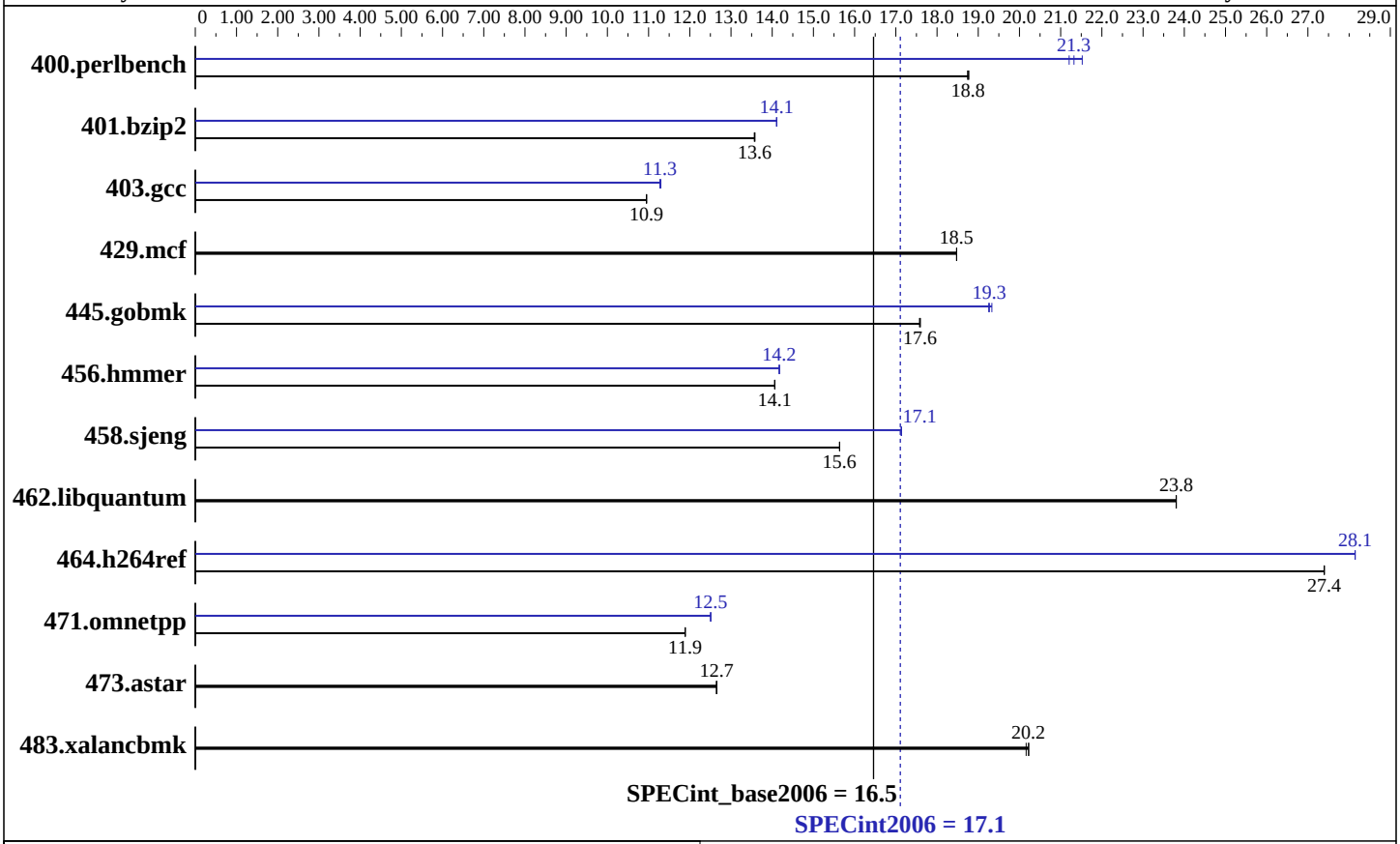
BladeSymphony BS1000 (Intel Xeon X5355)

SPECint®2006 = 17.1

SPECint_base2006 = 16.5

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jul-2007
Hardware Availability: Jan-2007
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB(8 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem:	2 x 73 GB 10000rpm SAS
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 R2, Enterprise x64 Edition
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library, Version 8.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	521	18.7	520	18.8	**521**	**18.8**	454	21.5	461	21.2	**458**	**21.3**
401.bzip2	**711**	**13.6**	711	13.6	711	13.6	684	14.1	684	14.1	**684**	**14.1**
403.gcc	**735**	**10.9**	735	11.0	735	10.9	714	11.3	**714**	**11.3**	713	11.3
429.mcf	494	18.5	**494**	**18.5**	494	18.5	494	18.5	**494**	**18.5**	494	18.5
445.gobmk	596	17.6	**596**	**17.6**	597	17.6	543	19.3	545	19.3	**544**	**19.3**
456.hmmer	663	14.1	**664**	**14.1**	664	14.1	**658**	**14.2**	659	14.2	658	14.2
458.sjeng	**774**	**15.6**	774	15.6	774	15.6	706	17.1	706	17.1	**706**	**17.1**
462.libquantum	**870**	**23.8**	871	23.8	870	23.8	**870**	**23.8**	871	23.8	870	23.8
464.h264ref	808	27.4	808	27.4	**808**	**27.4**	786	28.1	**786**	**28.1**	786	28.1
471.omnetpp	526	11.9	**526**	**11.9**	526	11.9	500	12.5	**500**	**12.5**	500	12.5
473.astar	**555**	**12.7**	555	12.7	555	12.7	**555**	**12.7**	555	12.7	555	12.7
483.xalancbmk	**341**	**20.2**	342	20.2	341	20.2	**341**	**20.2**	342	20.2	341	20.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Peak Optimization Flags

C benchmarks:

400.perlbench: `ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `Same as 400.perlbench`

403.gcc: `Same as 400.perlbench`

429.mcf: `basepeak = yes`

445.gobmk: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 400.perlbench`

462.libquantum: `basepeak = yes`

464.h264ref: `Same as 400.perlbench`

C++ benchmarks:

471.omnetpp: `ONESTEP -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/ic100.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/ic100.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
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