SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Lenovo Group Limited

(Test Sponsor: Intel Corporation)

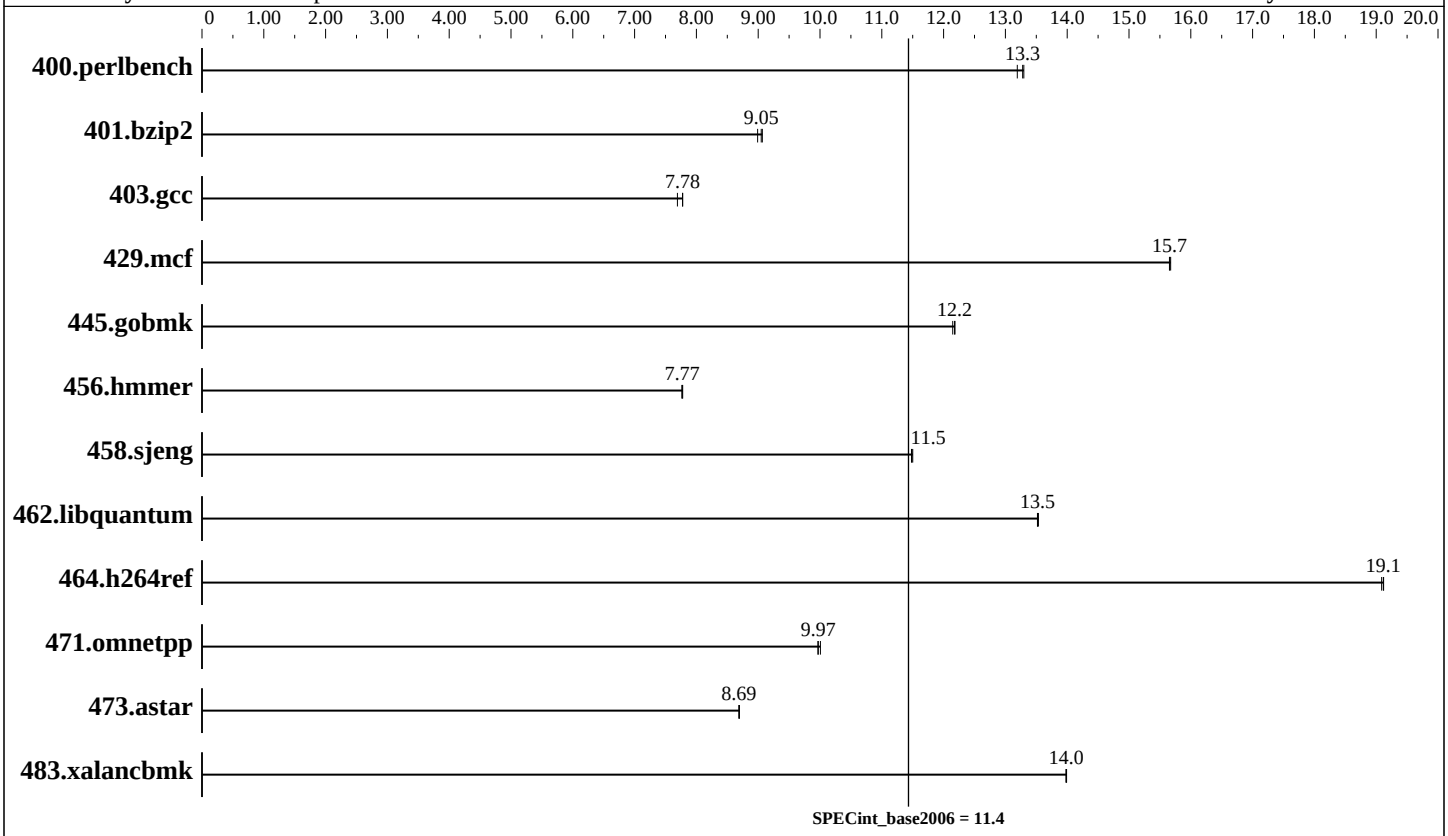
Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECint®2006 = Not Run

SPECint_base2006 = 11.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Core 2 Duo T7100
CPU Characteristics:	1.80 GHz, 2MB L2, 800 MHz Bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
Disk Subsystem:	Hitachi* HTS721010G9 SATA 100GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap library V7.4 from Microquill

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Lenovo Group Limited
(Test Sponsor: Intel Corporation)

Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECint2006 = Not Run

SPECint_base2006 = 11.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	741	13.2	**736**	**13.3**	735	13.3						
401.bzip2	1073	8.99	**1066**	**9.05**	1064	9.07						
403.gcc	1035	7.78	**1035**	**7.78**	1046	7.69						
429.mcf	582	15.7	**582**	**15.7**	583	15.7						
445.gobmk	864	12.1	861	12.2	**861**	**12.2**						
456.hmmer	1200	7.78	**1201**	**7.77**	1201	7.77						
458.sjeng	**1053**	**11.5**	1054	11.5	1052	11.5						
462.libquantum	1531	13.5	**1533**	**13.5**	1533	13.5						
464.h264ref	1157	19.1	**1158**	**19.1**	1159	19.1						
471.omnetpp	**627**	**9.97**	625	10.0	627	9.97						
473.astar	807	8.70	808	8.69	**808**	**8.69**						
483.xalancbmk	**493**	**14.0**	493	14.0	494	14.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:24:36 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.