



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®_rate2006 = 22.6

CELSIUS H250, Intel Core 2 Duo T7700 processor

SPECfp_rate_base2006 = 22.4

CPU2006 license: 22

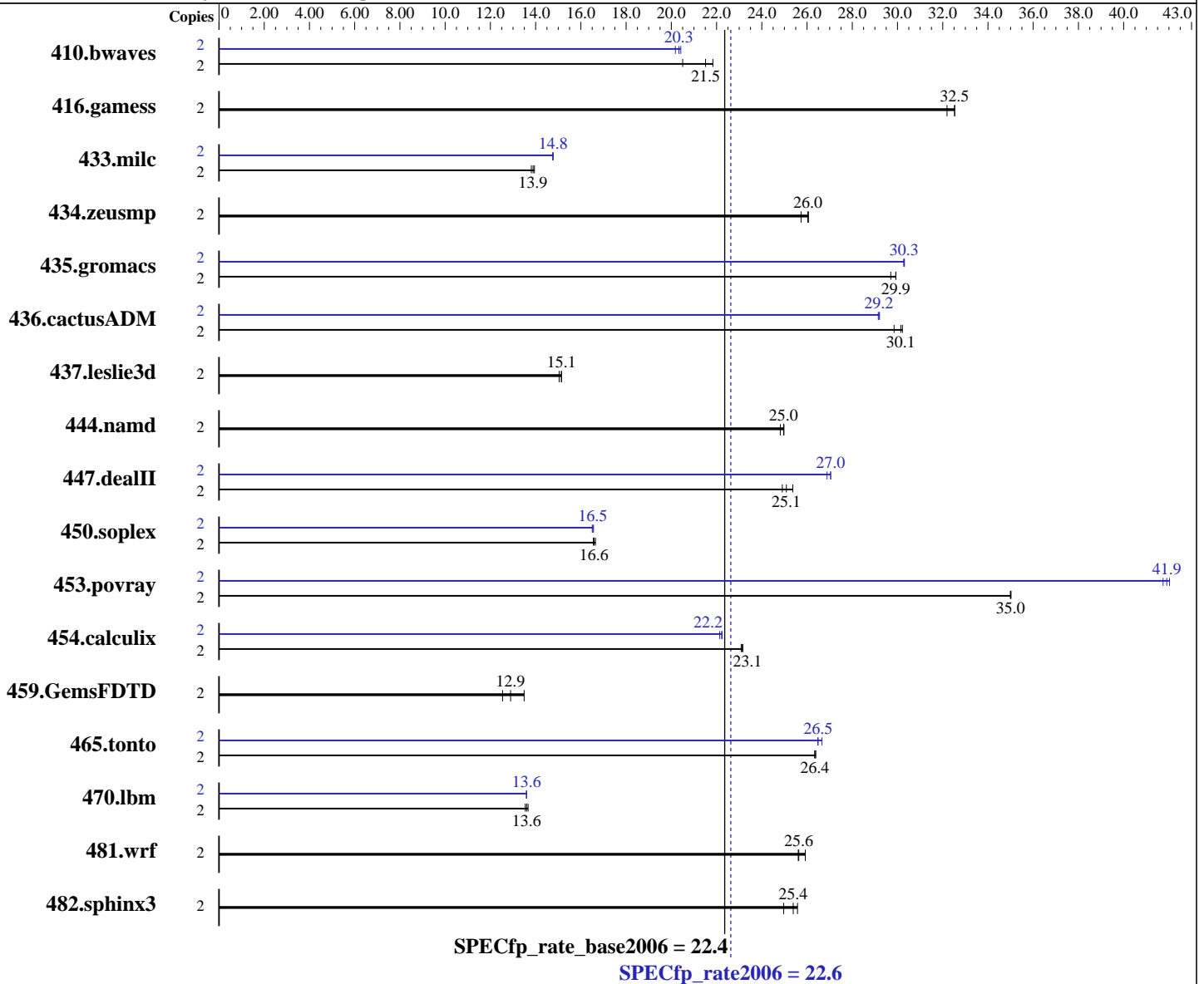
Test date: Aug-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Jul-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Jun-2007



Hardware

CPU Name: Intel Core 2 Duo T7700
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip

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Software

Operating System: Windows Vista Ultimate, 64 bit Version

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Hardware (Continued)

L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1 GB 667 MHz CL5 DDR2 SDRAM)
 Disk Subsystem: 1 x 80 GB SATA 7200 RPM
 Other Hardware: None

Software (Continued)

Compiler: Intel C++ Compiler
 . for applications running on Intel 64,
 . Version 10.0.026, Build 20070613
 Intel Visual Fortran Compiler
 . for applications running on Intel 64,
 . Version 10.0.026, Build 20070613
 Microsoft Visual Studio 2005 with SP1
 . (for libraries)

Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: Microquill SmartHeap Library 8.0 (64-bit version)

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1264	21.5	1245	21.8	1326	20.5	2	1331	20.4	1347	20.2	1337	20.3
416.gamess	2	1204	32.5	1204	32.5	1217	32.2	2	1204	32.5	1204	32.5	1217	32.2
433.milc	2	1316	13.9	1322	13.9	1330	13.8	2	1243	14.8	1244	14.8	1243	14.8
434.zeusmp	2	699	26.0	698	26.1	707	25.7	2	699	26.0	698	26.1	707	25.7
435.gromacs	2	477	29.9	477	29.9	481	29.7	2	472	30.3	471	30.3	471	30.3
436.cactusADM	2	793	30.1	791	30.2	801	29.9	2	819	29.2	820	29.1	818	29.2
437.leslie3d	2	1242	15.1	1242	15.1	1250	15.0	2	1242	15.1	1242	15.1	1250	15.0
444.namd	2	643	25.0	642	25.0	646	24.8	2	643	25.0	642	25.0	646	24.8
447.dealII	2	902	25.4	912	25.1	919	24.9	2	846	27.0	846	27.0	851	26.9
450.soplex	2	1002	16.6	1006	16.6	1008	16.6	2	1007	16.6	1009	16.5	1011	16.5
453.povray	2	304	35.0	304	35.0	304	35.0	2	255	41.7	253	42.0	254	41.9
454.calculix	2	715	23.1	712	23.2	714	23.1	2	745	22.1	742	22.2	742	22.2
459.GemsFDTD	2	1573	13.5	1646	12.9	1693	12.5	2	1573	13.5	1646	12.9	1693	12.5
465.tonto	2	746	26.4	747	26.4	747	26.3	2	743	26.5	738	26.7	743	26.5
470.ibm	2	2011	13.7	2029	13.5	2021	13.6	2	2021	13.6	2021	13.6	2021	13.6
481.wrf	2	862	25.9	871	25.6	872	25.6	2	862	25.9	871	25.6	872	25.6
482.sphinx3	2	1524	25.6	1561	25.0	1535	25.4	2	1524	25.6	1561	25.0	1535	25.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.



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General Notes

'start /b /wait /affinity' command is used to bind CPU(s) to processors. The Windows command "start /b /wait /affinity <hex_affinity_mask> application" starts the specified application without creating a new window (/b) and waits for its termination (/wait). Only the processors specified in <hex_affinity_mask> are allowed to execute the application. See the Windows documentation for the description of other parameters of the start command.

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Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.deallI: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64



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Base Optimization Flags

C benchmarks:

-fast -F950000000 shlW64M.lib

C++ benchmarks:

-fast -Qcxx-features -F950000000 shlW64M.lib

Fortran benchmarks:

-fast -F950000000

Benchmarks using both Fortran and C:

-fast -F950000000

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

470.lbm: Same as 433.milc

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F950000000

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: Same as 410.bwaves

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

436.cactusADM: Same as 435.gromacs

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.32.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.32.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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