SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

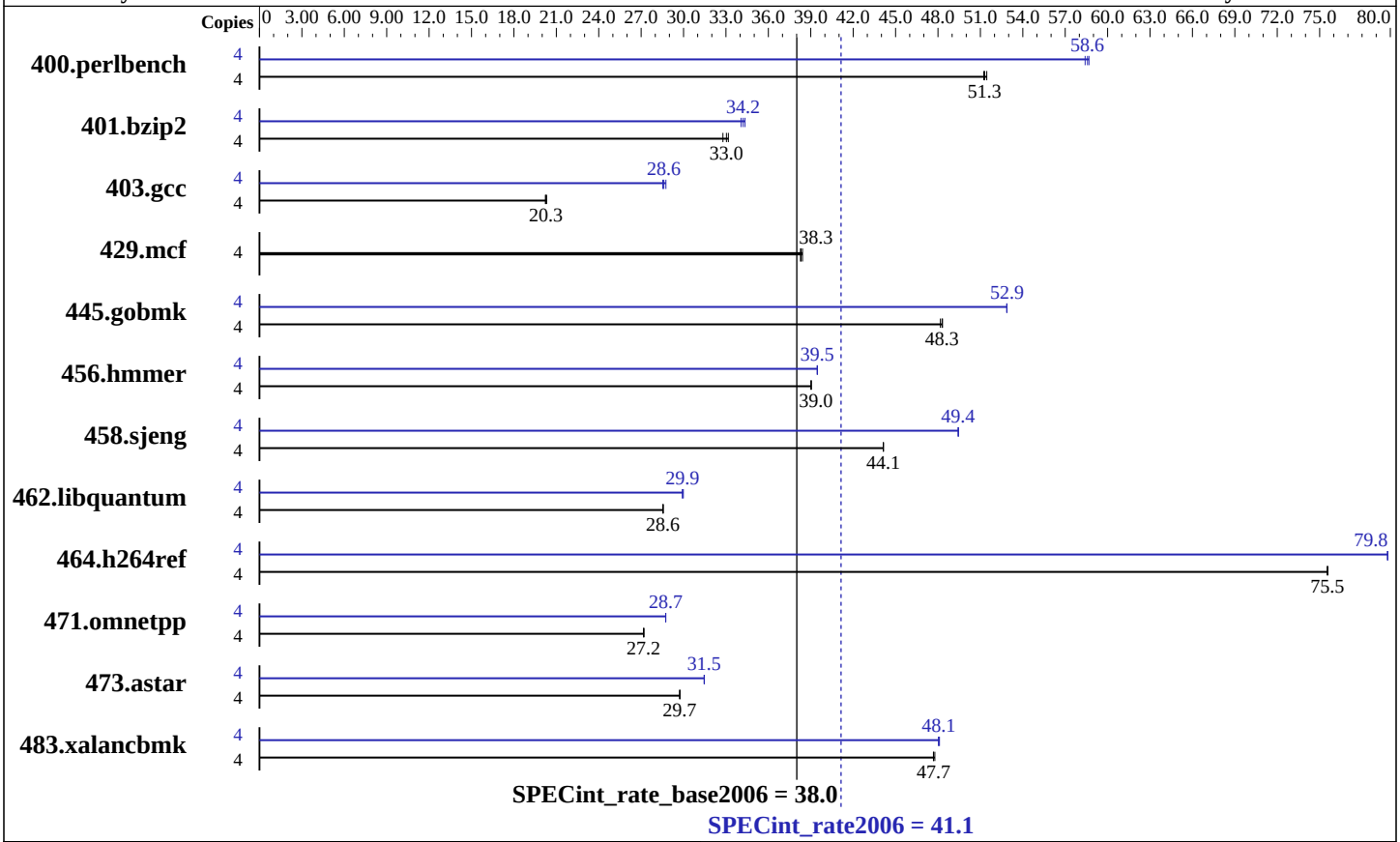
PowerEdge 2900 (Intel Xeon E5320, 1.86 GHz)

SPECint®_rate2006 = 41.1

SPECint_rate_base2006 = 38.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2007
Hardware Availability: Dec-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066 MHz Bus Speed
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8 x 1 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	146 GB SAS 15K RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library Version 8.0

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 2900 (Intel Xeon E5320, 1.86 GHz)

SPECint_rate2006 = 41.1

SPECint_rate_base2006 = 38.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2007
Hardware Availability: Dec-2006
Software Availability: Jun-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	760	51.5	763	51.2	**762**	**51.3**	4	666	58.7	669	58.4	**667**	**58.6**
401.bzip2	4	**1169**	**33.0**	1164	33.2	1177	32.8	4	**1128**	**34.2**	1133	34.1	1124	34.3
403.gcc	4	1592	20.2	**1589**	**20.3**	1585	20.3	4	1128	28.5	1120	28.8	**1126**	**28.6**
429.mcf	4	953	38.3	**952**	**38.3**	950	38.4	4	953	38.3	**952**	**38.3**	950	38.4
445.gobmk	4	868	48.3	**869**	**48.3**	871	48.2	4	794	52.9	**793**	**52.9**	793	52.9
456.hmmer	4	956	39.0	956	39.0	**956**	**39.0**	4	**946**	**39.5**	946	39.5	946	39.5
458.sjeng	4	**1097**	**44.1**	1097	44.1	1097	44.1	4	979	49.4	**979**	**49.4**	979	49.4
462.libquantum	4	2901	28.6	2904	28.5	**2903**	**28.6**	4	2764	30.0	2770	29.9	**2770**	**29.9**
464.h264ref	4	**1172**	**75.5**	1172	75.5	1171	75.6	4	1109	79.8	**1109**	**79.8**	1109	79.8
471.omnetpp	4	920	27.2	**920**	**27.2**	919	27.2	4	**870**	**28.7**	870	28.7	870	28.7
473.astar	4	945	29.7	**944**	**29.7**	944	29.7	4	892	31.5	893	31.5	**892**	**31.5**
483.xalancbmk	4	**579**	**47.7**	579	47.7	578	47.8	4	575	48.0	**574**	**48.1**	574	48.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
"start /b /wait /affinity" used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 -link /FORCE:MULTIPLE`

429.mcf: `basepeak = yes`

445.gobmk: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qipo -Qprec_div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE`

456.hmmer: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

458.sjeng: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:05:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 September 2007.