SPEC® CFP2006 Result

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Dell Inc.

(Test Sponsor: Intel Corporation)

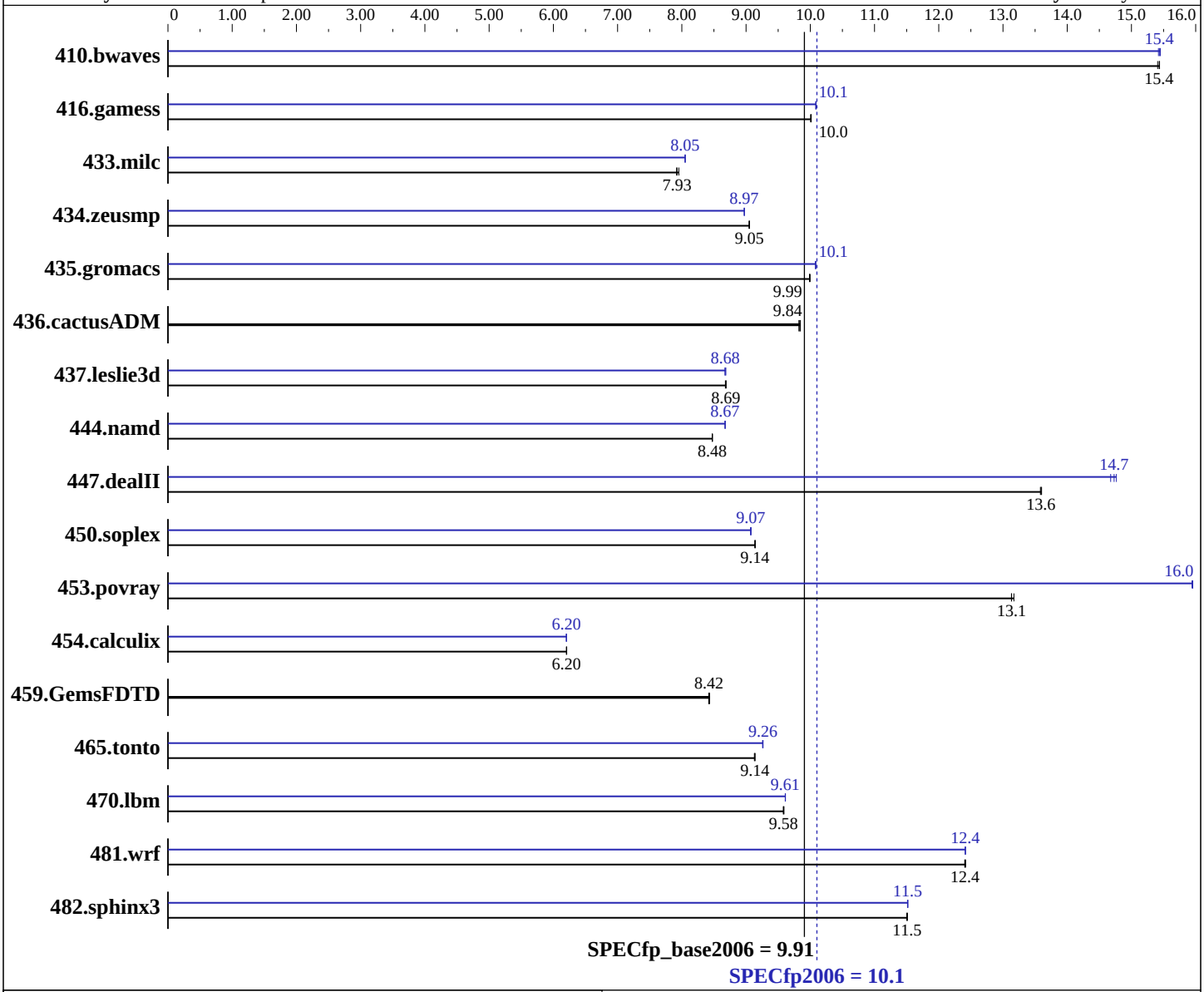
XPS M1710 (Intel Core Duo T2600)

SPECfp®2006 = 10.1

SPECfp_base2006 = 9.91

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2007
Hardware Availability: Jul-2007
Software Availability: May-2007



Hardware

CPU Name:	Intel Core Duo T2600
CPU Characteristics:	
CPU MHz:	2167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ and Fortran Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
Disk Subsystem: 100GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	880	15.4	**881**	**15.4**	882	15.4	881	15.4	880	15.5	**880**	**15.4**
416.gamess	1955	10.0	1957	10.0	**1956**	**10.0**	1941	10.1	**1941**	**10.1**	1941	10.1
433.milc	1154	7.95	1159	7.92	**1158**	**7.93**	1140	8.05	1140	8.05	**1140**	**8.05**
434.zeusmp	**1005**	**9.05**	1006	9.05	1005	9.06	1014	8.97	1014	8.97	**1014**	**8.97**
435.gromacs	715	9.99	**715**	**9.99**	714	10.0	708	10.1	708	10.1	**708**	**10.1**
436.cactusADM	1217	9.82	**1215**	**9.84**	1214	9.85	1217	9.82	**1215**	**9.84**	1214	9.85
437.leslie3d	1082	8.69	1083	8.68	**1082**	**8.69**	1082	8.69	**1083**	**8.68**	1084	8.67
444.namd	**946**	**8.48**	947	8.47	946	8.48	925	8.67	**925**	**8.67**	925	8.67
447.dealII	**842**	**13.6**	842	13.6	841	13.6	**777**	**14.7**	775	14.8	779	14.7
450.soplex	**913**	**9.14**	912	9.14	913	9.14	919	9.07	919	9.08	**919**	**9.07**
453.povray	404	13.2	405	13.1	**405**	**13.1**	**333**	**16.0**	334	15.9	333	16.0
454.calculix	1330	6.20	**1330**	**6.20**	1329	6.21	1330	6.20	**1330**	**6.20**	1330	6.20
459.GemsFDTD	1260	8.42	**1260**	**8.42**	1258	8.43	1260	8.42	**1260**	**8.42**	1258	8.43
465.tonto	1077	9.14	1078	9.13	**1077**	**9.14**	1063	9.26	1062	9.26	**1062**	**9.26**
470.lbm	**1434**	**9.58**	1434	9.58	1433	9.59	1430	9.61	1429	9.61	**1430**	**9.61**
481.wrf	**900**	**12.4**	900	12.4	899	12.4	**900**	**12.4**	900	12.4	900	12.4
482.sphinx3	1695	11.5	1693	11.5	**1693**	**11.5**	1693	11.5	**1692**	**11.5**	1692	11.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
nVidia GeForce Go 7900 discrete graphics card
Binaries were built on Windows XP Professional SP2
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F950000000

Benchmarks using both Fortran and C:
-QxP -Qipo -O3 -Qprec-div- /F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Oa /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Qscalar-rep- -Qprefetch /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Oa -Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qprefetch -Qcxx_features /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qcxx_features /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-QxP -Qipo -O3 -Qprec-div- /F950000000`

416.gamess: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F950000000`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -O2 -Qprec_div- -Qunroll0 -Qscalar-rep- /F950000000`

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -Qipo -O3 -Qprec-div- /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -Qipo -O3 -Qprec-div- -Oa /F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-QxP -Qipo -O3 -Qprec-div- /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.xml`

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