SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

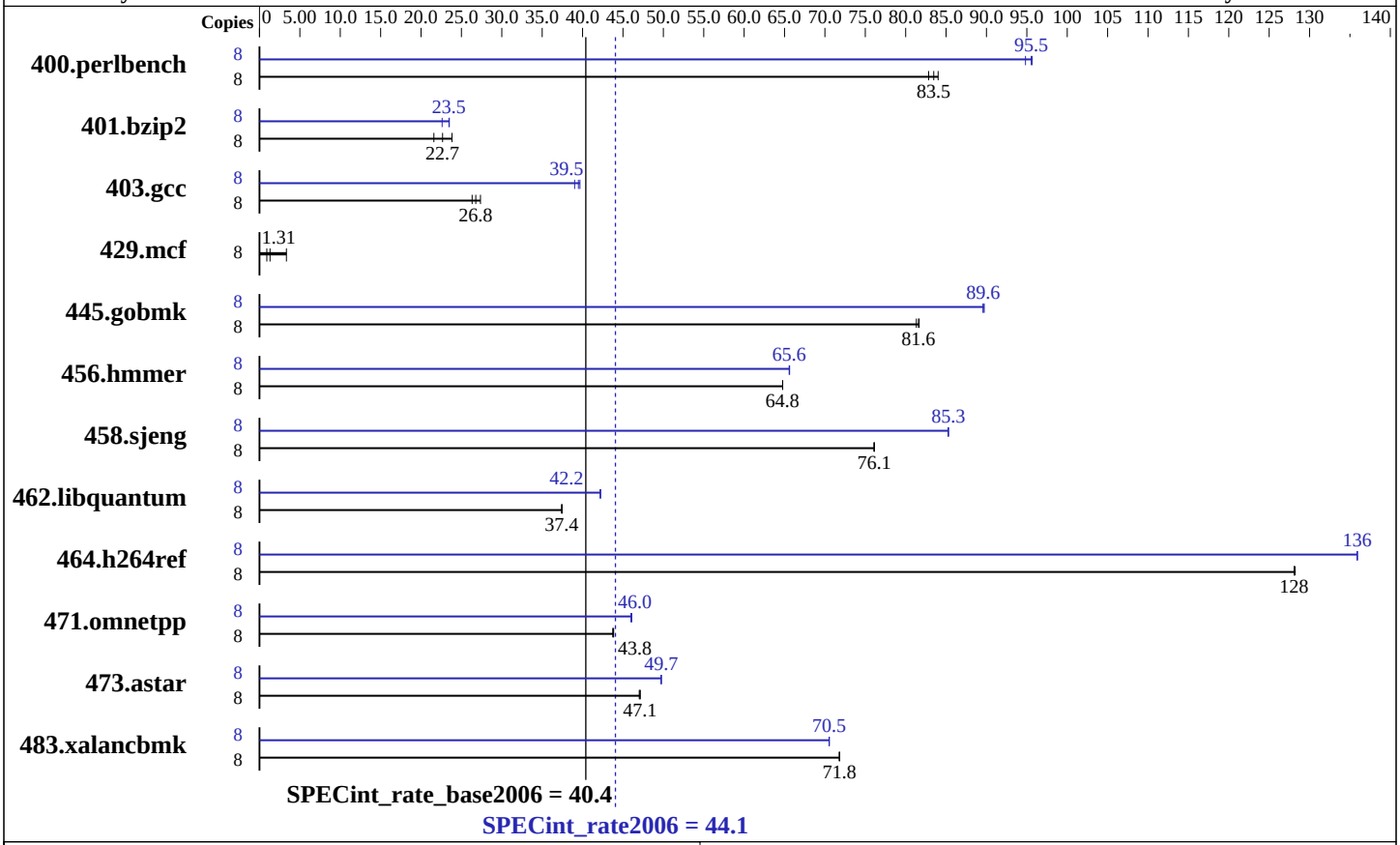
PowerEdge 1950 (Intel Xeon E5310, 1.60 GHz)

SPECint®_rate2006 = 44.1

SPECint_rate_base2006 = 40.4

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2007
Hardware Availability: Dec-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066 MHz Bus Speed
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4 x 1 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
Disk Subsystem:	146 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library Version 8.0

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge 1950 (Intel Xeon E5310, 1.60 GHz)

SPECint_rate2006 = 44.1

SPECint_rate_base2006 = 40.4

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2007
Hardware Availability: Dec-2006
Software Availability: Jun-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**936**	**83.5**	930	84.0	944	82.8	8	824	94.8	**818**	**95.5**	817	95.7
401.bzip2	8	3576	21.6	3239	23.8	**3406**	**22.7**	8	3285	23.5	3414	22.6	**3291**	**23.5**
403.gcc	8	2354	27.4	**2405**	**26.8**	2446	26.3	8	**1631**	**39.5**	1651	39.0	1623	39.7
429.mcf	8	**55856**	**1.31**	80215	0.910	21879	3.33	8	**55856**	**1.31**	80215	0.910	21879	3.33
445.gobmk	8	1032	81.3	**1029**	**81.6**	1028	81.7	8	**937**	**89.6**	935	89.7	937	89.6
456.hmmer	8	1153	64.8	**1152**	**64.8**	1152	64.8	8	**1138**	**65.6**	1137	65.6	1138	65.6
458.sjeng	8	1272	76.1	**1272**	**76.1**	1272	76.1	8	1135	85.3	**1135**	**85.3**	1135	85.3
462.libquantum	8	4429	37.4	**4428**	**37.4**	4427	37.4	8	**3928**	**42.2**	3930	42.2	3926	42.2
464.h264ref	8	1382	128	**1382**	**128**	1381	128	8	1303	136	1302	136	**1303**	**136**
471.omnetpp	8	1143	43.8	1140	43.8	**1142**	**43.8**	8	1085	46.1	1087	46.0	**1087**	**46.0**
473.astar	8	**1192**	**47.1**	1195	47.0	1192	47.1	8	**1129**	**49.7**	1129	49.7	1129	49.7
483.xalancbmk	8	769	71.8	**769**	**71.8**	769	71.8	8	783	70.5	782	70.6	**783**	**70.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The Windows start command was used to bind processes to cores.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 -link /FORCE:MULTIPLE`

429.mcf: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec_div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.20090714.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:02:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 October 2007.