SPEC® CINT2006 Result

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Hewlett-Packard Company

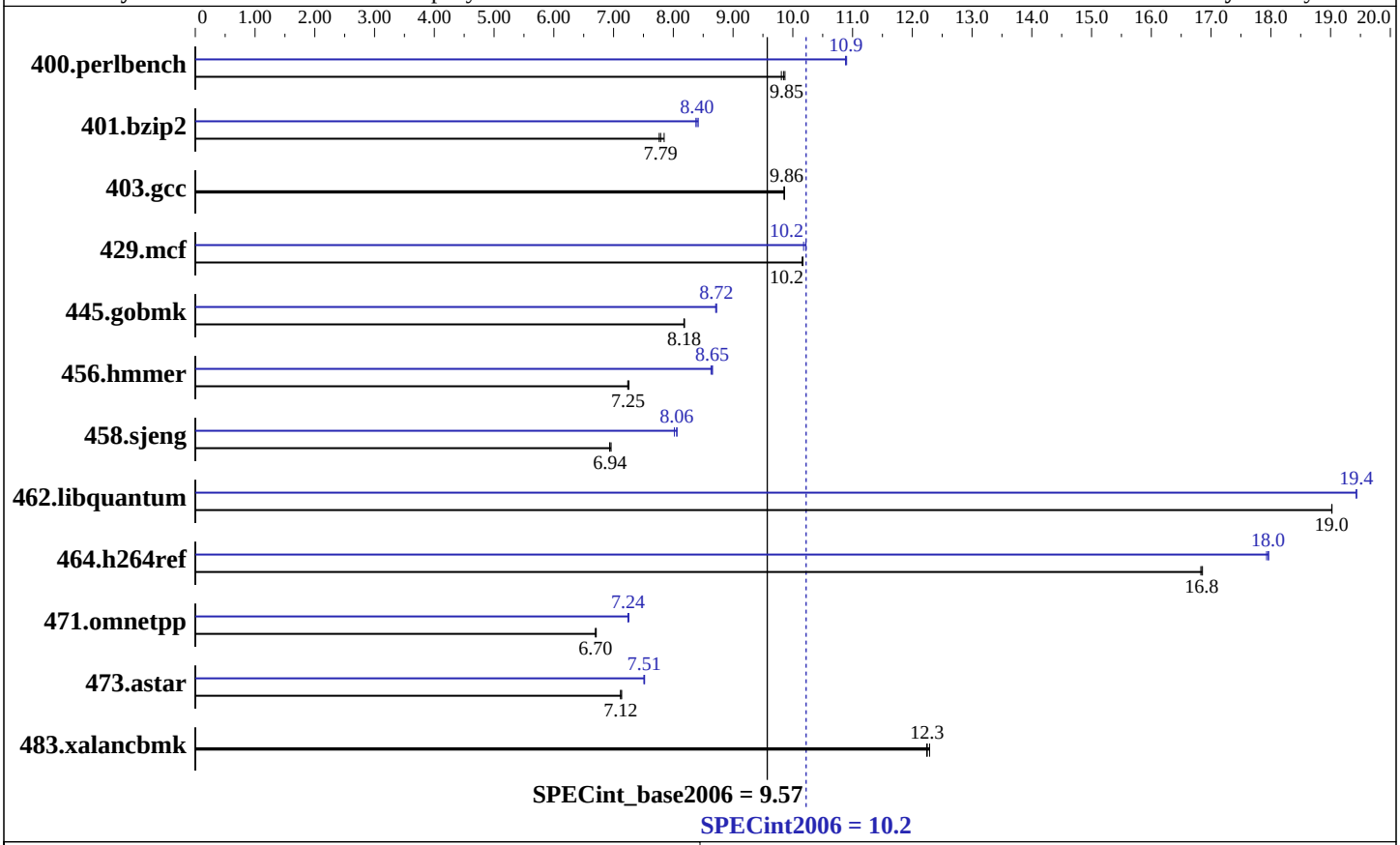
ProLiant DL580 G4
(3.0 GHz, Intel Xeon processor 7120M)

SPECint®2006 = 10.2

SPECint_base2006 = 9.57

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jul-2007
Hardware Availability: Aug-2006
Software Availability: May-2007



Hardware

CPU Name:	Intel Xeon 7120M
CPU Characteristics:	3.0 GHz, 800 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16x2 GB PC2-3200R)
Disk Subsystem:	1x72 GB 15K SAS
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for IA32/EM64T applications, Version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1 libraries

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	990	9.87	996	9.81	**992**	**9.85**	898	10.9	896	10.9	**897**	**10.9**
401.bzip2	1243	7.76	1230	7.85	**1239**	**7.79**	**1149**	**8.40**	1152	8.38	1146	8.42
403.gcc	816	9.86	817	9.85	**817**	**9.86**	816	9.86	817	9.85	**817**	**9.86**
429.mcf	898	10.2	**897**	**10.2**	897	10.2	**893**	**10.2**	892	10.2	896	10.2
445.gobmk	1281	8.19	**1282**	**8.18**	1283	8.18	1203	8.72	1204	8.71	**1203**	**8.72**
456.hmmer	**1288**	**7.25**	1286	7.26	1289	7.24	**1078**	**8.65**	1078	8.65	1081	8.63
458.sjeng	**1743**	**6.94**	1745	6.93	1739	6.96	1501	8.06	1509	8.02	**1502**	**8.06**
462.libquantum	1089	19.0	**1089**	**19.0**	1089	19.0	**1066**	**19.4**	1066	19.4	1066	19.4
464.h264ref	1313	16.9	1315	16.8	**1314**	**16.8**	1232	18.0	1234	17.9	**1232**	**18.0**
471.omnetpp	**933**	**6.70**	931	6.71	933	6.70	**863**	**7.24**	863	7.24	861	7.26
473.astar	**986**	**7.12**	986	7.12	984	7.14	935	7.51	**934**	**7.51**	934	7.52
483.xalancbmk	561	12.3	**563**	**12.3**	564	12.2	561	12.3	**563**	**12.3**	564	12.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
 Power Regulator Disabled
 Adjacent Sector Prefetch Enabled
 Hardware Prefetcher Enabled
 Hyper-Threading Technology Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xP -ipo -O3 -static -no-prec-div

C++ benchmarks:
-xP -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -no-prec-div`

403.gcc: `basepeak = yes`

429.mcf: `-xP -ipo -O3 -static -no-prec-div -prefetch -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O2 -ipo -no-prec_div -ansi-alias`

456.hmmer: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll2 -ansi-alias`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -ipo -O3 -static -no-prec-div -unroll4`

462.libquantum: `Same as 458.sjeng`

464.h264ref: `Same as 456.hmmer`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xP -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/cpu2006.1.0/SmartHeap_8.1/lib -lsmartheap`

473.astar: `Same as 471.omnetpp`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic10-flags.20090714.xml`

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