SPEC® CINT2006 Result

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Acer Incorporated

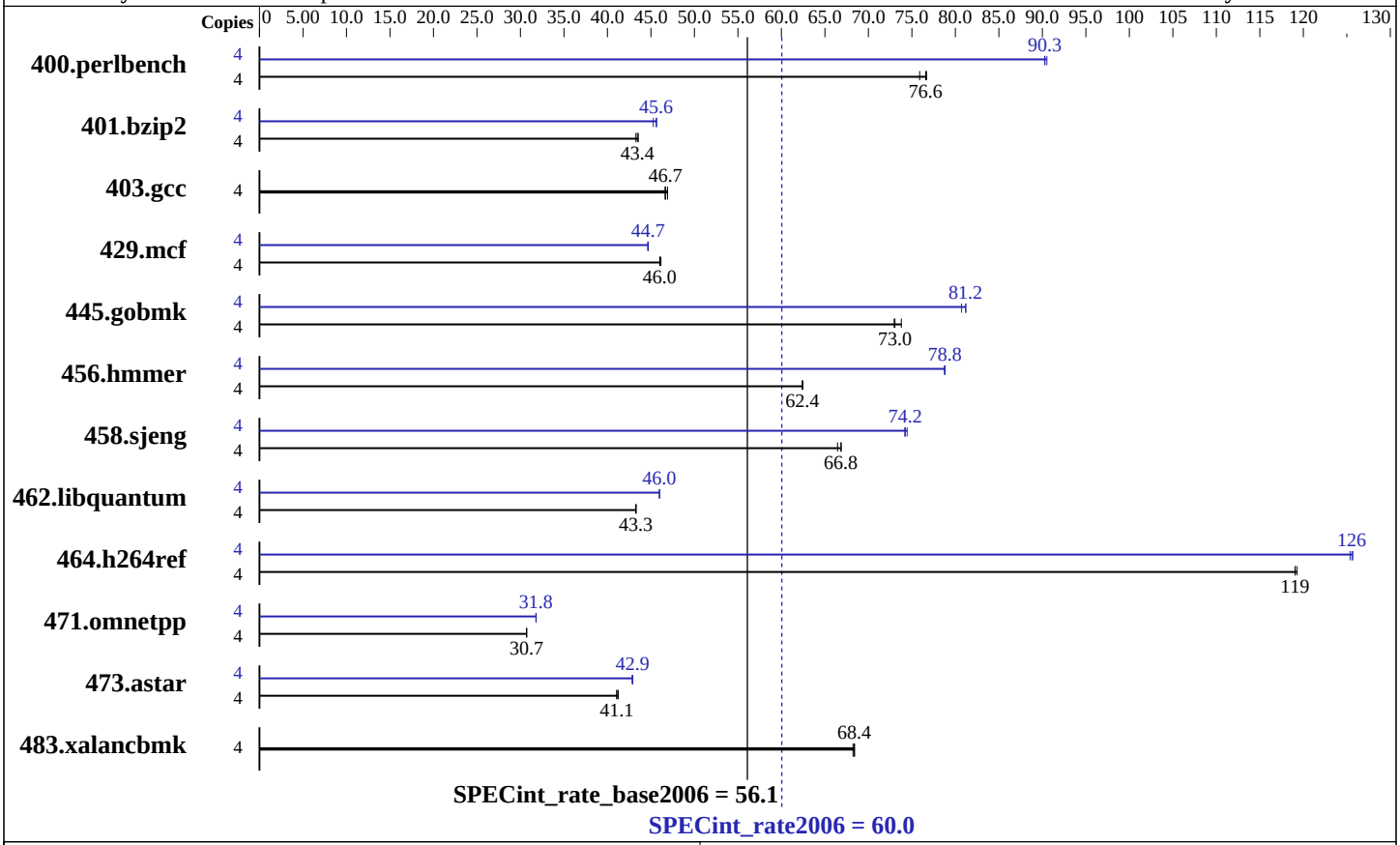
Acer Altos R720 (Intel Xeon 5160)

SPECint®_rate2006 = 60.0

SPECint_rate_base2006 = 56.1

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Aug-2007
Hardware Availability: Nov-2006
Software Availability: Jun-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	1333MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10000RPM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.0 Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	510	76.7	**510**	**76.6**	515	75.9	4	433	90.3	**433**	**90.3**	432	90.5
401.bzip2	4	886	43.5	892	43.3	**889**	**43.4**	4	853	45.3	**847**	**45.6**	845	45.7
403.gcc	4	691	46.6	686	46.9	**690**	**46.7**	4	691	46.6	686	46.9	**690**	**46.7**
429.mcf	4	**793**	**46.0**	790	46.2	793	46.0	4	817	44.6	**816**	**44.7**	816	44.7
445.gobmk	4	575	73.0	569	73.8	**574**	**73.0**	4	520	80.7	**517**	**81.2**	517	81.2
456.hmmer	4	598	62.4	**598**	**62.4**	598	62.4	4	**474**	**78.8**	474	78.7	473	78.8
458.sjeng	4	728	66.5	**724**	**66.8**	724	66.9	4	650	74.5	652	74.2	**652**	**74.2**
462.libquantum	4	1915	43.3	**1915**	**43.3**	1916	43.3	4	1803	46.0	**1802**	**46.0**	1802	46.0
464.h264ref	4	**744**	**119**	744	119	742	119	4	704	126	**705**	**126**	706	125
471.omnetpp	4	**814**	**30.7**	814	30.7	814	30.7	4	**786**	**31.8**	786	31.8	787	31.8
473.astar	4	684	41.1	**683**	**41.1**	681	41.2	4	654	42.9	**655**	**42.9**	656	42.8
483.xalancbmk	4	404	68.2	403	68.4	**404**	**68.4**	4	404	68.2	403	68.4	**404**	**68.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
 This result was measured on the Acer Altos R520
 The Altos R720 and Altos R520 are electronically equivalent.

 BIOS Settings
   Snoop Filter set to OFF
    Snoop Filter preserves cache coherency while
    minimizing snoops to remote nodes.

   Adjacent Cache Line Prefetch set to OFF
    Prefetch data in order to shorten execution cycles
    and maximize data processing efficiency.

   Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec_div -ansi-alias -Wl,-z,muldefs -L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.23.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.23.xml

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