SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

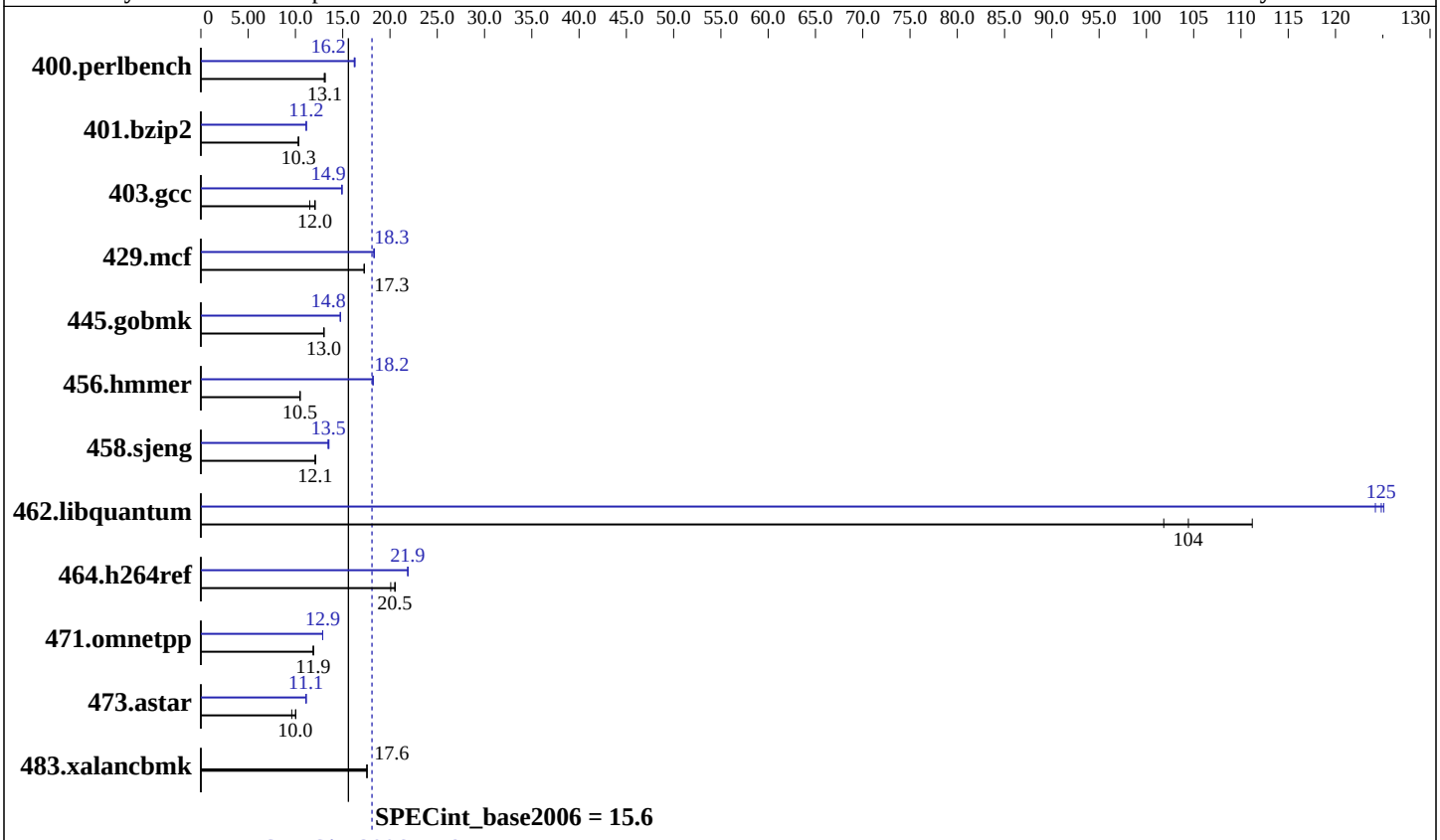
Supermicro X7DB8+ (Intel Xeon processor E5335, 2.00 GHz)

SPECint®2006 = 18.1

SPECint_base2006 = 15.6

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2007
Hardware Availability: Jul-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5335
CPU Characteristics:	Quad Core, 2.00 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	Seagate, SCSI, 73GB, 10Krpm, 1 disk only
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel linux-cbgm 2.6.16.43-0.5-smp for x86_64
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	749	13.0	**745**	**13.1**	743	13.2	599	16.3	**602**	**16.2**	602	16.2
401.bzip2	938	10.3	933	10.3	**935**	**10.3**	867	11.1	865	11.2	**865**	**11.2**
403.gcc	699	11.5	**668**	**12.0**	665	12.1	539	14.9	540	14.9	**539**	**14.9**
429.mcf	528	17.3	**528**	**17.3**	527	17.3	499	18.3	497	18.4	**498**	**18.3**
445.gobmk	806	13.0	**806**	**13.0**	806	13.0	711	14.8	**711**	**14.8**	711	14.8
456.hmmer	890	10.5	**890**	**10.5**	889	10.5	**512**	**18.2**	513	18.2	512	18.2
458.sjeng	1000	12.1	**1000**	**12.1**	998	12.1	**897**	**13.5**	900	13.4	893	13.5
462.libquantum	203	102	186	111	**198**	**104**	166	125	**166**	**125**	167	124
464.h264ref	**1080**	**20.5**	1102	20.1	1076	20.6	1010	21.9	**1011**	**21.9**	1011	21.9
471.omnetpp	525	11.9	**525**	**11.9**	526	11.9	**485**	**12.9**	485	12.9	485	12.9
473.astar	**702**	**10.0**	730	9.61	699	10.0	**631**	**11.1**	632	11.1	630	11.1
483.xalancbmk	**393**	**17.6**	393	17.6	393	17.6	**393**	**17.6**	393	17.6	393	17.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Enabled
Adjacent Sector Prefetch: Enabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -vec-guard-write -parallel -par-runtime-control`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml

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