SPEC® CINT2006 Result

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Acer Incorporated

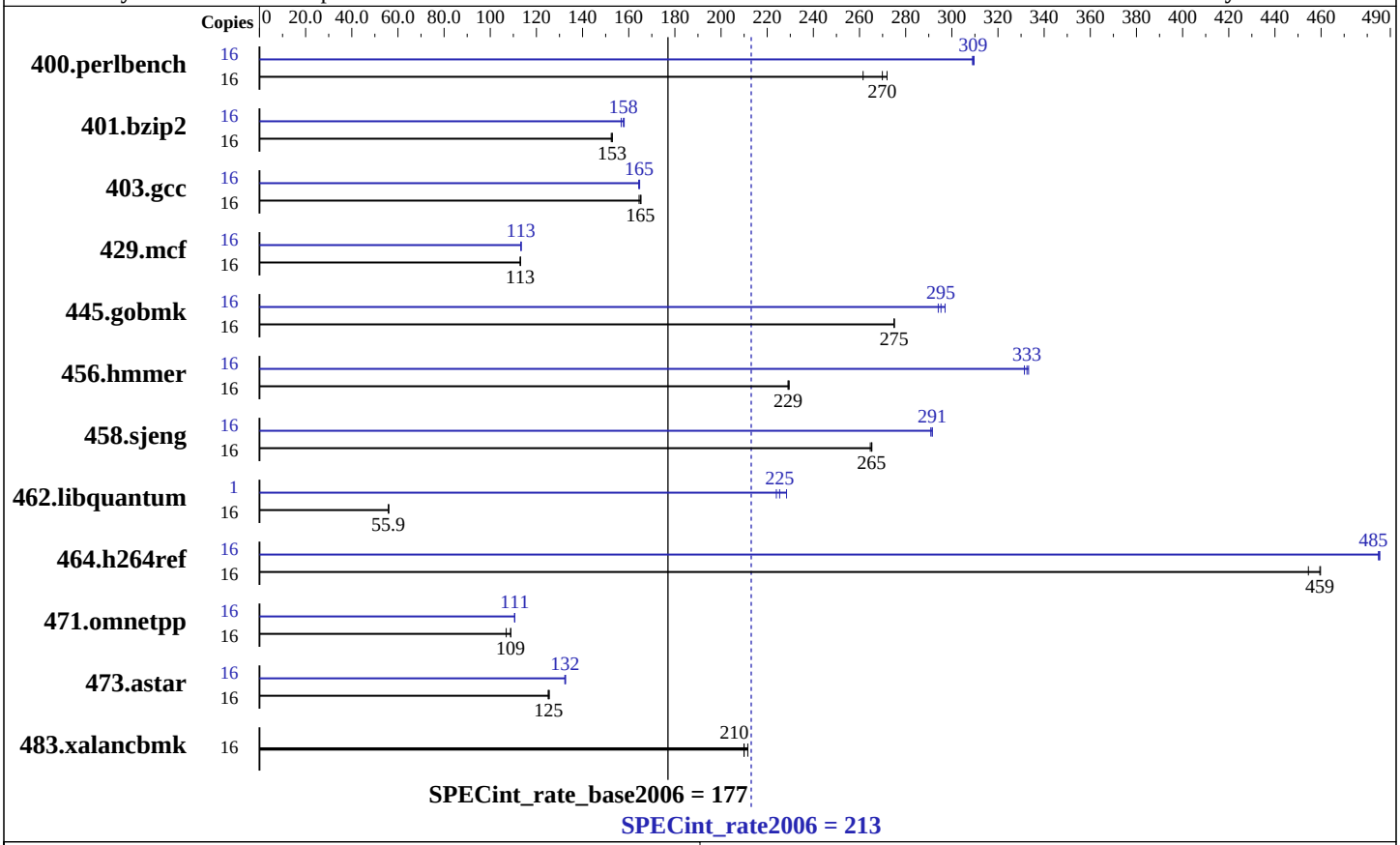
Acer Altos R920 (Intel Xeon X7350)

SPECint®_rate2006 = 213

SPECint_rate_base2006 = 177

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Oct-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X7350
CPU Characteristics:	1066MHz system bus
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1, 2, 4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (16 x 1024MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem:	1 x 73GB 10000PRM SAS HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	598	262	575	272	**579**	**270**	16	506	309	505	310	**505**	**309**
401.bzip2	16	**1011**	**153**	1013	152	1010	153	16	**979**	**158**	977	158	985	157
403.gcc	16	779	165	783	164	**780**	**165**	16	784	164	782	165	**783**	**165**
429.mcf	16	**1291**	**113**	1291	113	1290	113	16	1288	113	**1287**	**113**	1287	113
445.gobmk	16	610	275	610	275	**610**	**275**	16	565	297	**568**	**295**	571	294
456.hmmer	16	652	229	650	230	**652**	**229**	16	448	333	450	331	**449**	**333**
458.sjeng	16	730	265	**730**	**265**	731	265	16	664	292	666	291	**664**	**291**
462.libquantum	16	5921	56.0	5932	55.9	**5931**	**55.9**	1	92.5	224	**91.9**	**225**	90.7	228
464.h264ref	16	770	460	779	455	**771**	**459**	16	**730**	**485**	729	485	730	485
471.omnetpp	16	918	109	935	107	**919**	**109**	16	905	111	**904**	**111**	904	111
473.astar	16	894	126	898	125	**897**	**125**	16	847	133	**848**	**132**	849	132
483.xalancbmk	16	**526**	**210**	526	210	522	212	16	**526**	**210**	526	210	522	212

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
BIOS Settings
  Snoop Filter set to OFF
   Snoop Filter preserves cache coherency while
   minimizing snoops to remote nodes.

  Adjacent Cache Line Prefetch set to OFF
   Prefetch data in order to shorten execution cycles
   and maximize data processing efficiency.

  Optimization for high-frequency FSB applications set to OFF

All results used taskset to bind processes to cores.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:06:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 October 2007.