SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.
SPEC CINT2006 Result

Hewlett-Packard Company
ProLiant ML350 G5
(3.0 GHz, Intel Xeon processor X5365)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Results Table

Benchmark | Copies | Seconds | Ratio | Copies | Seconds | Ratio | Copies | Seconds | Ratio | Copies | Seconds | Ratio | Copies | Seconds | Ratio
---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
400.perlbench | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
401.bzip2 | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
403.gcc | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
429.mcf | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
445.gobmk | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
456.hmmer | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
458.sjeng | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
462.libquantum | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
464.h264ref | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
471.omnetpp | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC | 8 | NC | NC
473.astar | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC
483.xalanc | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC | NC

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Non-Compliant
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Platform Notes

BIOS configuration:
- Power Regulator set to Static High Performance Mode
- Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
- icc

C++ benchmarks:
- icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
- -fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
- -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
- -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

Non-Compliant
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Non-Compliant
### SPEC CINT2006 Result

**Hewlett-Packard Company**

ProLiant ML350 G5  
(3.0 GHz, Intel Xeon processor X5365)

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<thead>
<tr>
<th>CPU2006 license: 3</th>
<th>Test date: Oct-2007</th>
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<tbody>
<tr>
<td>Test sponsor:</td>
<td>Hardware Availability: Sep-2007</td>
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<tr>
<td>Tested by:</td>
<td>Software Availability: Nov-2007</td>
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**SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.**

#### Peak Optimization Flags (Continued)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
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</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch</td>
</tr>
<tr>
<td>403.gcc</td>
<td>-fast -inline-calloc -opt-malloc-options=3</td>
</tr>
<tr>
<td>429.mcf</td>
<td>-fast -prefetch</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>-fast -unroll2 -ansi-alias -opt-multi-version-aggressive</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias</td>
</tr>
<tr>
<td>C++ benchmarks:</td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>basepeak = yes</td>
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<td>SPEC CINT2006 Result</td>
<td></td>
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<tr>
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<td><strong>SPECint_rate2006 = NC</strong></td>
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**Hewlett-Packard Company**

ProLiant ML350 G5
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### Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at


You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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