SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.
SPEC CINT2006 Result

Hewlett-Packard Company

ProLiant ML350 G5
(3.0 GHz, Intel Xeon processor X5365)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 200M

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.
SPEC CINT2006 Result

Hewlett-Packard Company
ProLiant ML350 G5
(3.0 GHz, Intel Xeon processor X5365)

SPECint2006 = NC
SPECint_base2006 = NC

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2007
Hardware Availability: Sep-2007
Software Availability: Nov-2007

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Platform Notes
BIOS configuration:
Power Regulator set to Static High Performance Mode

Base Compiler Invocation
C benchmarks:
iccc
C++ benchmarks:
icpc

Base Portability Flags
400.perlbmk: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
tline-calloc -opt-malloc-options=3
C++ benchmarks:
-T -ipo -O3 -no-prec-div -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags
C benchmarks:

Non-Compliant
Hewlett-Packard Company
ProLiant ML350 G5
(3.0 GHz, Intel Xeon processor X5365)

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Base Other Flags (Continued)
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):

```
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icpc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

Peak Portability Flags
400.perlbench: -DSPEC_CPU_LINUX_IA32
-DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags
C benchmarks:
Continued on next page
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-cALLOC -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll1 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

- prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.html

You can also download the XML flags source by saving the following link: http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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