



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 143

Acer Altos R920 (Intel Xeon E7320)

SPECint_rate_base2006 = 131

CPU2006 license: 97

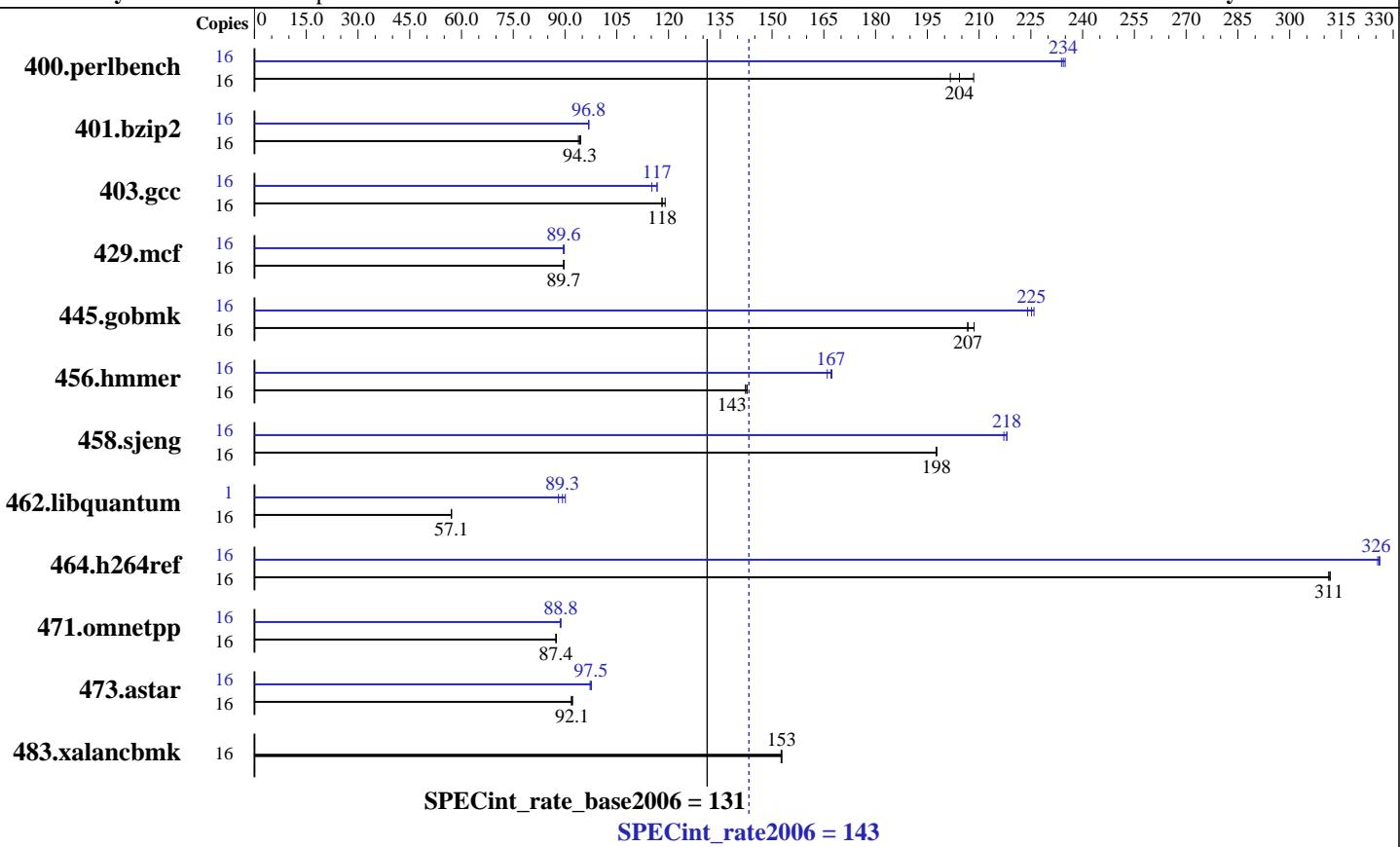
Test date: Oct-2007

Test sponsor: Acer Incorporated

Hardware Availability: Nov-2007

Tested by: Acer Incorporated

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7320
CPU Characteristics: 1066MHz system bus
CPU MHz: 2130
FPU: Integrated
CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
CPU(s) orderable: 1, 2, 4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (16 x 1048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	750	208	765	204	775	202	16	665	235	668	234	667	234
401.bzip2	16	1645	93.9	1638	94.3	1633	94.5	16	1593	96.9	1594	96.8	1595	96.8
403.gcc	16	1090	118	1082	119	1091	118	16	1118	115	1104	117	1104	117
429.mcf	16	1628	89.7	1629	89.6	1627	89.7	16	1630	89.5	1625	89.8	1628	89.6
445.gobmk	16	812	207	812	207	805	209	16	749	224	745	225	743	226
456.hammer	16	1049	142	1046	143	1046	143	16	894	167	892	167	899	166
458.sjeng	16	980	198	980	198	979	198	16	888	218	891	217	888	218
462.libquantum	16	5803	57.1	5810	57.1	5814	57.0	1	232	89.3	235	88.1	230	90.1
464.h264ref	16	1137	311	1136	312	1137	311	16	1088	325	1086	326	1086	326
471.omnetpp	16	1143	87.5	1145	87.4	1145	87.4	16	1127	88.8	1129	88.6	1126	88.8
473.astar	16	1218	92.2	1220	92.1	1223	91.8	16	1150	97.7	1155	97.3	1152	97.5
483.xalancbmk	16	723	153	722	153	723	153	16	723	153	722	153	723	153

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

BIOS Settings

Hardware Prefetcher set to OFF

Adjacent Cache Line Prefetch set to OFF

Optimization for high-frequency FSB applications set to OFF

All results used taskset to bind processes to cores.

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:

```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.22.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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