NEC Corporation
Express5800/120Rg-1
(Intel Xeon processor 5130)

SPECfp®_rate2006 = 36.9
SPECfp_rate_base2006 = 35.8

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Hardware Availability: May-2007
Software Availability: Apr-2007

Continued on next page
SPEC CFP2006 Result

NEC Corporation
Express5800/120Rg-1
(Intel Xeon processor 5130)

SPECfp_rate2006 = 36.9
SPECfp_rate_base2006 = 35.8

NEC Corporation
(intel Xeon processor 5130)

L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x146.5 GB SAS, 15000RPM
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Tested by: NEC Corporation
Software Availability: Apr-2007
Hardware Availability: May-2007
Test date: Oct-2007
CPU2006 license: 9006
Test sponsor: NEC Corporation

Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1333 MHz
All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.

The Express5800/120Rg-1(Intel Xeon Prosessor 5130) and
the Express5800/120Ri-2(Intel Xeon Prosessor 5130) models are electronically equivalent.
The results have been measured on a Express5800/120Ri-2(Intel Xeon Prosessor 5130) model.
**SPEC CFP2006 Result**

**NEC Corporation**

Express5800/120Rg-1  
(Intel Xeon processor 5130)

<table>
<thead>
<tr>
<th>SPECfp_rate2006 = 36.9</th>
</tr>
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<tbody>
<tr>
<td>SPECfp_rate_base2006 = 35.8</td>
</tr>
</tbody>
</table>

---

**Base Compiler Invocation**

C benchmarks:  
```  icc  ```

C++ benchmarks:  
```  icpc  ```

Fortran benchmarks:  
```  ifort  ```

Benchmarks using both Fortran and C:  
```  icc ifort  ```

---

**Base Portability Flags**

```  410.bwaves: -DSPEC_CPU_LP64  ```
```  416.gamess: -DSPEC_CPU_LP64  ```
```  433.milc: -DSPEC_CPU_LP64  ```
```  434.zeusmp: -DSPEC_CPU_LP64  ```
```  435.gromacs: -DSPEC_CPU_LP64 -nofor_main  ```
```  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  ```
```  437.leslie3d: -DSPEC_CPU_LP64  ```
```  444.namd: -DSPEC_CPU_LP64  ```
```  447.dealII: -DSPEC_CPU_LP64  ```
```  450.soplex: -DSPEC_CPU_LP64  ```
```  453.povray: -DSPEC_CPU_LP64 -nofor_main  ```
```  454.calculix: -DSPEC_CPU_LP64 -nofor_main  ```
```  459.GemsFDTD: -DSPEC_CPU_LP64  ```
```  465.tonto: -DSPEC_CPU_LP64  ```
```  470.lbm: -DSPEC_CPU_LP64  ```
```  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  ```
```  482.sphinx3: -DSPEC_CPU_LP64  ```

---

**Base Optimization Flags**

C benchmarks:  
```  -fast  ```

C++ benchmarks:  
```  -fast  ```

Fortran benchmarks:  
```  -fast  ```

Benchmarks using both Fortran and C:  
```  -fast  ```

---

*NEC Corporation*

*Express5800/120Rg-1 (Intel Xeon processor 5130)*

- **CPU2006 license:** 9006
- **Test sponsor:** NEC Corporation
- **Tested by:** NEC Corporation
- **Test date:** Oct-2007
- **Hardware Availability:** May-2007
- **Software Availability:** Apr-2007
SPEC CFP2006 Result

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CPU2006 license: 9006
Test sponsor: NEC Corporation
Test date: Oct-2007
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Peak Compiler Invocation

C benchmarks:
/usr/intel/cc/9.1.049/bin/icc -I/usr/intel/cc/9.1.049/include
-L/usr/intel/cc/9.1.049/lib

C++ benchmarks (except as noted below):
icpc
450.soplex: /usr/intel/cc/9.1.049/bin/icpc
-I/usr/intel/cc/9.1.049/include -L/usr/intel/cc/9.1.049/lib

Fortran benchmarks (except as noted below):
ifort
434.zeusmp: /usr/intel/fc/9.1.045/bin/ifort
-I/usr/intel/fc/9.1.045/include -L/usr/intel/fc/9.1.045/lib

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
470.lbm: Same as 433.milc
482.sphinx3: -fast

C++ benchmarks:

Continued on next page
SPEC CFP2006 Result

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Test date: Oct-2007
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Software Availability: Apr-2007

Peak Optimization Flags (Continued)

444.namd: basepeak = yes
447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast
450.soplex: Same as 447.dealII
453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: -fast
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast
436.cactusADM: basepeak = yes
454.calculix: Same as 435.gromacs
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/NEC-ic91-FP-linux-flags.html
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/NEC-ic91-FP-linux-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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