SPEC® CINT2006 Result

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Intel Corporation

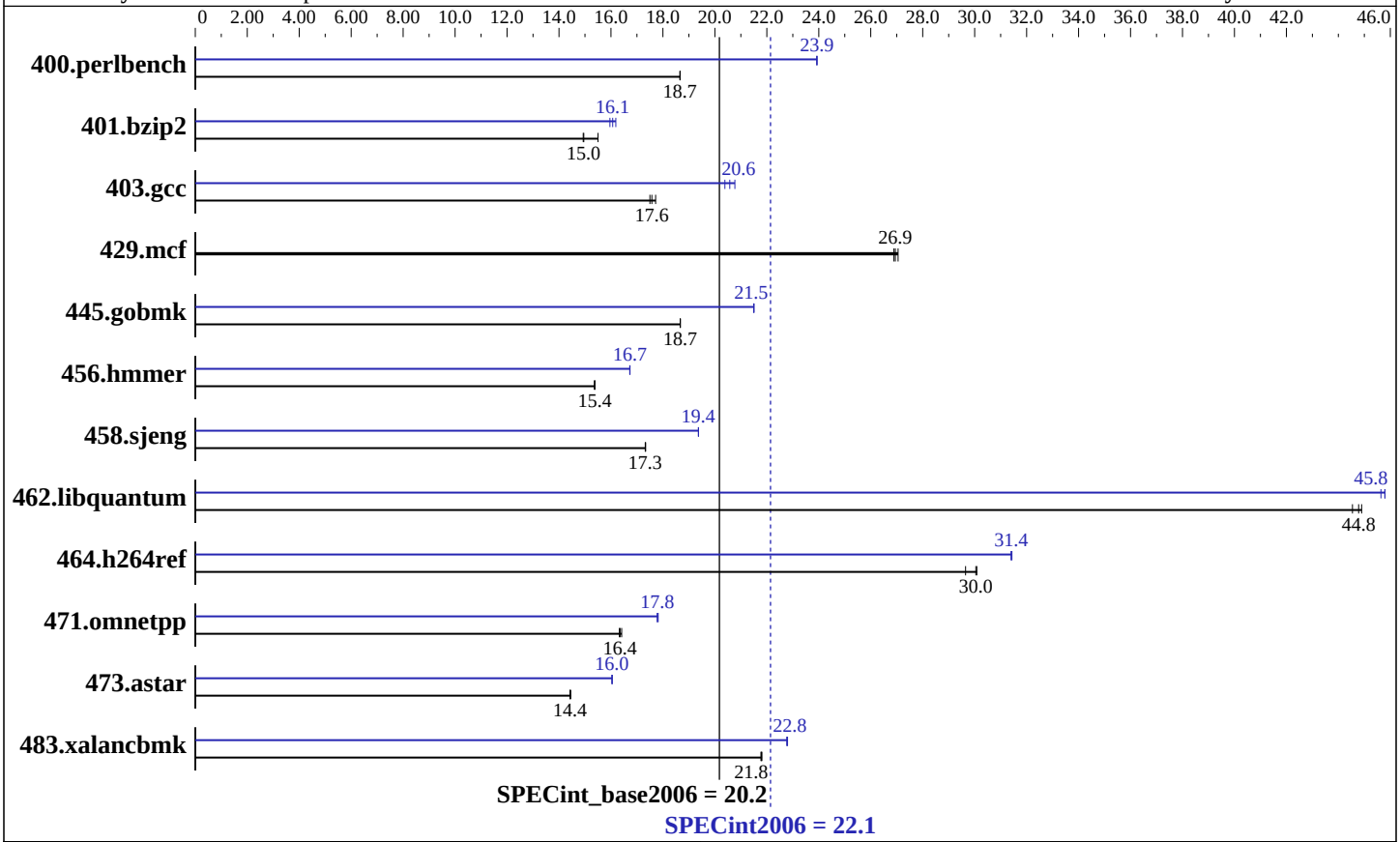
Intel D975XBX2 motherboard (Intel Core 2 Quad QX6800)

SPECint®2006 = 22.1

SPECint_base2006 = 20.2

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Nov-2007
Hardware Availability: Sep-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Quad QX6800
CPU Characteristics:	2.93 GHz 1066 MHz FSB
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1GB Micron MT16HTF12864AY-80ED4 DDR2-800 CL5)
Disk Subsystem:	Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista64 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	524	18.7	524	18.7	**524**	**18.7**	408	23.9	**408**	**23.9**	408	23.9
401.bzip2	**645**	**15.0**	646	14.9	623	15.5	**601**	**16.1**	605	16.0	596	16.2
403.gcc	**458**	**17.6**	460	17.5	454	17.7	388	20.8	**391**	**20.6**	395	20.4
429.mcf	337	27.1	339	26.9	**338**	**26.9**	337	27.1	339	26.9	**338**	**26.9**
445.gobmk	562	18.7	562	18.7	**562**	**18.7**	488	21.5	**488**	**21.5**	488	21.5
456.hmmer	607	15.4	607	15.4	**607**	**15.4**	**558**	**16.7**	558	16.7	558	16.7
458.sjeng	698	17.3	699	17.3	**698**	**17.3**	625	19.4	**625**	**19.4**	625	19.4
462.libquantum	465	44.5	**463**	**44.8**	462	44.9	454	45.6	**453**	**45.8**	452	45.8
464.h264ref	**737**	**30.0**	746	29.6	736	30.1	705	31.4	704	31.4	**705**	**31.4**
471.omnetpp	381	16.4	383	16.3	**382**	**16.4**	351	17.8	**351**	**17.8**	351	17.8
473.astar	486	14.5	**487**	**14.4**	487	14.4	438	16.0	**438**	**16.0**	438	16.0
483.xalancbmk	316	21.8	**317**	**21.8**	317	21.8	303	22.8	**303**	**22.8**	303	22.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 11/2007:
http://www.intel.com/products/motherboard/D975XBX2/index.htm
The system bus runs at 1066 MHz
System was configured with Asus EN8800GTX discrete graphics card
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch -Qparallel -Qpar-runtime-control /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qprefetch /F512000000 libguide40.lib`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 libguide40.lib`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml

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