



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 25.5

Asus P5E3 Deluxe (Intel Core 2 Extreme QX9650)

SPECint_base2006 = 22.3

CPU2006 license: 13

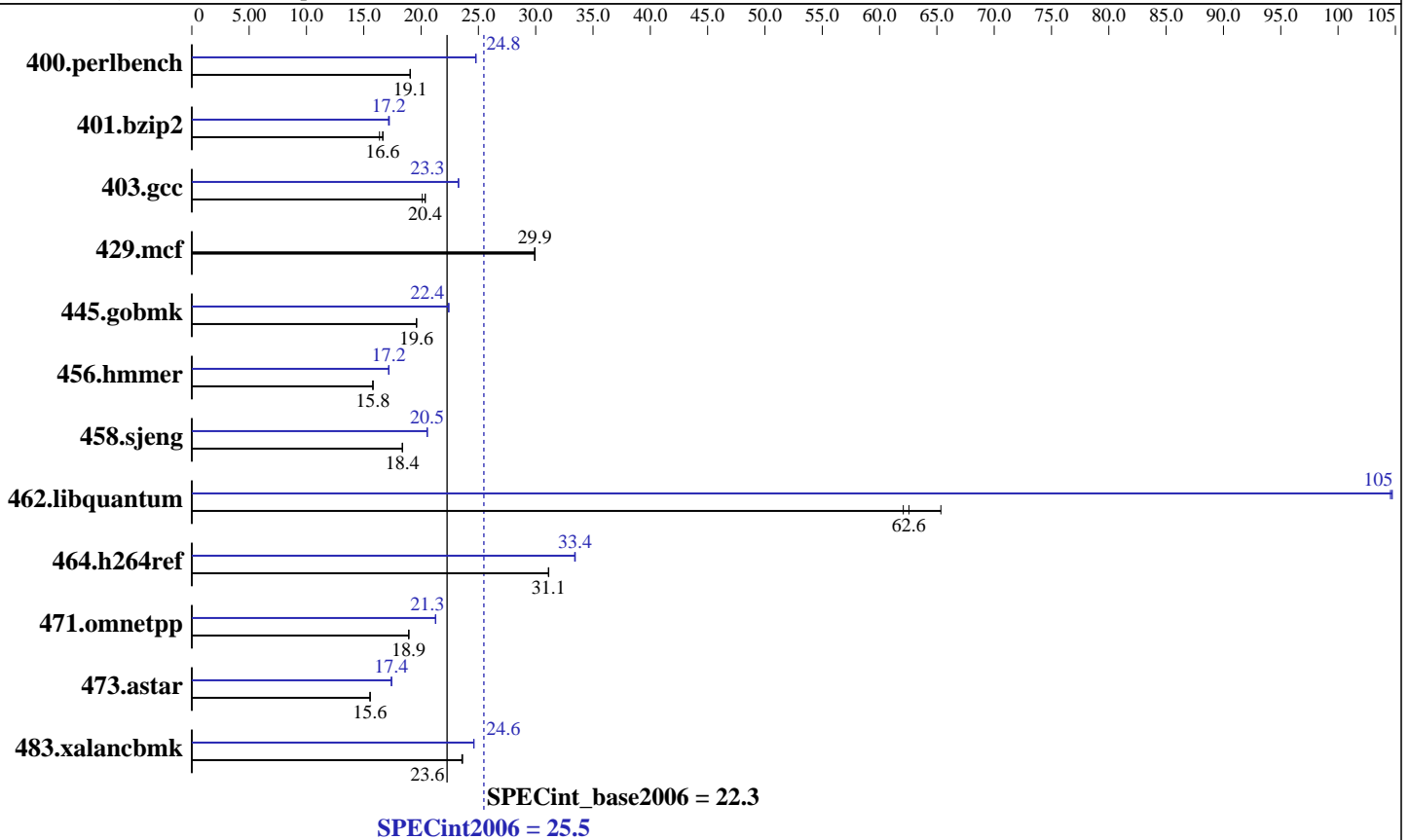
Test date: Nov-2007

Test sponsor: Intel Corporation

Hardware Availability: Nov-2007

Tested by: Intel Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Extreme QX9650
 CPU Characteristics: 3.00 GHz 1333 MHz FSB
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB Corsair TWIN3X2048-1333C9DHX DDR3-1333 CL9)
 Disk Subsystem: Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista64 Ultimate
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	513	19.0	512	19.1	<u>513</u>	<u>19.1</u>	395	24.8	394	24.8	<u>394</u>	<u>24.8</u>
401.bzip2	<u>580</u>	<u>16.6</u>	589	16.4	579	16.7	562	17.2	<u>561</u>	<u>17.2</u>	560	17.2
403.gcc	400	20.1	<u>396</u>	<u>20.4</u>	395	20.4	346	23.2	<u>346</u>	<u>23.3</u>	346	23.3
429.mcf	<u>305</u>	<u>29.9</u>	305	29.9	305	29.9	<u>305</u>	<u>29.9</u>	305	29.9	305	29.9
445.gobmk	535	19.6	535	19.6	<u>535</u>	<u>19.6</u>	468	22.4	<u>468</u>	<u>22.4</u>	468	22.4
456.hammer	591	15.8	590	15.8	<u>591</u>	<u>15.8</u>	<u>543</u>	<u>17.2</u>	543	17.2	543	17.2
458.sjeng	<u>659</u>	<u>18.4</u>	659	18.4	659	18.4	589	20.5	<u>589</u>	<u>20.5</u>	589	20.5
462.libquantum	<u>331</u>	<u>62.6</u>	334	62.1	317	65.3	<u>198</u>	<u>105</u>	198	105	198	105
464.h264ref	711	31.1	711	31.1	<u>711</u>	<u>31.1</u>	663	33.4	<u>662</u>	<u>33.4</u>	662	33.4
471.omnetpp	<u>330</u>	<u>18.9</u>	330	18.9	330	18.9	294	21.3	294	21.3	<u>294</u>	<u>21.3</u>
473.astar	451	15.6	451	15.6	<u>451</u>	<u>15.6</u>	<u>403</u>	<u>17.4</u>	403	17.4	403	17.4
483.xalancbmk	292	23.6	<u>292</u>	<u>23.6</u>	293	23.6	281	24.6	280	24.6	<u>281</u>	<u>24.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
Product description located as of 11/2007:

<http://www.asus.com/products.aspx?l1=3&l2=11&l3=572&l4=0&model=1872&modelmenu=1>

The system bus runs at 1333 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```



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Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll14
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll14 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll12
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>

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For other inquiries, please contact webmaster@spec.org.

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