SPEC® CINT2006 Result

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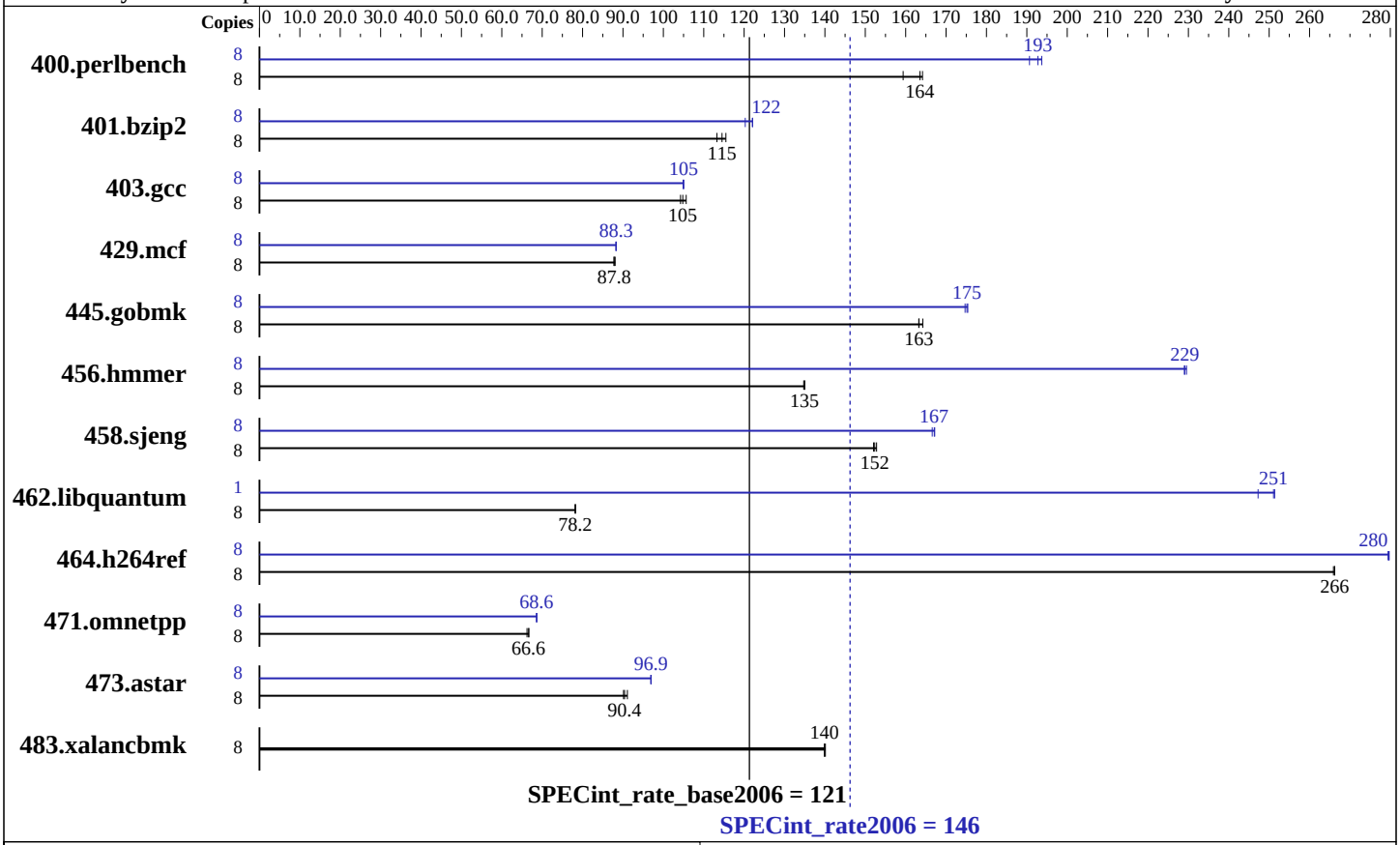
Supermicro
Motherboard X7DWT-INF

SPECint®_rate2006 = 146

SPECint_rate_base2006 = 121

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5482
CPU Characteristics:	Quad Core, 3.20GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	32 GB (8 X 4GB ECC PC2-6400, CL5, FBDIMM)
Disk Subsystem:	Western DigitalD WD1600YS-01SHB1 160GB SATA II, 7200RPM
Other Hardware:	None

Software

Operating System:	64-Bit Suse Linux Enterprise Server 10 w/ SP1
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	490	159	**478**	**164**	476	164	8	**406**	**193**	410	191	404	194
401.bzip2	8	682	113	**674**	**115**	668	115	8	632	122	642	120	**633**	**122**
403.gcc	8	618	104	610	106	**614**	**105**	8	614	105	**613**	**105**	613	105
429.mcf	8	831	87.8	**831**	**87.8**	828	88.1	8	826	88.3	**826**	**88.3**	826	88.3
445.gobmk	8	514	163	511	164	**514**	**163**	8	**479**	**175**	480	175	478	175
456.hmmer	8	553	135	554	135	**553**	**135**	8	326	229	**326**	**229**	325	230
458.sjeng	8	634	153	637	152	**635**	**152**	8	579	167	**579**	**167**	581	167
462.libquantum	8	2120	78.2	**2120**	**78.2**	2119	78.2	1	83.8	247	82.4	251	**82.5**	**251**
464.h264ref	8	**665**	**266**	665	266	666	266	8	633	280	634	279	**633**	**280**
471.omnetpp	8	750	66.7	755	66.2	**750**	**66.6**	8	728	68.6	729	68.6	**728**	**68.6**
473.astar	8	**621**	**90.4**	617	91.1	623	90.1	8	579	97.0	**579**	**96.9**	579	96.9
483.xalancbmk	8	**394**	**140**	395	140	394	140	8	**394**	**140**	395	140	394	140

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with SC-808T-980 case,
To ensure system stability, a 550W (minimum) ATX power supply
[4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon1333/5400/X7DWT-INF.cfm
The system bus runs at 1600 MHz
BIOS Setting: Default
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

456.hmmer:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml`

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For other inquiries, please contact webmaster@spec.org.

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