SPEC® CINT2006 Result

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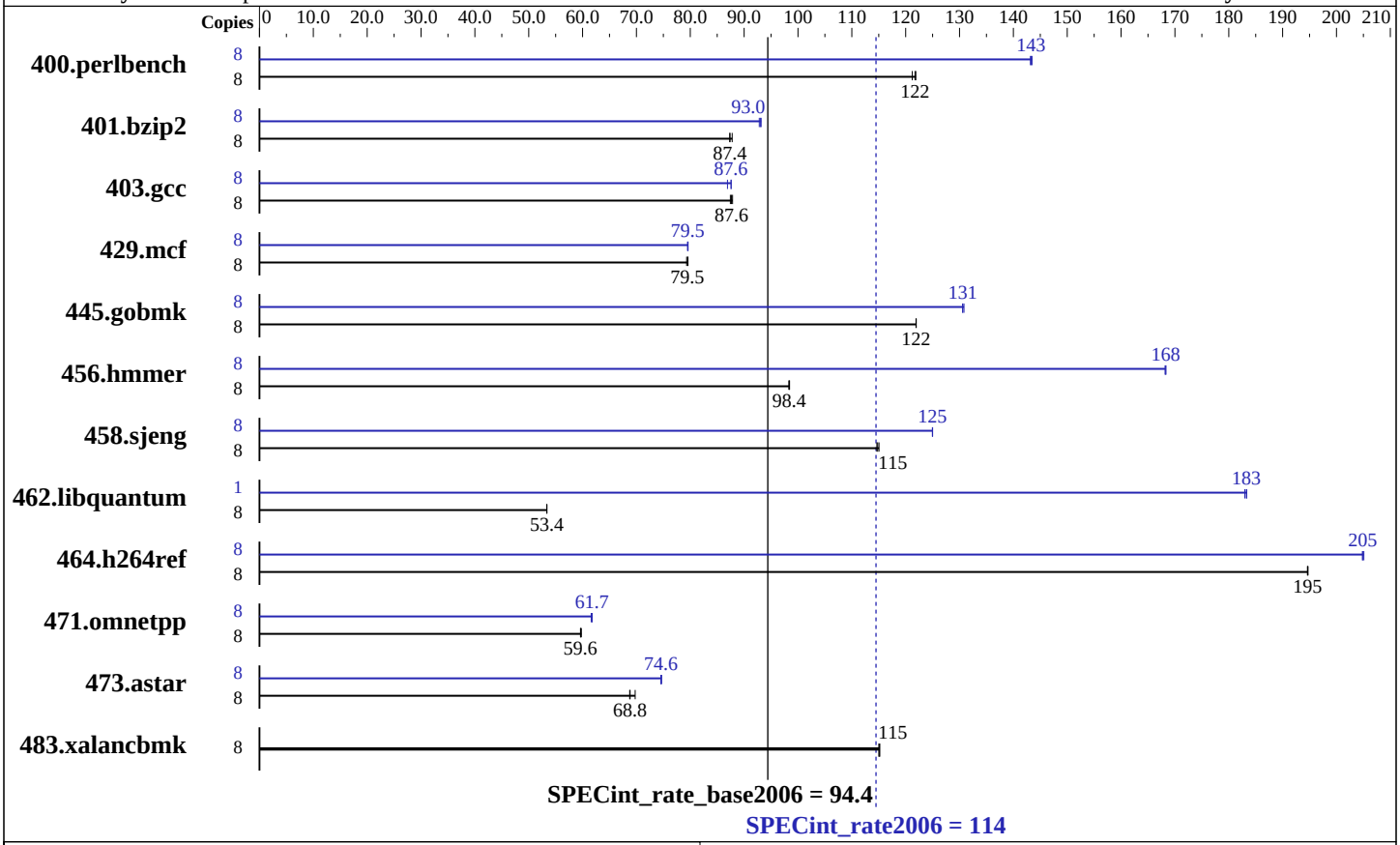
Supermicro Motherboard X7DB3

SPECint®_rate2006 = 114

SPECint_rate_base2006 = 94.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	Quad Core, 2.33GHz
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	WD WD5000YS Caviar RE2, 500GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	64-Bit Suse Linux Enterprise Server 10 w/ SP1
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	645	121	**642**	**122**	641	122	8	546	143	**545**	**143**	545	143
401.bzip2	8	879	87.8	**884**	**87.4**	884	87.3	8	829	93.2	832	92.8	**830**	**93.0**
403.gcc	8	**735**	**87.6**	736	87.4	733	87.9	8	735	87.6	741	86.9	**735**	**87.6**
429.mcf	8	917	79.6	920	79.3	**918**	**79.5**	8	918	79.5	917	79.6	**917**	**79.5**
445.gobmk	8	688	122	**688**	**122**	688	122	8	643	131	**643**	**131**	641	131
456.hmmer	8	**759**	**98.4**	759	98.3	758	98.4	8	444	168	443	168	**444**	**168**
458.sjeng	8	844	115	**843**	**115**	841	115	8	775	125	774	125	**775**	**125**
462.libquantum	8	3106	53.4	**3106**	**53.4**	3107	53.4	1	**113**	**183**	113	183	113	183
464.h264ref	8	910	195	**910**	**195**	909	195	8	**864**	**205**	864	205	863	205
471.omnetpp	8	**839**	**59.6**	836	59.8	839	59.6	8	**810**	**61.7**	809	61.8	812	61.6
473.astar	8	**816**	**68.8**	817	68.7	805	69.8	8	752	74.7	753	74.5	**753**	**74.6**
483.xalancbmk	8	480	115	**480**	**115**	479	115	8	480	115	**480**	**115**	479	115

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with CSE-825TQ-R700LPV case,
To ensure system stability, a 500W (minimum)
ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DB3.cfm
The system bus runs at 1333 MHz
BIOS Setting: Default
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc`
`-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib`
`-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 15:09:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 December 2007.