Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5440, 2.83 GHz

<table>
<thead>
<tr>
<th>SPECint_rate2006</th>
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<tbody>
<tr>
<td>SPECint_rate_base2006</td>
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**CPU2006 license:** 22  
**Test sponsor:**  Fujitsu Siemens Computers  
**Tested by:**  Fujitsu Siemens Computers  
**Test date:**  Nov-2007  
**Hardware Availability:**  Dec-2007  
**Software Availability:**  Nov-2007

<table>
<thead>
<tr>
<th>Test</th>
<th>CPU</th>
<th>Memory</th>
<th>Disk</th>
<th>Software</th>
<th>Operating System</th>
<th>Compiler</th>
<th>Auto Parallel</th>
<th>File System</th>
<th>System State</th>
<th>Base Pointers</th>
<th>Peak Pointers</th>
<th>Other Software</th>
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<tbody>
<tr>
<td>400/perlbench</td>
<td>8</td>
<td>16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)</td>
<td>Seagate ST973451SS (SAS, 73GB, 15000rpm)</td>
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<td>SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp</td>
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**Hardware**

- **CPU Name:** Intel Xeon E5440
- **CPU Characteristics:** 1333 MHz system bus
- **CPU MHz:** 2833
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip
- **CPU(s) orderable:** 1.2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 12 MB I+D on chip per chip, 6 MB shared / 2 cores
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
- **Disk Subsystem:** Seagate ST973451SS (SAS, 73GB, 15000rpm)
SPEC CINT2006 Result

Fujitsu Siemens Computers

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CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

SPECint_rate2006 = 105
SPECint_rate_base2006 = 105

Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
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### Base Optimization Flags

- **C benchmarks:**  
  - fast -inline-calloc -opt-malloc-options=3

- **C++ benchmarks:**  
  - -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
  - -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

### Base Other Flags

- **C benchmarks:**  
  - 403.gcc: -Dalloca=_alloca

### Peak Optimization Flags

- **C benchmarks:**  
  - 400.perlbench: basepeak = yes  
  - 401.bzip2: basepeak = yes  
  - 403.gcc: basepeak = yes  
  - 429.mcf: basepeak = yes  
  - 445.gobmk: basepeak = yes  
  - 456.hmmer: basepeak = yes  
  - 458.sjeng: basepeak = yes  
  - 462.libquantum: basepeak = yes  
  - 464.h264ref: basepeak = yes

- **C++ benchmarks:**  
  - 471.omnetpp: basepeak = yes  
  - 473.astar: basepeak = yes  
  - 483.xalancbmk: basepeak = yes
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The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.