SPEC® CINT2006 Result

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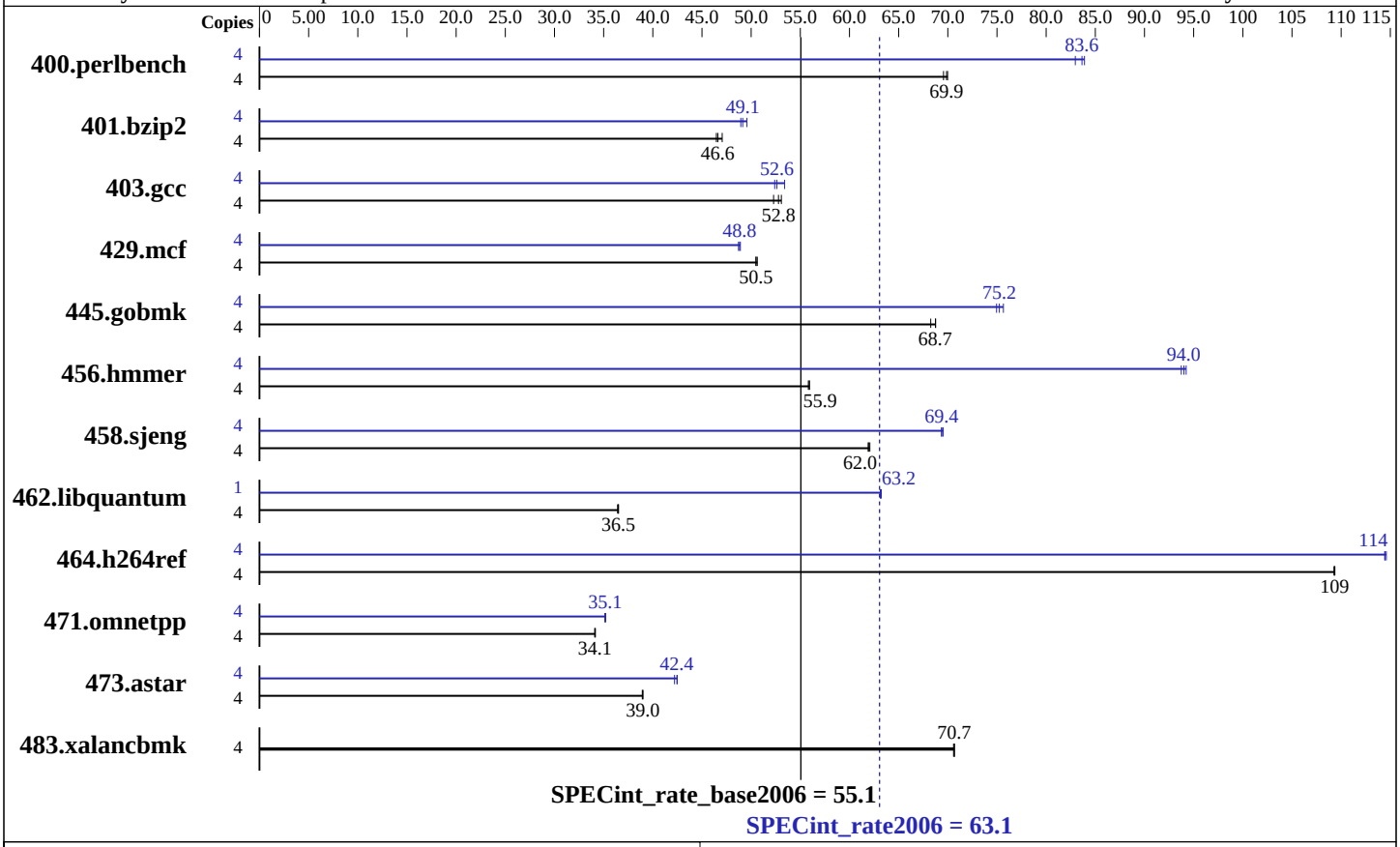
Acer Incorporated
Acer Altos G330 Mk2

SPECint®_rate2006 = 63.1

SPECint_rate_base2006 = 55.1

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Nov-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X3230
CPU Characteristics:	1066 MHz system bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
Disk Subsystem:	1 x 160 GB 7200RPM SATA HDD
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**559**	**69.9**	558	70.0	562	69.6	4	**467**	**83.6**	471	83.0	466	83.9
401.bzip2	4	831	46.5	**828**	**46.6**	820	47.1	4	789	48.9	779	49.6	**785**	**49.1**
403.gcc	4	616	52.3	**610**	**52.8**	607	53.1	4	615	52.4	**612**	**52.6**	603	53.4
429.mcf	4	723	50.5	**722**	**50.5**	721	50.6	4	746	48.9	**747**	**48.8**	749	48.7
445.gobmk	4	615	68.3	610	68.8	**610**	**68.7**	4	555	75.7	**558**	**75.2**	560	75.0
456.hmmer	4	667	55.9	669	55.8	**668**	**55.9**	4	**397**	**94.0**	396	94.2	398	93.7
458.sjeng	4	**781**	**62.0**	782	61.9	780	62.1	4	696	69.5	**697**	**69.4**	698	69.4
462.libquantum	4	2270	36.5	2276	36.4	**2271**	**36.5**	1	**328**	**63.2**	328	63.1	328	63.2
464.h264ref	4	810	109	**810**	**109**	809	109	4	774	114	**773**	**114**	772	115
471.omnetpp	4	**733**	**34.1**	733	34.1	732	34.2	4	712	35.1	710	35.2	**711**	**35.1**
473.astar	4	721	38.9	720	39.0	**720**	**39.0**	4	661	42.5	**662**	**42.4**	665	42.2
483.xalancbmk	4	**391**	**70.7**	391	70.6	391	70.7	4	**391**	**70.7**	391	70.6	391	70.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode

taskset was used to bind copies to cores, except for 462.libquantum peak
OMP_NUM_THREADS was set to number of logical processors as seen by the OS
KMP_AFFINITY was set to physical,0
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

456.hmmer:
```
/opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.38.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.38.xml`

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