



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®2006 = 19.8

CELSIUS R550, Intel Xeon E5410 processor

SPECfp_base2006 = 16.7

CPU2006 license: 22

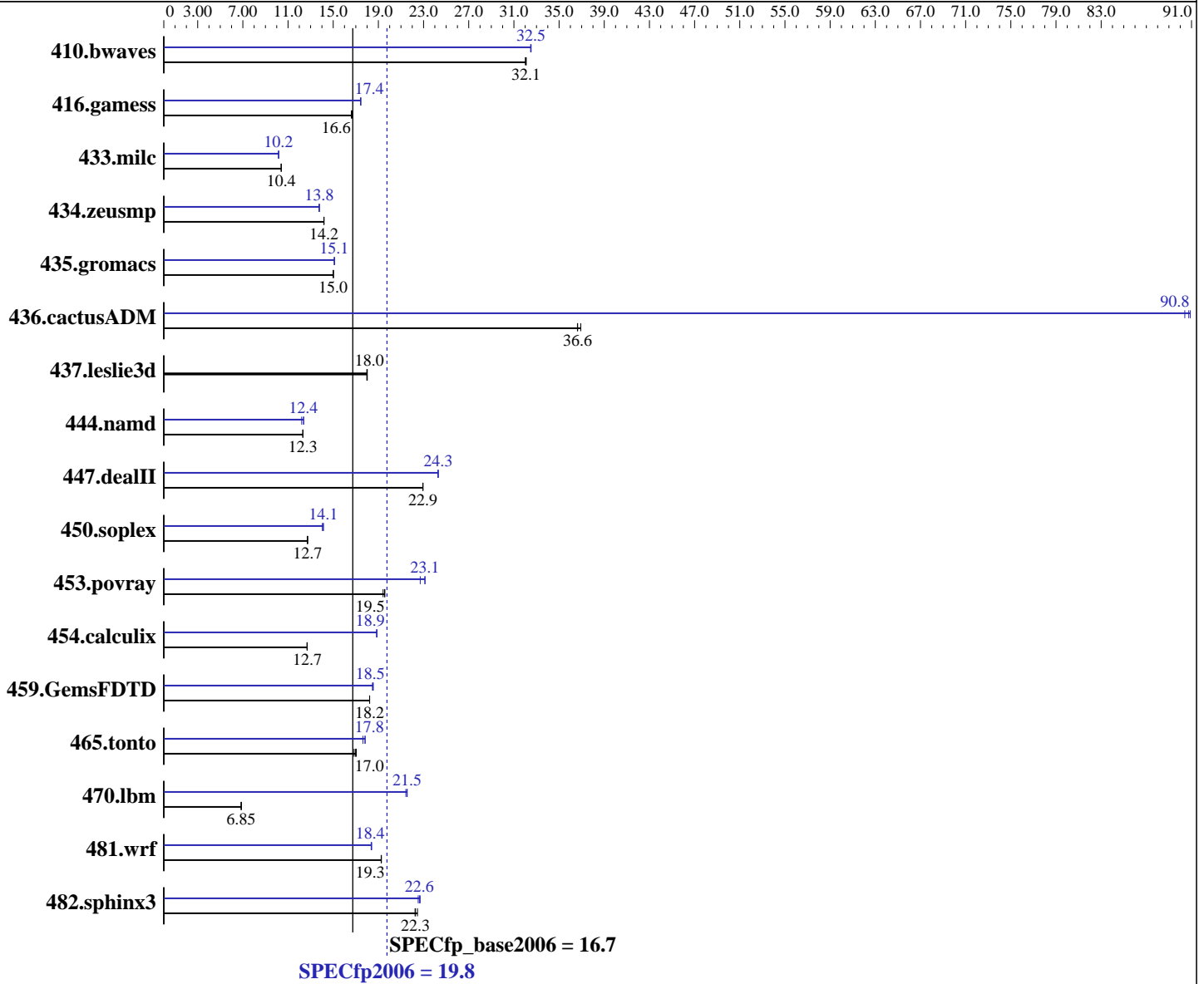
Test date: Nov-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Nov-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics:
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64, version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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Hardware (Continued)

Software (Continued)

L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x SATA II, 400 GB, 7200 rpm
Other Hardware: None

Other Software: binutils-2.17.50.0.5-0.1.x86_64

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	424	32.1	<u>424</u>	<u>32.1</u>	425	32.0	418	32.5	<u>418</u>	<u>32.5</u>	418	32.5
416.gamess	1174	16.7	1179	16.6	<u>1177</u>	<u>16.6</u>	1123	17.4	<u>1123</u>	<u>17.4</u>	1124	17.4
433.milc	882	10.4	883	10.4	<u>883</u>	<u>10.4</u>	904	10.2	<u>903</u>	<u>10.2</u>	902	10.2
434.zeusmp	642	14.2	642	14.2	<u>642</u>	<u>14.2</u>	<u>661</u>	<u>13.8</u>	660	13.8	662	13.8
435.gromacs	475	15.0	476	15.0	<u>476</u>	<u>15.0</u>	<u>473</u>	<u>15.1</u>	473	15.1	472	15.1
436.cactusADM	326	36.6	<u>326</u>	<u>36.6</u>	324	36.9	131	90.9	132	90.4	<u>132</u>	<u>90.8</u>
437.leslie3d	522	18.0	<u>522</u>	<u>18.0</u>	523	18.0	522	18.0	<u>522</u>	<u>18.0</u>	523	18.0
444.namd	651	12.3	<u>651</u>	<u>12.3</u>	651	12.3	648	12.4	657	12.2	<u>648</u>	<u>12.4</u>
447.dealII	<u>499</u>	<u>22.9</u>	499	22.9	498	22.9	471	24.3	<u>471</u>	<u>24.3</u>	471	24.3
450.soplex	654	12.7	657	12.7	<u>655</u>	<u>12.7</u>	590	14.1	594	14.0	<u>590</u>	<u>14.1</u>
453.povray	<u>272</u>	<u>19.5</u>	272	19.6	274	19.4	230	23.1	<u>230</u>	<u>23.1</u>	234	22.7
454.calculix	<u>650</u>	<u>12.7</u>	649	12.7	651	12.7	437	18.9	438	18.8	<u>437</u>	<u>18.9</u>
459.GemsFDTD	582	18.2	<u>582</u>	<u>18.2</u>	582	18.2	574	18.5	572	18.6	<u>573</u>	<u>18.5</u>
465.tonto	577	17.0	584	16.9	<u>579</u>	<u>17.0</u>	552	17.8	<u>552</u>	<u>17.8</u>	558	17.6
470.lbm	1998	6.88	2007	6.85	<u>2007</u>	<u>6.85</u>	638	21.6	<u>638</u>	<u>21.5</u>	641	21.4
481.wrf	<u>580</u>	<u>19.3</u>	580	19.2	580	19.3	<u>608</u>	<u>18.4</u>	607	18.4	608	18.4
482.sphinx3	868	22.5	876	22.2	<u>875</u>	<u>22.3</u>	859	22.7	<u>861</u>	<u>22.6</u>	865	22.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
SnooFilter = Disable



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General Notes

All binaries were built with 64-bit Intel compiler except: 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see: <http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```



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Base Optimization Flags

C benchmarks:

-fast -parallel

C++ benchmarks:

-fast -parallel

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel

Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main

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Peak Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.31.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.31.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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