SPEC® CINT2006 Result

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Fujitsu Siemens Computers

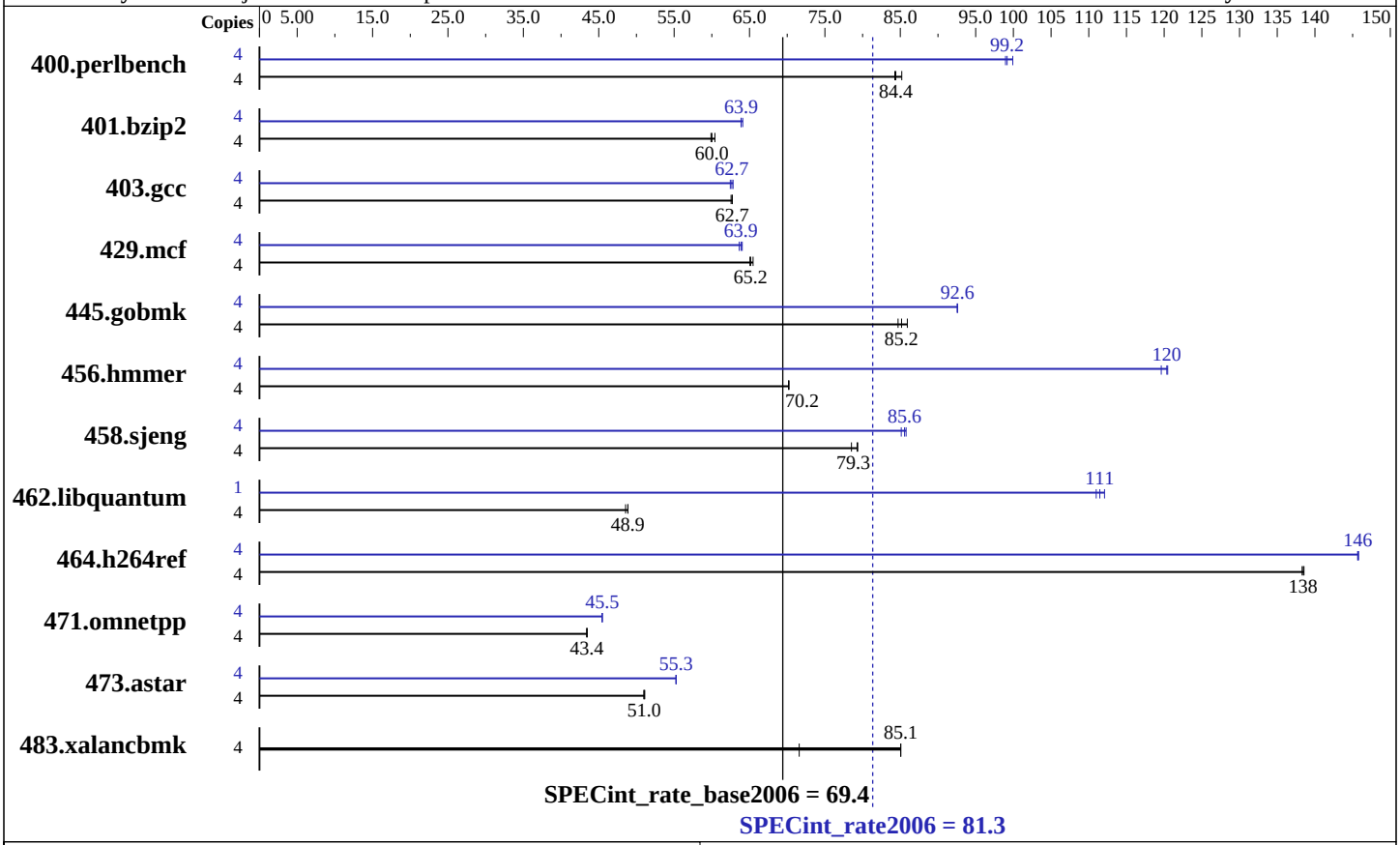
PRIMERGY BX620 S4, Intel Xeon processor X5260, 3.33 GHz

SPECint®_rate2006 = 81.3

SPECint_rate_base2006 = 69.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Dec-2007
Hardware Availability: Dec-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	Yes
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	464	84.3	459	85.2	**463**	**84.4**	4	391	99.9	**394**	**99.2**	395	99.0
401.bzip2	4	639	60.4	**643**	**60.0**	644	59.9	4	604	63.9	**604**	**63.9**	602	64.1
403.gcc	4	**514**	**62.7**	515	62.6	513	62.7	4	513	62.8	516	62.5	**514**	**62.7**
429.mcf	4	**560**	**65.2**	561	65.1	557	65.5	4	573	63.6	**571**	**63.9**	570	64.0
445.gobmk	4	488	85.9	495	84.7	**493**	**85.2**	4	**453**	**92.6**	453	92.6	453	92.6
456.hmmer	4	531	70.2	532	70.2	**532**	**70.2**	4	310	120	312	120	**310**	**120**
458.sjeng	4	610	79.4	**611**	**79.3**	616	78.5	4	564	85.8	569	85.1	**566**	**85.6**
462.libquantum	4	1707	48.6	1696	48.9	**1696**	**48.9**	1	**186**	**111**	185	112	187	111
464.h264ref	4	639	139	**639**	**138**	640	138	4	**608**	**146**	608	146	607	146
471.omnetpp	4	**576**	**43.4**	576	43.4	575	43.5	4	550	45.5	**550**	**45.5**	550	45.5
473.astar	4	551	51.0	549	51.1	**550**	**51.0**	4	508	55.3	**508**	**55.3**	508	55.3
483.xalancbmk	4	324	85.1	386	71.6	**324**	**85.1**	4	324	85.1	386	71.6	**324**	**85.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.xml`

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For other inquiries, please contact webmaster@spec.org.

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