



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp®_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5430)

SPECfp_rate_base2006 = 65.2

CPU2006 license: 11

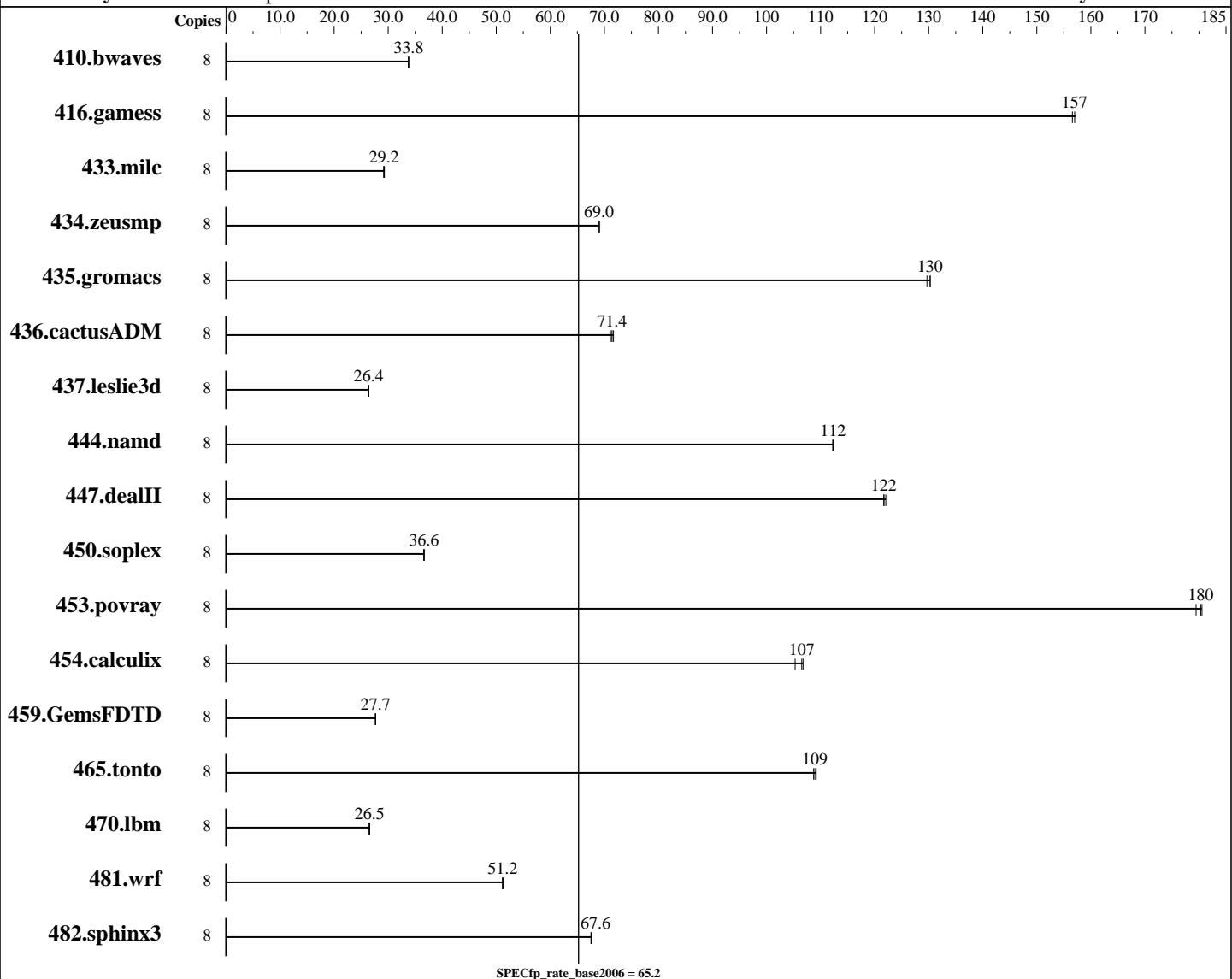
Test date: Nov-2007

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5430)

SPECfp_rate_base2006 = 65.2

CPU2006 license: 11

Test date: Nov-2007

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 36 GB SAS, 10000 RPM
 Other Hardware: None

Peak Pointers: Not Applicable
 Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3219	33.8	3220	33.8	3219	33.8							
416.gamess	8	1000	157	998	157	996	157							
433.milc	8	2511	29.2	2518	29.2	2506	29.3							
434.zeusmp	8	1055	69.0	1054	69.1	1057	68.9							
435.gromacs	8	438	130	440	130	438	130							
436.cactusADM	8	1341	71.3	1334	71.7	1338	71.4							
437.leslie3d	8	2852	26.4	2848	26.4	2850	26.4							
444.namd	8	571	112	571	112	572	112							
447.dealII	8	752	122	752	122	750	122							
450.soplex	8	1823	36.6	1819	36.7	1822	36.6							
453.povray	8	236	181	237	179	236	180							
454.calculix	8	620	107	627	105	618	107							
459.GemsFDTD	8	3065	27.7	3066	27.7	3075	27.6							
465.tonto	8	723	109	724	109	721	109							
470.lbm	8	4141	26.5	4152	26.5	4138	26.6							
481.wrf	8	1746	51.2	1744	51.2	1747	51.2							
482.sphinx3	8	2310	67.5	2306	67.6	2307	67.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode
 Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0
 KMP_STACKSIZE set to 64M
 taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
 icc

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5430)

SPECfp_rate_base2006 = 65.2

CPU2006 license: 11

Test date: Nov-2007

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

Base Compiler Invocation (Continued)

C++ benchmarks:

`icpc`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`icc ifort`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.games: `-DSPEC_CPU_LP64`
 433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
 444.namd: `-DSPEC_CPU_LP64`
 447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
 465.tonto: `-DSPEC_CPU_LP64`
 470.lbm: `-DSPEC_CPU_LP64`
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:

`-fast`

C++ benchmarks:

`-fast`

Fortran benchmarks:

`-fast`

Benchmarks using both Fortran and C:

`-fast`



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5430)

SPECfp_rate_base2006 = 65.2

CPU2006 license: 11

Test date: Nov-2007

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 15:00:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 26 December 2007.