SPEC® CINT2006 Result

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Dell Inc.

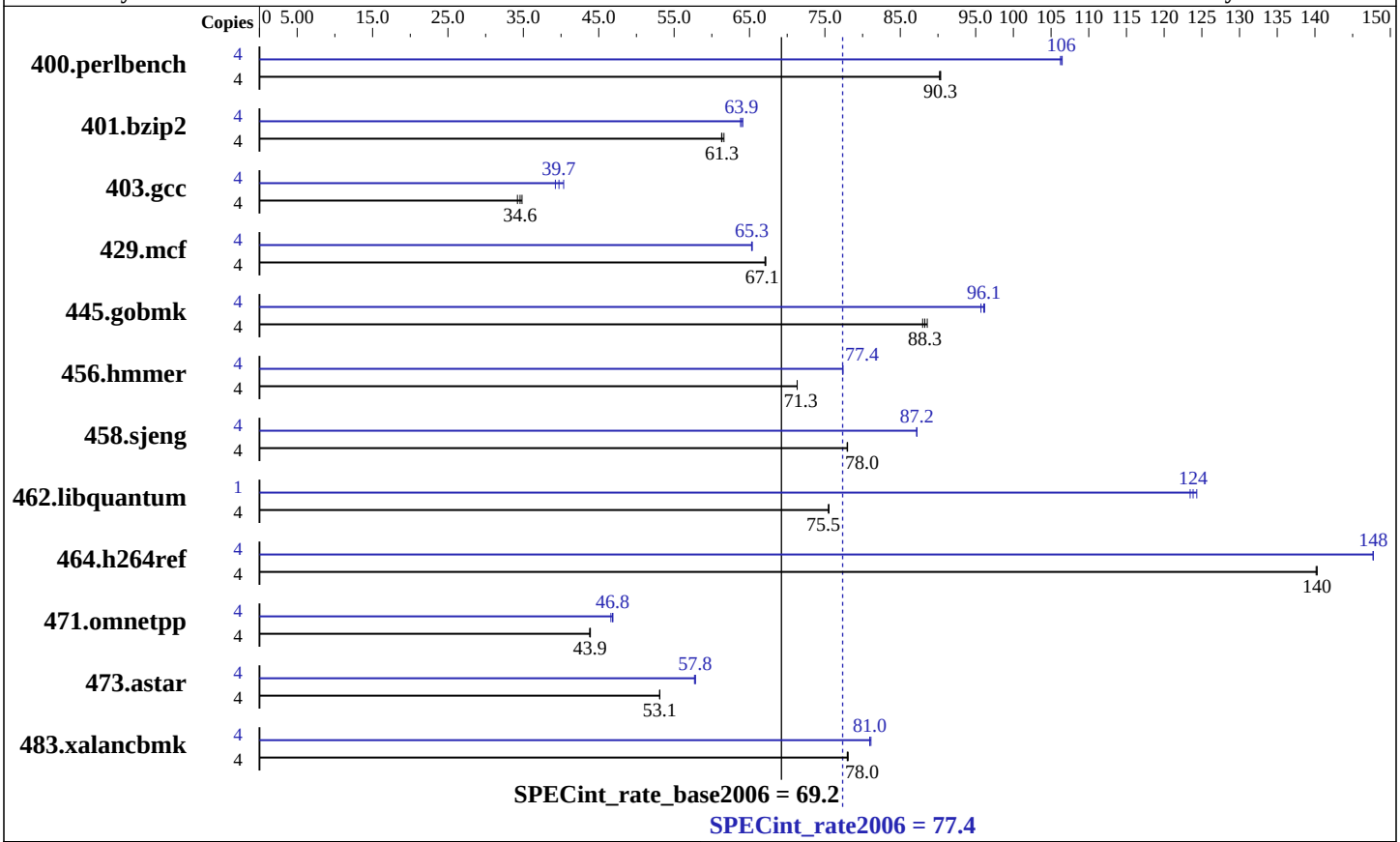
Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECint®_rate2006 = 77.4

SPECint_rate_base2006 = 69.2

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5272
CPU Characteristics:	1600 MHz Bus Speed
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB 800 MHz FB-DIMM CL5)
Disk Subsystem:	1 x 73 GB SAS 15K RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional x64 Edition SP2
Compiler:	Intel C++ Compiler for IA-32, Version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	433	90.2	**433**	**90.3**	433	90.3	4	368	106	**367**	**106**	367	106
401.bzip2	4	**629**	**61.3**	630	61.3	627	61.6	4	**604**	**63.9**	602	64.1	605	63.8
403.gcc	4	941	34.2	925	34.8	**931**	**34.6**	4	**811**	**39.7**	820	39.2	798	40.3
429.mcf	4	**544**	**67.1**	543	67.2	544	67.1	4	559	65.3	558	65.4	**558**	**65.3**
445.gobmk	4	**475**	**88.3**	477	88.0	474	88.6	4	438	95.7	**437**	**96.1**	436	96.2
456.hmmer	4	**523**	**71.3**	523	71.3	523	71.3	4	482	77.4	**482**	**77.4**	482	77.4
458.sjeng	4	**621**	**78.0**	620	78.0	621	77.9	4	555	87.2	**555**	**87.2**	555	87.2
462.libquantum	4	1097	75.5	1098	75.5	**1098**	**75.5**	1	168	123	167	124	**167**	**124**
464.h264ref	4	**631**	**140**	631	140	632	140	4	599	148	**599**	**148**	599	148
471.omnetpp	4	571	43.8	570	43.9	**570**	**43.9**	4	533	46.9	**534**	**46.8**	536	46.6
473.astar	4	**529**	**53.1**	529	53.1	529	53.1	4	**486**	**57.8**	487	57.7	486	57.8
483.xalancbmk	4	353	78.1	354	78.0	**354**	**78.0**	4	340	81.1	**341**	**81.0**	341	80.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings
  Adjacent Cache Line Prefetch set to ON
```

Base Compiler Invocation

C benchmarks:

```
icl -Qstd=c99
```

C++ benchmarks:

```
icl
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qvec-guard-write /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qstd=c99
```

C++ benchmarks:
```
icl
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qprefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: Same as 401.bzip2

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000 -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000 shlw32mt.lib -link /FORCE:MULTIPLE

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.xml

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