



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 25.1

Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECint\_base2006 = 22.4

CPU2006 license: 55

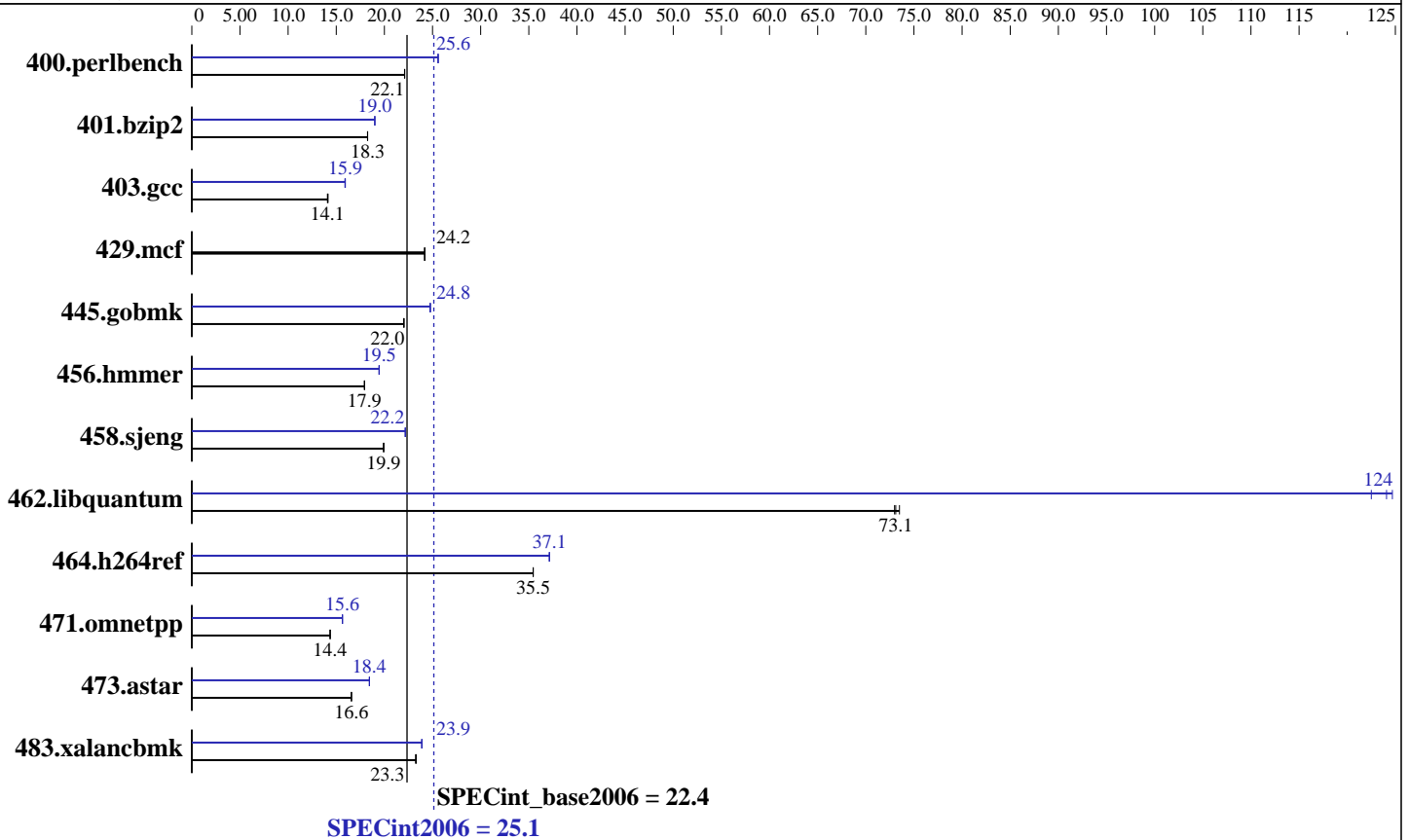
Test date: Dec-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



## Hardware

CPU Name: Intel Xeon X5272  
 CPU Characteristics: 1600 MHz Bus Speed  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB 800 MHz FB-DIMM CL5)  
 Disk Subsystem: 1 x 73 GB SAS 15K RPM  
 Other Hardware: None

## Software

Operating System: Windows XP Professional x64 Edition SP2  
 Compiler: Intel C++ Compiler for IA-32, Version 10.1  
 Build 20070913 Package ID: w\_cc\_p\_10.1.011  
 Microsoft Visual Studio 2005 SP1  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	442	22.1	<b>442</b>	<b>22.1</b>	442	22.1	382	25.6	<b>382</b>	<b>25.6</b>	382	25.6
401.bzip2	528	18.3	529	18.2	<b>529</b>	<b>18.3</b>	508	19.0	508	19.0	<b>508</b>	<b>19.0</b>
403.gcc	569	14.2	<b>571</b>	<b>14.1</b>	572	14.1	506	15.9	<b>505</b>	<b>15.9</b>	505	15.9
429.mcf	377	24.2	<b>377</b>	<b>24.2</b>	377	24.2	377	24.2	<b>377</b>	<b>24.2</b>	377	24.2
445.gobmk	476	22.0	476	22.0	<b>476</b>	<b>22.0</b>	423	24.8	424	24.7	<b>424</b>	<b>24.8</b>
456.hammer	<b>520</b>	<b>17.9</b>	520	17.9	520	17.9	<b>480</b>	<b>19.5</b>	480	19.5	480	19.5
458.sjeng	608	19.9	<b>607</b>	<b>19.9</b>	607	19.9	<b>546</b>	<b>22.2</b>	546	22.2	545	22.2
462.libquantum	<b>283</b>	<b>73.1</b>	284	73.0	282	73.5	<b>167</b>	<b>124</b>	166	125	169	123
464.h264ref	<b>624</b>	<b>35.5</b>	624	35.5	624	35.5	596	37.1	596	37.1	<b>596</b>	<b>37.1</b>
471.omnetpp	435	14.4	<b>435</b>	<b>14.4</b>	435	14.4	400	15.6	<b>399</b>	<b>15.6</b>	399	15.6
473.astar	424	16.6	<b>423</b>	<b>16.6</b>	423	16.6	381	18.4	<b>381</b>	<b>18.4</b>	380	18.5
483.xalancbmk	<b>297</b>	<b>23.3</b>	296	23.3	297	23.3	<b>289</b>	<b>23.9</b>	289	23.9	289	23.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings

Adjacent Cache Line Prefetch set to ON

## Compiler Invocation

C benchmarks:

icl -Qstd=c99

C++ benchmarks:

icl

## Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword



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## Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
shlw32mt.lib libguide40.lib -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

```
401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

```
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE
```

```
456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

```
458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

```
462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 shlw32mt.lib
libguide40.lib -link /FORCE:MULTIPLE
```

```
464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE
```

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
             -Qopt-ra-region-strategy=block -Qcxx_features /F512000000
             shlw32mt.lib libguide40.lib
             -link /FORCE:MULTIPLE
```

```
473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
           -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
           shlw32mt.lib libguide40.lib
           -link /FORCE:MULTIPLE
```

```
483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
               -Qcxx_features /F512000000 shlw32mt.lib libguide40.lib
               -link /FORCE:MULTIPLE
```

## Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
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