SPEC® CINT2006 Result

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SGI

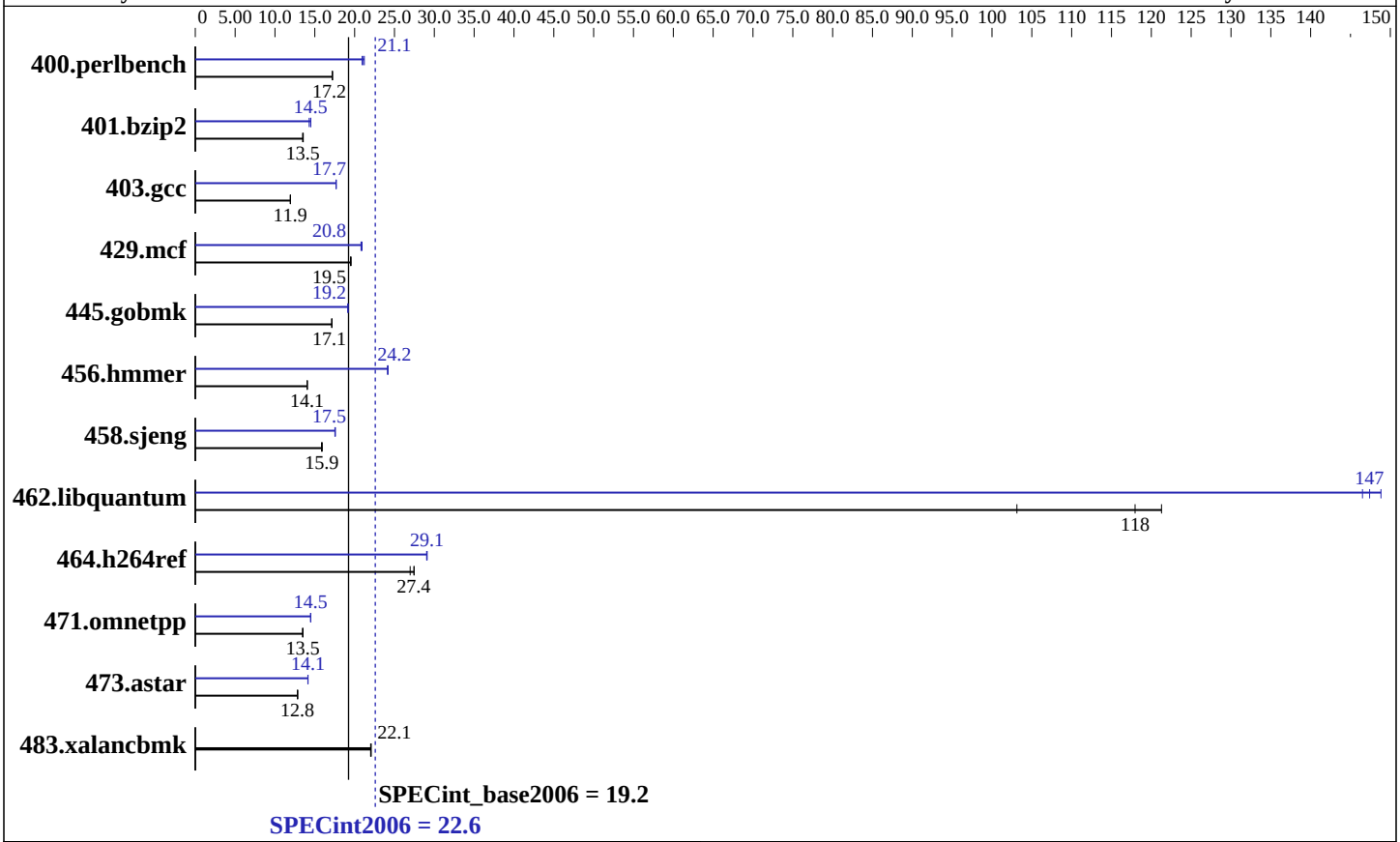
SGI Altix ICE 8200 (Intel Xeon Processor X5355, 2.66 GHz)

SPECint®2006 = 22.6

SPECint_base2006 = 19.2

CPU2006 license: 4
Test sponsor: SGI
Tested by: SGI

Test date: Sep-2007
Hardware Availability: Aug-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	Quad Core, 2.66 GHz
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8*2GB PC2-5300 FB-DIMMs)
Disk Subsystem:	16 TB RAID 6+1 32 x 500 GB SATA (Seagate Barracuda 7200 rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 SP1 (x86_64) Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64, Version 10.1, Build 20070824
Auto Parallel:	Yes
File System:	NFSv3 IPoIB
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SGI ProPack Compute Node 5 SP2 for Linux MicroQuill SmartHeap Library 8 Binutils 2.17

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**567**	**17.2**	567	17.2	569	17.2	460	21.2	467	20.9	**463**	**21.1**
401.bzip2	**715**	**13.5**	712	13.6	716	13.5	**667**	**14.5**	666	14.5	675	14.3
403.gcc	**674**	**11.9**	675	11.9	674	11.9	455	17.7	456	17.7	**455**	**17.7**
429.mcf	466	19.6	**467**	**19.5**	468	19.5	438	20.8	**437**	**20.8**	436	20.9
445.gobmk	612	17.1	**612**	**17.1**	612	17.1	**547**	**19.2**	547	19.2	547	19.2
456.hmmer	**664**	**14.1**	664	14.1	665	14.0	387	24.1	**386**	**24.2**	386	24.2
458.sjeng	**761**	**15.9**	764	15.8	760	15.9	690	17.5	688	17.6	**690**	**17.5**
462.libquantum	201	103	171	121	**176**	**118**	139	149	141	147	**141**	**147**
464.h264ref	821	27.0	**806**	**27.4**	806	27.5	**761**	**29.1**	760	29.1	762	29.0
471.omnetpp	463	13.5	**463**	**13.5**	464	13.5	432	14.5	431	14.5	**431**	**14.5**
473.astar	**547**	**12.8**	547	12.8	547	12.8	497	14.1	**497**	**14.1**	496	14.2
483.xalancbmk	314	22.0	**313**	**22.1**	313	22.1	314	22.0	**313**	**22.1**	313	22.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
BIOS settings:
  Hardware Prefetcher:  enabled
  Adjacent Cacheline Prefetcher:  enabled
All codes compiled with automatic parallelization and run with
  OMP_NUM_THREADS = 8
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-fast -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

C++ benchmarks:
`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.07.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.07.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:08:44 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.