SPEC® CFP2006 Result

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Dell Inc.

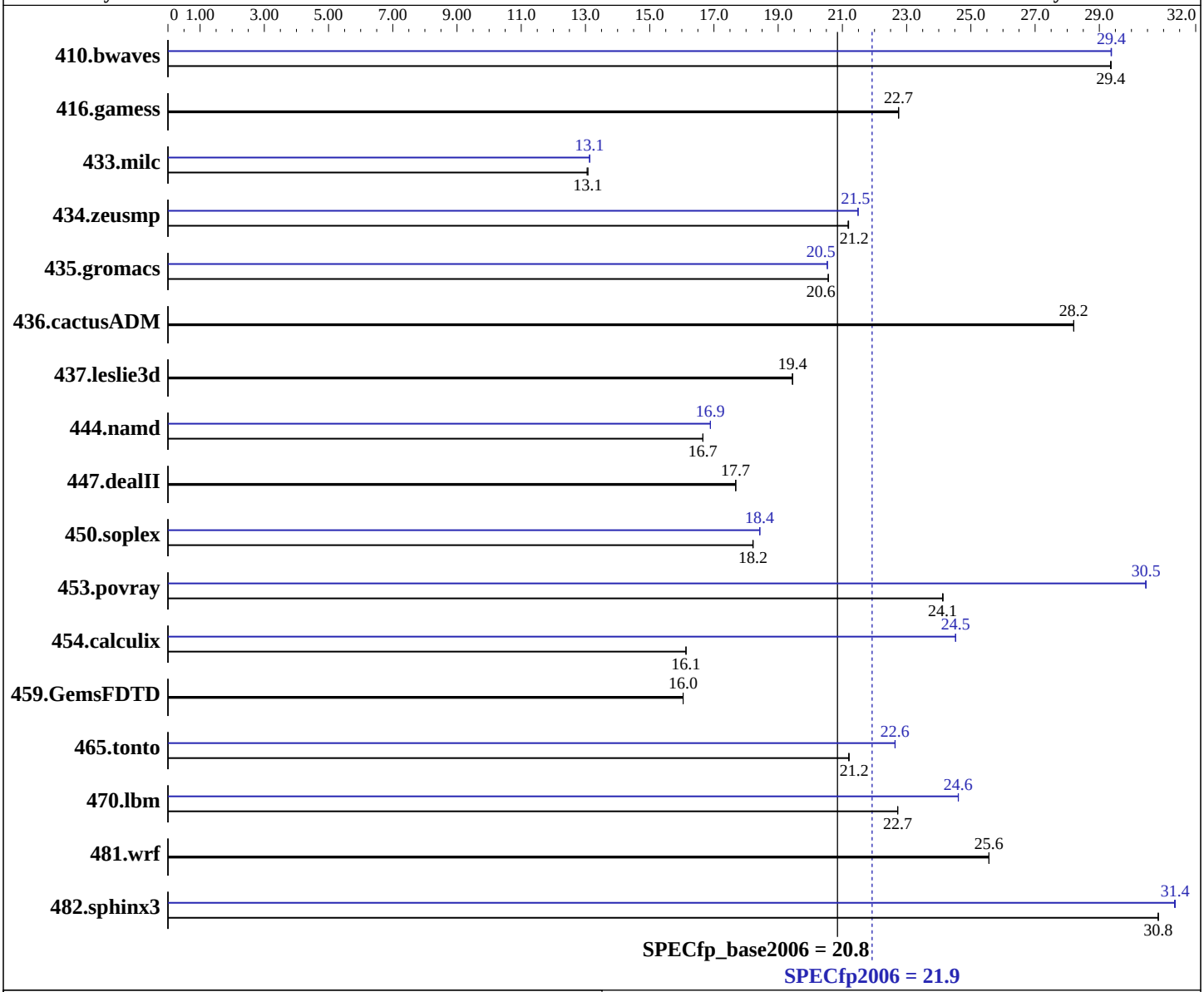
Dell Precision T3400 (Intel E8500, 3.16 GHz)

SPECfp®2006 = 21.9

SPECfp_base2006 = 20.8

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E8500
CPU Characteristics:	1333 MHz Bus Speed
CPU MHz:	3166
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	Windows XP Professional x64 Edition SP2
Compiler:	Intel C++ Compiler for Intel 64, Version 10.1 Build 20070809 Package ID: w_cc_p_10.1.011 Intel Visual Fortran Compiler for Intel 64, Version 10.0 Build 20070809 Package ID: w_fc_p_10.1.011 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32/64-bit
Memory:	4 GB (4x1 GB 800 MHz ECC CL6 DDR2)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 80 GB SATA 7200 RPM	Other Software:	MicroQuill SmartHeap Library 8.1 for x64
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	463	29.3	**463**	**29.4**	463	29.4	463	29.4	463	29.4	**463**	**29.4**
416.gamess	861	22.7	**861**	**22.7**	861	22.8	861	22.7	**861**	**22.7**	861	22.8
433.milc	702	13.1	**702**	**13.1**	704	13.0	**699**	**13.1**	699	13.1	699	13.1
434.zeusmp	429	21.2	430	21.2	**429**	**21.2**	423	21.5	424	21.5	**423**	**21.5**
435.gromacs	**347**	**20.6**	347	20.6	347	20.6	**348**	**20.5**	348	20.5	348	20.5
436.cactusADM	424	28.2	424	28.2	**424**	**28.2**	424	28.2	424	28.2	**424**	**28.2**
437.leslie3d	483	19.4	483	19.4	**483**	**19.4**	483	19.4	483	19.4	**483**	**19.4**
444.namd	**482**	**16.7**	481	16.7	482	16.7	**475**	**16.9**	475	16.9	475	16.9
447.dealII	647	17.7	**647**	**17.7**	647	17.7	647	17.7	**647**	**17.7**	647	17.7
450.soplex	458	18.2	458	18.2	**458**	**18.2**	453	18.4	453	18.4	**453**	**18.4**
453.povray	**221**	**24.1**	221	24.1	220	24.1	**175**	**30.5**	175	30.5	175	30.4
454.calculix	511	16.1	**511**	**16.1**	512	16.1	336	24.5	**336**	**24.5**	336	24.5
459.GemsFDTD	661	16.0	661	16.0	**661**	**16.0**	661	16.0	661	16.0	**661**	**16.0**
465.tonto	464	21.2	**464**	**21.2**	464	21.2	435	22.6	435	22.6	**435**	**22.6**
470.lbm	**605**	**22.7**	605	22.7	605	22.7	558	24.6	**558**	**24.6**	558	24.6
481.wrf	437	25.6	**437**	**25.6**	437	25.6	437	25.6	**437**	**25.6**	437	25.6
482.sphinx3	632	30.8	**632**	**30.8**	632	30.8	621	31.4	**622**	**31.4**	622	31.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qstd=c99
```

C++ benchmarks:

```
icl
```

Fortran benchmarks:

```
ifort
```

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -Qauto-ilp32 -Qparallel /F1000000000 shlw64mt.lib
libguide40.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qauto-ilp32 -Qparallel -Qcxx_features /F1000000000
shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast -Qauto-ilp32 -Qparallel /F1000000000 libguide40.lib
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast -Qauto-ilp32 -Qparallel /F1000000000 libguide40.lib
-link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qstd=c99

C++ benchmarks:
icl

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll2 -Oa /F1000000000 shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll2 -Qscalar-rep- -Qprefetch /F1000000000 shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

482.sphinx3: -fast -Qauto-ilp32 -Qunroll2 /F1000000000 shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Oa -Qcxx_features /F1000000000 shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qparallel -Qcxx_features /F1000000000 shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32 -Qunroll4 -Qansi-alias -Qcxx_features /F1000000000 shlw64mt.lib libguide40.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-fast -Qauto-ilp32 -Qparallel -Qprefetch /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

416.gamess: `basepeak = yes`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qauto-ilp32 -Qunroll4 -Qauto /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qauto-ilp32 -Oa -Qprefetch /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast -Qauto-ilp32 -Qunroll-aggressive /F1000000000 libguide40.lib -link /FORCE:MULTIPLE`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.xml`

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