



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®_rate2006 = 38.4

CELSIUS M460, Intel Core 2 Duo E6850 processor

SPECint_rate_base2006 = 34.7

CPU2006 license: 22

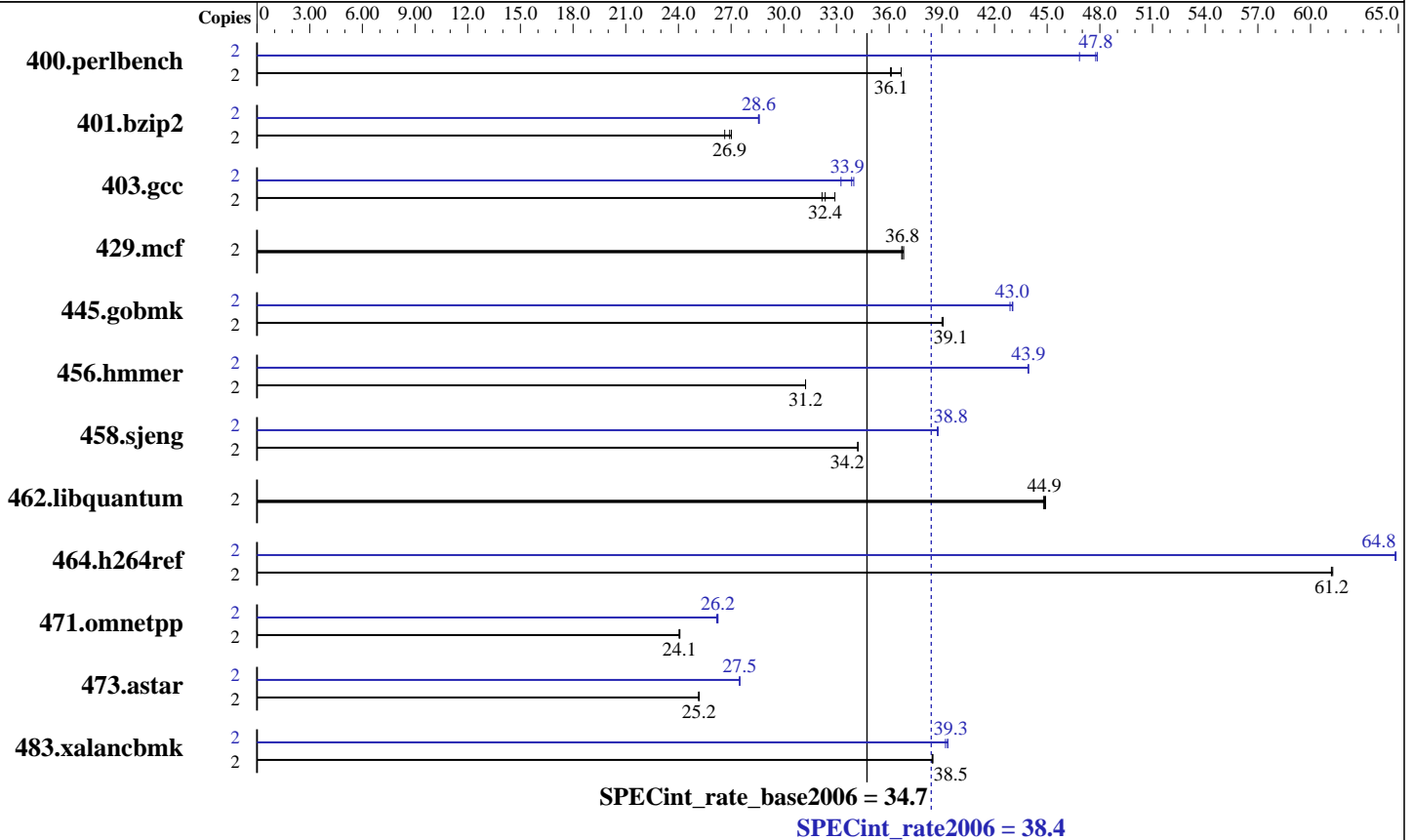
Test date: Dec-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Nov-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E6850
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA II 7200 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Vista Ultimate (x64)
 Compiler: Intel C++ Compilers for IA-32 and for Intel64, Version 10.1, Build 20070913
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	533	36.7	<u>541</u>	<u>36.1</u>	542	36.1	2	417	46.8	<u>409</u>	<u>47.8</u>	408	47.9
401.bzip2	2	725	26.6	<u>717</u>	<u>26.9</u>	714	27.0	2	675	28.6	<u>675</u>	<u>28.6</u>	676	28.6
403.gcc	2	489	32.9	<u>498</u>	<u>32.4</u>	500	32.2	2	484	33.3	474	34.0	<u>475</u>	<u>33.9</u>
429.mcf	2	497	36.7	495	36.8	<u>496</u>	<u>36.8</u>	2	497	36.7	495	36.8	<u>496</u>	<u>36.8</u>
445.gobmk	2	537	39.1	<u>537</u>	<u>39.1</u>	538	39.0	2	487	43.0	<u>488</u>	<u>43.0</u>	489	42.9
456.hmmmer	2	<u>597</u>	<u>31.2</u>	597	31.2	597	31.2	2	<u>425</u>	<u>43.9</u>	425	43.9	425	43.9
458.sjeng	2	<u>707</u>	<u>34.2</u>	707	34.2	707	34.2	2	<u>624</u>	<u>38.8</u>	625	38.7	624	38.8
462.libquantum	2	<u>924</u>	<u>44.9</u>	923	44.9	925	44.8	2	<u>924</u>	<u>44.9</u>	923	44.9	925	44.8
464.h264ref	2	<u>723</u>	<u>61.2</u>	723	61.2	722	61.3	2	683	64.8	682	64.9	<u>683</u>	<u>64.8</u>
471.omnetpp	2	<u>520</u>	<u>24.1</u>	520	24.0	519	24.1	2	477	26.2	476	26.3	<u>476</u>	<u>26.2</u>
473.astar	2	<u>558</u>	<u>25.2</u>	557	25.2	558	25.1	2	<u>511</u>	<u>27.5</u>	511	27.5	511	27.5
483.xalancbmk	2	<u>359</u>	<u>38.5</u>	359	38.5	359	38.4	2	351	39.3	<u>351</u>	<u>39.3</u>	352	39.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qvec-guard-write -F512000000
C++ benchmarks:
-fast -Qcxx_features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icl -Qvc8 -Qc99
401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
456.hmmer: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
C++ benchmarks:
icl -Qvc8

Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64

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Peak Portability Flags (Continued)

403.gcc: -DSPEC_CPU_WIN32
 456.hmmr: -DSPEC_CPU_P64
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
 -Qprefetch -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
 -F512000000

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
 -Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
 -Qansi-alias -Qopt-multi-version-aggressive -F512000000

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
 -F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
 -Qansi-alias -F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
 -Qopt-ra-region-strategy=block -Qcxx_features -F512000000
 shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
 -Qopt-ra-region-strategy=routine -Qcxx_features -F512000000
 shlW32M.lib -link -FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
 -Qcxx_features -F512000000 shlW32M.lib
 -link -FORCE:MULTIPLE



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml

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For other inquiries, please contact webmaster@spec.org.

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