Intel Corporation
Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECfp_rate2006 = 23.7
SPECfp_rate_base2006 = 23.1

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>2.00</th>
<th>4.00</th>
<th>6.00</th>
<th>8.00</th>
<th>10.0</th>
<th>12.0</th>
<th>14.0</th>
<th>16.0</th>
<th>18.0</th>
<th>20.0</th>
<th>22.0</th>
<th>24.0</th>
<th>26.0</th>
<th>28.0</th>
<th>30.0</th>
<th>32.0</th>
<th>34.0</th>
<th>36.0</th>
<th>38.0</th>
<th>40.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>416.gamess</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
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<td></td>
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<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>433.milc</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>2</td>
<td>14.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>435.gromacs</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>2</td>
<td>15.0</td>
<td></td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>444.namd</td>
<td>2</td>
<td></td>
<td></td>
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<td></td>
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<td></td>
</tr>
<tr>
<td>447.dealII</td>
<td>2</td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>450.soplex</td>
<td>2</td>
<td></td>
<td></td>
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<td></td>
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<tr>
<td>453.povray</td>
<td>2</td>
<td></td>
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<td></td>
</tr>
<tr>
<td>454.calculix</td>
<td>2</td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>2</td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>465.tonto</td>
<td>2</td>
<td></td>
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<td></td>
</tr>
<tr>
<td>470.lbm</td>
<td>2</td>
<td></td>
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<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>481.wrf</td>
<td>2</td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>2</td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Hardware

<table>
<thead>
<tr>
<th>CPU Name</th>
<th>Intel Core 2 Extreme X7900</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics</td>
<td></td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2800</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>2 cores, 1 chip, 2 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>4 MB I4+D on chip per chip</td>
</tr>
</tbody>
</table>

Software

<table>
<thead>
<tr>
<th>Operating System</th>
<th>Windows Vista Ultimate(32-bit)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>Intel C++ Compiler for IA32 version 10.1</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>File System:</td>
<td>NTFS</td>
</tr>
<tr>
<td>System State:</td>
<td>Default</td>
</tr>
</tbody>
</table>

Continued on next page
SPEC CFP2006 Result

Intel Corporation

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECfp_rate2006 = 23.7
SPECfp_rate_base2006 = 23.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
Disk Subsystem: Fujitsu 120GB GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None

SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>2</td>
<td>1206</td>
<td>22.5</td>
<td>1208</td>
<td>22.5</td>
<td>1198</td>
<td>22.7</td>
</tr>
<tr>
<td>416.gamess</td>
<td>2</td>
<td>1241</td>
<td>31.6</td>
<td>1228</td>
<td>31.9</td>
<td>1258</td>
<td>31.1</td>
</tr>
<tr>
<td>433.milc</td>
<td>2</td>
<td>1248</td>
<td>14.7</td>
<td>1254</td>
<td>14.6</td>
<td>1250</td>
<td>14.7</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>2</td>
<td>673</td>
<td>27.0</td>
<td>673</td>
<td>27.1</td>
<td>673</td>
<td>27.0</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>2</td>
<td>487</td>
<td>29.9</td>
<td>481</td>
<td>29.7</td>
<td>485</td>
<td>29.5</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>2</td>
<td>779</td>
<td>30.7</td>
<td>780</td>
<td>30.7</td>
<td>782</td>
<td>30.5</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>2</td>
<td>1235</td>
<td>15.2</td>
<td>1237</td>
<td>15.2</td>
<td>1237</td>
<td>15.2</td>
</tr>
<tr>
<td>444.namd</td>
<td>2</td>
<td>607</td>
<td>26.4</td>
<td>617</td>
<td>26.0</td>
<td>617</td>
<td>26.0</td>
</tr>
<tr>
<td>447.dealII</td>
<td>2</td>
<td>740</td>
<td>30.9</td>
<td>742</td>
<td>30.8</td>
<td>738</td>
<td>31.0</td>
</tr>
<tr>
<td>450.soplex</td>
<td>2</td>
<td>977</td>
<td>17.1</td>
<td>976</td>
<td>17.1</td>
<td>976</td>
<td>17.1</td>
</tr>
<tr>
<td>453.povray</td>
<td>2</td>
<td>278</td>
<td>38.2</td>
<td>278</td>
<td>38.3</td>
<td>279</td>
<td>38.1</td>
</tr>
<tr>
<td>454.calculix</td>
<td>2</td>
<td>791</td>
<td>20.9</td>
<td>782</td>
<td>21.1</td>
<td>786</td>
<td>21.0</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>2</td>
<td>1611</td>
<td>13.2</td>
<td>1612</td>
<td>13.2</td>
<td>1613</td>
<td>13.2</td>
</tr>
<tr>
<td>465.tonto</td>
<td>2</td>
<td>754</td>
<td>26.1</td>
<td>757</td>
<td>26.0</td>
<td>754</td>
<td>26.1</td>
</tr>
<tr>
<td>470.lbm</td>
<td>2</td>
<td>1986</td>
<td>13.8</td>
<td>1986</td>
<td>13.8</td>
<td>1986</td>
<td>13.8</td>
</tr>
<tr>
<td>481.wrf</td>
<td>2</td>
<td>862</td>
<td>25.9</td>
<td>861</td>
<td>25.9</td>
<td>862</td>
<td>25.9</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>2</td>
<td>1383</td>
<td>28.2</td>
<td>1384</td>
<td>28.2</td>
<td>1383</td>
<td>28.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 800 MHz
Binaries were built on Windows Vista32
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
The start command with the /affinity switch was used to bind processes to cores

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

Continued on next page
Intel Corporation

Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECfp_rate2006 = 23.7
SPECfp_rate_base2006 = 23.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Hardware Availability: Jan-2008
Tested by: Intel Corporation
Software Availability: Nov-2007

Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDATA_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F1000000000

C++ benchmarks:
-fast -Qcxx_features /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F1000000000

Benchmarks using both Fortran and C:
-fast /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Continued on next page
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:
433.milc: -fast -Qunroll12 -Oa /F1000000000
470.lbm: -fast -Qunroll12 -Qscalar-rep- -Qprefetch /F1000000000
482.sphinx3: -fast -Qunroll12 /F1000000000

C++ benchmarks:
444.namd: -fast -Oa -Qcxx_features /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE
447.dealII: -fast -Qunroll12 -Qprefetch -Qcxx_features /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE
450.soplex: -fast -Qcxx_features /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE
453.povray: -fast -Qunroll14 -Qansi-alias -Qcxx_features /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -fast -Qunroll12 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000
434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll10 -Qscalar-rep- /F1000000000
Intel Corporation
Alienware Area-51 M15x-R1 (Intel Core 2 Extreme X7900)

SPECfp_rate2006 = 23.7
SPECfp_rate_base2006 = 23.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Peak Optimization Flags (Continued)

437.leslie3d: -fast -Qprefetch /F1000000000
459.GemsFDTD: -fast -Qunroll12 -Obo -Qprefetch /F1000000000
465.tonto: -fast -Qunroll14 -Qauto /F1000000000

Benchmarks using both Fortran and C:
435.gromacs: -fast -Oa -Qprefetch /F1000000000
436.cactusADM: -fast -Qunroll12 -Qprefetch /F1000000000
454.calculix: -fast -Qunroll-aggressive /F1000000000
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:05:28 2014 by SPEC CPU2006 PS/PDF formatter v6932.