



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = **20.1**

Lenovo ThinkPad T61 (Intel Core 2 Duo T9500)

SPECint_base2006 = **18.1**

CPU2006 license: 13

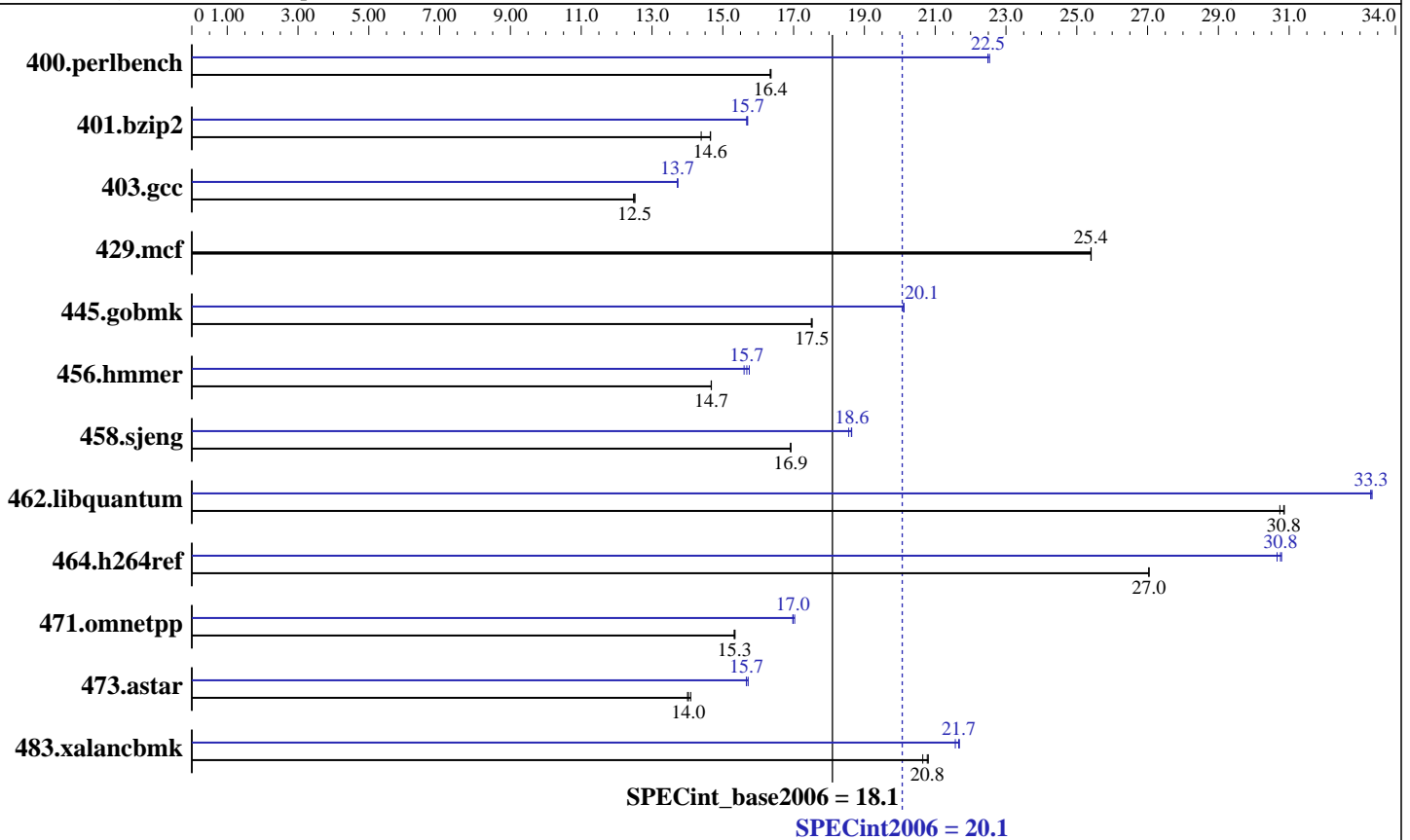
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Dec-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo T9500
 CPU Characteristics:
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)
 Disk Subsystem: Hitachi 100 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 10.1
 Build 20070913 Package ID: w_cc_p_10.1.011
 Microsoft Visual Studio 2005 SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>597</u>	<u>16.4</u>	597	16.4	598	16.3	434	22.5	<u>434</u>	<u>22.5</u>	434	22.5
401.bzip2	671	14.4	658	14.7	<u>659</u>	<u>14.6</u>	<u>615</u>	<u>15.7</u>	615	15.7	616	15.7
403.gcc	645	12.5	<u>644</u>	<u>12.5</u>	643	12.5	586	13.7	587	13.7	<u>587</u>	<u>13.7</u>
429.mcf	359	25.4	359	25.4	<u>359</u>	<u>25.4</u>	359	25.4	359	25.4	<u>359</u>	<u>25.4</u>
445.gobmk	<u>599</u>	<u>17.5</u>	599	17.5	599	17.5	<u>522</u>	<u>20.1</u>	521	20.1	522	20.1
456.hammer	636	14.7	<u>636</u>	<u>14.7</u>	636	14.7	598	15.6	592	15.7	<u>595</u>	<u>15.7</u>
458.sjeng	<u>715</u>	<u>16.9</u>	715	16.9	715	16.9	649	18.6	652	18.6	<u>649</u>	<u>18.6</u>
462.libquantum	671	30.9	674	30.7	<u>672</u>	<u>30.8</u>	<u>622</u>	<u>33.3</u>	622	33.3	621	33.3
464.h264ref	<u>818</u>	<u>27.0</u>	819	27.0	818	27.0	<u>720</u>	<u>30.8</u>	719	30.8	722	30.7
471.omnetpp	407	15.3	<u>408</u>	<u>15.3</u>	408	15.3	368	17.0	<u>368</u>	<u>17.0</u>	367	17.0
473.astar	498	14.1	<u>500</u>	<u>14.0</u>	501	14.0	448	15.7	<u>448</u>	<u>15.7</u>	447	15.7
483.xalanbmk	<u>332</u>	<u>20.8</u>	332	20.8	334	20.6	318	21.7	<u>319</u>	<u>21.7</u>	320	21.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The system bus runs at 800 MHz
 Binaries were built on Windows Vista (32-bit)
 The following VS 2005 SP1 updates were applied: KB926601 and KB932232
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to physical,0

Compiler Invocation

C benchmarks:
 icl -Qvc8 -Qc99

C++ benchmarks:
 icl -Qvc8

Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll14
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll14 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll12
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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