SPEC® CFP2006 Result

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Intel Corporation

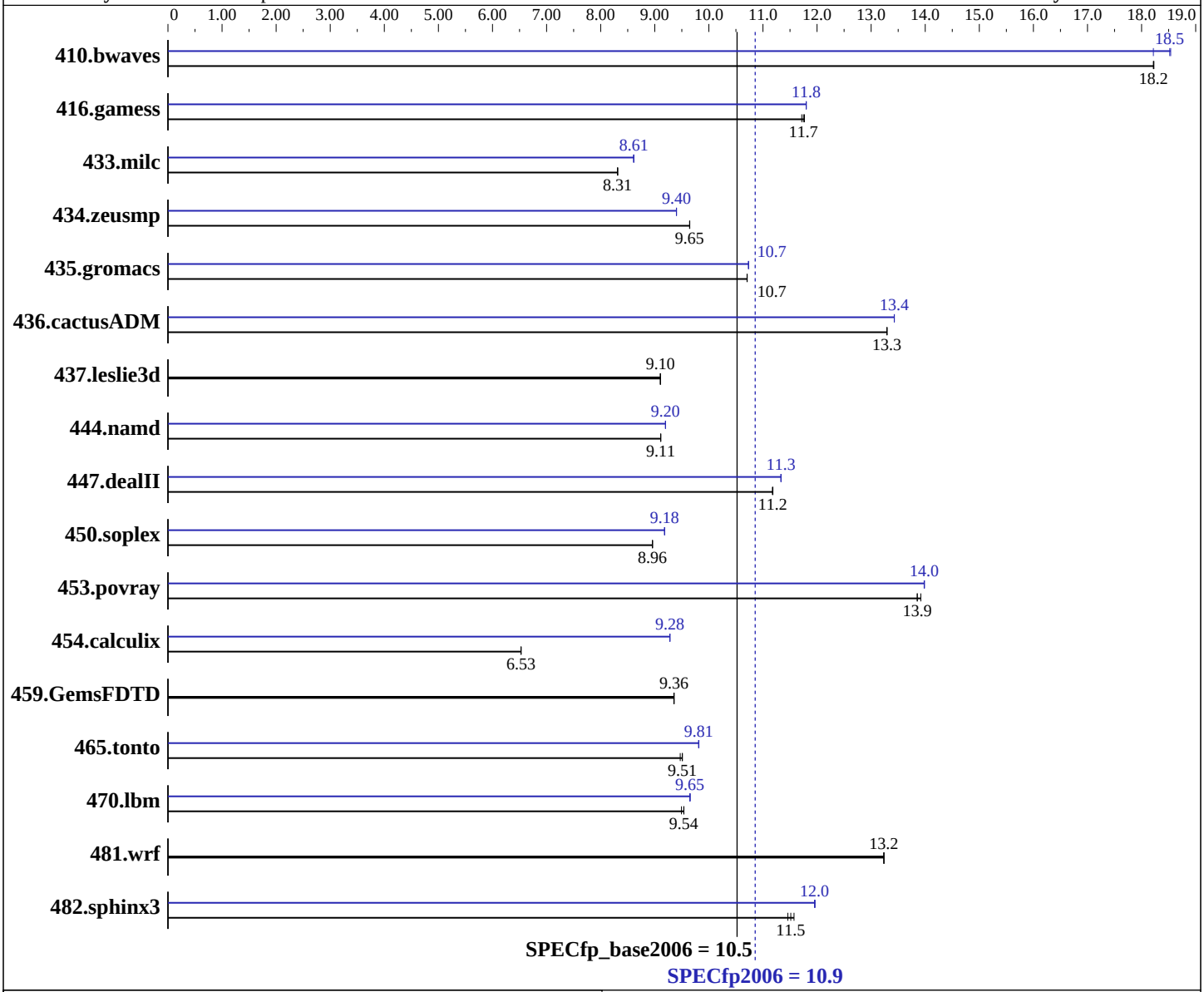
Lenovo ThinkPad T60 (Intel Core Duo T2700)

SPECfp®2006 = 10.9

SPECfp_base2006 = 10.5

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core Duo T2700
CPU Characteristics:	
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Intel Fortran Compiler for IA32 version 10.1 Build 20070913 Package ID: w_fc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem: Hitachi 100 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None
SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	746	18.2	**746**	**18.2**	746	18.2	746	18.2	**734**	**18.5**	733	18.5
416.gamess	**1667**	**11.7**	1664	11.8	1671	11.7	1660	11.8	**1659**	**11.8**	1659	11.8
433.milc	1105	8.31	**1104**	**8.31**	1104	8.32	1066	8.61	**1066**	**8.61**	1066	8.61
434.zeusmp	**943**	**9.65**	943	9.65	944	9.64	968	9.40	968	9.40	**968**	**9.40**
435.gromacs	**667**	**10.7**	667	10.7	667	10.7	665	10.7	665	10.7	**665**	**10.7**
436.cactusADM	899	13.3	**899**	**13.3**	899	13.3	890	13.4	890	13.4	**890**	**13.4**
437.leslie3d	1032	9.11	**1033**	**9.10**	1033	9.10	1032	9.11	**1033**	**9.10**	1033	9.10
444.namd	**880**	**9.11**	880	9.11	880	9.11	872	9.20	**872**	**9.20**	872	9.20
447.dealII	1024	11.2	**1023**	**11.2**	1023	11.2	1010	11.3	1009	11.3	**1009**	**11.3**
450.soplex	**931**	**8.96**	931	8.96	931	8.96	908	9.18	**908**	**9.18**	908	9.18
453.povray	**384**	**13.9**	382	13.9	384	13.9	381	14.0	380	14.0	**380**	**14.0**
454.calculix	1264	6.53	1264	6.53	**1264**	**6.53**	**889**	**9.28**	889	9.28	889	9.28
459.GemsFDTD	1134	9.36	1134	9.35	**1134**	**9.36**	1134	9.36	1134	9.35	**1134**	**9.36**
465.tonto	1039	9.47	**1035**	**9.51**	1035	9.51	1002	9.82	1003	9.81	**1003**	**9.81**
470.lbm	1447	9.49	1440	9.54	**1441**	**9.54**	1423	9.66	**1424**	**9.65**	1424	9.65
481.wrf	**844**	**13.2**	844	13.2	844	13.2	**844**	**13.2**	844	13.2	844	13.2
482.sphinx3	**1693**	**11.5**	1684	11.6	1701	11.5	**1630**	**12.0**	1630	12.0	1629	12.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 667 MHz
Binaries were built on Windows Vista (32-bit)
The following VS 2005 SP1 updates were applied: KB926601 and KB932232
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qparallel /F1000000000 libguide40.lib

C++ benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qparallel -Qcxx_features /F1000000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qparallel /F1000000000 libguide40.lib

Benchmarks using both Fortran and C:
-QxP -Qipo -O3 -Qprec-div- -Qparallel /F1000000000 libguide40.lib

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Oa /F1000000000 libguide40.lib

470.lbm: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qscalar-rep- -Qprefetch /F1000000000 libguide40.lib

482.sphinx3: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000 libguide40.lib

C++ benchmarks:

444.namd: -QxP -Qipo -O3 -Qprec-div- -Oa -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

447.dealII: -QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qprefetch -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

450.soplex: -QxP -Qipo -O3 -Qprec-div- -Qparallel -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

453.povray: -QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Qcxx_features /F1000000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `-QxP -Qipo -O3 -Qprec-div- -Qparallel -Qprefetch /F1000000000 libguide40.lib`

416.gamess: `-QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000 libguide40.lib`

434.zeusmp: `-QxP -O2 -Qprec-div- -Qunroll0 -Qscalar-rep- /F1000000000 libguide40.lib`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-QxP -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000 libguide40.lib`

Benchmarks using both Fortran and C:

435.gromacs: `-QxP -Qipo -O3 -Qprec-div- -Oa -Qprefetch /F1000000000 libguide40.lib`

436.cactusADM: `-QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Qparallel -Qprefetch /F1000000000 libguide40.lib`

454.calculix: `-QxP -Qipo -O3 -Qprec-div- -Qunroll-aggressive /F1000000000 libguide40.lib`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml`

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