



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 16.8

CELSIUS R550, Intel Xeon E5205, 1.87 GHz

SPECint_base2006 = 14.1

CPU2006 license: 22

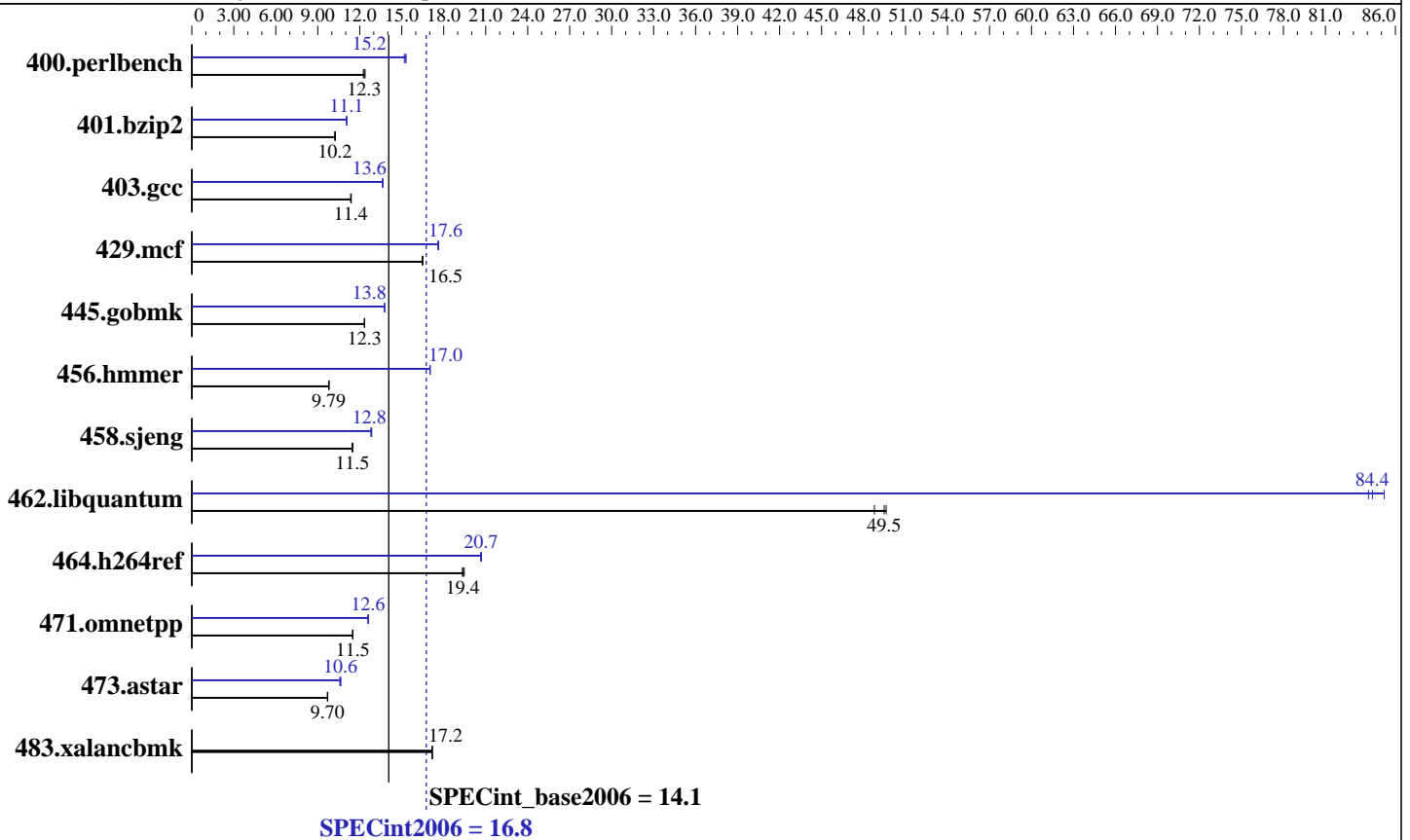
Test date: Jan-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Jan-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1
 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	797	12.3	789	12.4	<u>793</u>	<u>12.3</u>	643	15.2	638	15.3	<u>641</u>	<u>15.2</u>
401.bzip2	<u>944</u>	<u>10.2</u>	944	10.2	941	10.3	873	11.1	871	11.1	<u>873</u>	<u>11.1</u>
403.gcc	<u>708</u>	<u>11.4</u>	709	11.4	707	11.4	590	13.6	<u>590</u>	<u>13.6</u>	591	13.6
429.mcf	<u>553</u>	<u>16.5</u>	553	16.5	554	16.5	518	17.6	<u>518</u>	<u>17.6</u>	517	17.6
445.gobmk	<u>850</u>	<u>12.3</u>	850	12.3	850	12.3	<u>762</u>	<u>13.8</u>	762	13.8	762	13.8
456.hmmmer	<u>953</u>	<u>9.79</u>	952	9.80	953	9.79	<u>548</u>	<u>17.0</u>	548	17.0	548	17.0
458.sjeng	1052	11.5	<u>1053</u>	<u>11.5</u>	1058	11.4	941	12.9	945	12.8	<u>944</u>	<u>12.8</u>
462.libquantum	425	48.8	<u>419</u>	<u>49.5</u>	418	49.6	243	85.2	246	84.1	<u>246</u>	<u>84.4</u>
464.h264ref	<u>1143</u>	<u>19.4</u>	1144	19.3	1137	19.5	1072	20.6	1070	20.7	<u>1070</u>	<u>20.7</u>
471.omnetpp	543	11.5	544	11.5	<u>543</u>	<u>11.5</u>	497	12.6	495	12.6	<u>496</u>	<u>12.6</u>
473.astar	725	9.69	<u>724</u>	<u>9.70</u>	723	9.72	<u>661</u>	<u>10.6</u>	664	10.6	659	10.6
483.xalancbmk	401	17.2	402	17.2	<u>402</u>	<u>17.2</u>	401	17.2	402	17.2	<u>402</u>	<u>17.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Enhanced Speedstep Technology = Disable
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
SnoopFilter = Disable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

Continued on next page



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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
           -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
           -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
             -no-prec-div -ansi-alias -opt-ra-region-strategy=block
             -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

```



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Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.03.xml>

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For other inquiries, please contact webmaster@spec.org.

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