SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Non-Compliant
Hewlett-Packard Company

ProLiant ML350 G5
(3.33 GHz, Intel Xeon X5260)

SPECint2006 = NC
SPECint_base2006 = NC

Hewlett-Packard Company
Test sponsor:
Hewlett-Packard Company

Hewlett-Packard Company
Test date:
Feb-2008

Hewlett-Packard Company
Hardware Availability:
Jan-2008

Hewlett-Packard Company
Software Availability:
Nov-2007

SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Results Table

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</table>
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode

Base Compiler Invocation

C benchmarks:
  icc

C++ benchmarks:
  icpc

Base Portability Flags

400.perlbmk: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -Dguard-write -opt-malloc-options=3 -parallel
  -Dpar-runtime-control

C++ benchmarks:
  -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
  -L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

Continued on next page

Non-Compliant
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
   -L/opt/intel/cce/10.1.008/lib
   -I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
   -L/opt/intel/cce/10.1.008/lib
   -I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbmk: -DSPEC_CPU_LINUX_IA32
   -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

483.xalancbmk: basepeak = yes
SPEC has determined that this result was not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter reported that the result would not meet the 3 month availability requirement in the SPEC CPU2006 run rules due to a change in the availability date of the system.

**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at [http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html](http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090713.html)

You can also download the XML flags source by saving the following link:


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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