



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

**SPECint®2006 = 22.9**

## CELSIUS M460, Intel Core 2 Quad Q9550

**SPECint\_base2006 = 20.6**

CPU2006 license: 22

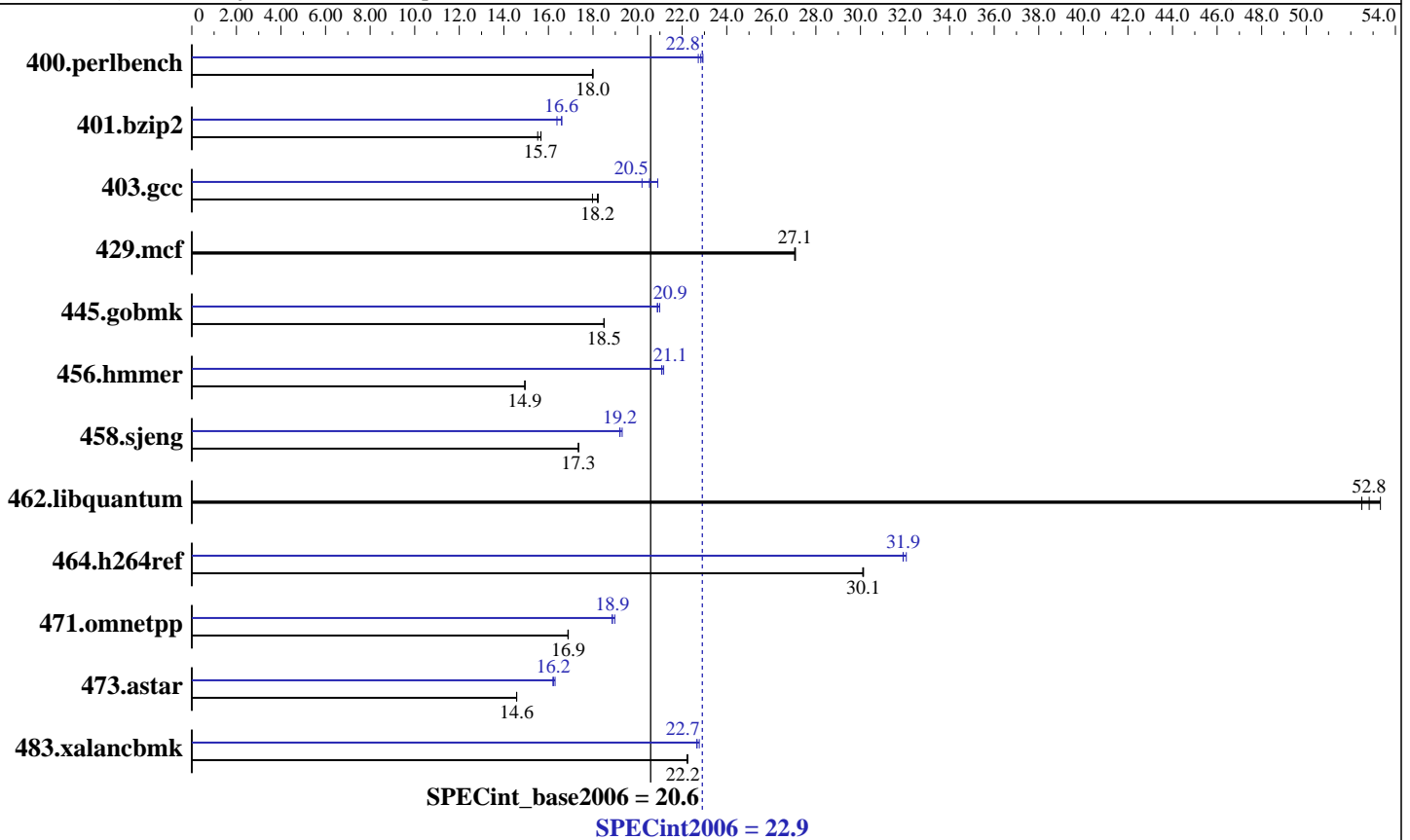
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Core 2 Quad Q9550  
 CPU Characteristics: 2833  
 CPU MHz: 2833  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)  
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM  
 Other Hardware: None

### Software

Operating System: Windows Vista Ultimate, 64 bit Version  
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20070913  
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913  
 Microsoft Visual Studio 2005 with SP1 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	544	18.0	543	18.0	<u>543</u>	<u>18.0</u>	426	22.9	<u>428</u>	<u>22.8</u>	430	22.7
401.bzip2	622	15.5	616	15.7	<u>616</u>	<u>15.7</u>	581	16.6	<u>582</u>	<u>16.6</u>	589	16.4
403.gcc	<u>442</u>	<u>18.2</u>	448	18.0	441	18.2	385	20.9	<u>392</u>	<u>20.5</u>	399	20.2
429.mcf	<u>337</u>	<u>27.1</u>	337	27.1	337	27.1	<u>337</u>	<u>27.1</u>	337	27.1	337	27.1
445.gobmk	567	18.5	567	18.5	<u>567</u>	<u>18.5</u>	500	21.0	<u>502</u>	<u>20.9</u>	502	20.9
456.hmmmer	624	14.9	624	15.0	<u>624</u>	<u>14.9</u>	441	21.2	442	21.1	<u>442</u>	<u>21.1</u>
458.sjeng	697	17.4	<u>698</u>	<u>17.3</u>	698	17.3	627	19.3	<u>630</u>	<u>19.2</u>	630	19.2
462.libquantum	395	52.5	<u>392</u>	<u>52.8</u>	389	53.3	395	52.5	<u>392</u>	<u>52.8</u>	389	53.3
464.h264ref	735	30.1	734	30.1	<u>734</u>	<u>30.1</u>	690	32.1	<u>693</u>	<u>31.9</u>	693	31.9
471.omnetpp	370	16.9	<u>370</u>	<u>16.9</u>	370	16.9	329	19.0	<u>331</u>	<u>18.9</u>	331	18.9
473.astar	482	14.6	482	14.6	<u>482</u>	<u>14.6</u>	431	16.3	<u>433</u>	<u>16.2</u>	433	16.2
483.xalancbmk	311	22.2	310	22.2	<u>310</u>	<u>22.2</u>	303	22.8	<u>304</u>	<u>22.7</u>	304	22.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

BIOS default settings have been used.

## General Notes

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com/>

## Base Compiler Invocation

C benchmarks:  
icl -Qvc8 -Qc99

C++ benchmarks:  
icl -Qvc8



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## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword

## Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000  
libguide40.lib

C++ benchmarks:

-fast -Qcxx-features -F512000000 libguide40.lib shlw32M.lib  
-link -FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icl -Qvc8 -Qc99

401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe  
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include  
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

456.hmmr: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe  
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include  
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"  
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"

C++ benchmarks:

icl -Qvc8



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## Peak Portability Flags

401.bzip2: -DSPEC\_CPU\_P64  
 403.gcc: -DSPEC\_CPU\_WIN32  
 456.hmmr: -DSPEC\_CPU\_P64  
 464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
 483.xalancbmk: -Qoption, cpp, --no\_wchar\_t\_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qparallel  
 -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000  
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qprefetch  
 -F512000000 libguide40.lib

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -F512000000  
 libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -O2 -Qipo -QxT  
 -Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
 -Qansi-alias -Qopt-multi-version-aggressive -F512000000  
 libguide40.lib

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
 -F512000000 libguide40.lib

462.libquantum: basepeak = yes

464.h264ref: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
 -Qansi-alias -F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx-features  
 -Qansi-alias -Qopt-ra-region-strategy=block -F512000000  
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx-features  
 -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000  
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

```
483.xalanbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
              -Qansi-alias -F512000000 libguide40.lib shlw32M.lib
              -link -FORCE:MULTIPLE
```

## Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090713.02.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090713.02.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml)

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Mon Jul 13 17:57:01 2009 by SPEC CPU2006 PS/PDF formatter v6323.