



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®2006 = 21.4

CELSIUS M460, Intel Core 2 Quad Q9550

SPECfp_base2006 = 20.0

CPU2006 license: 22

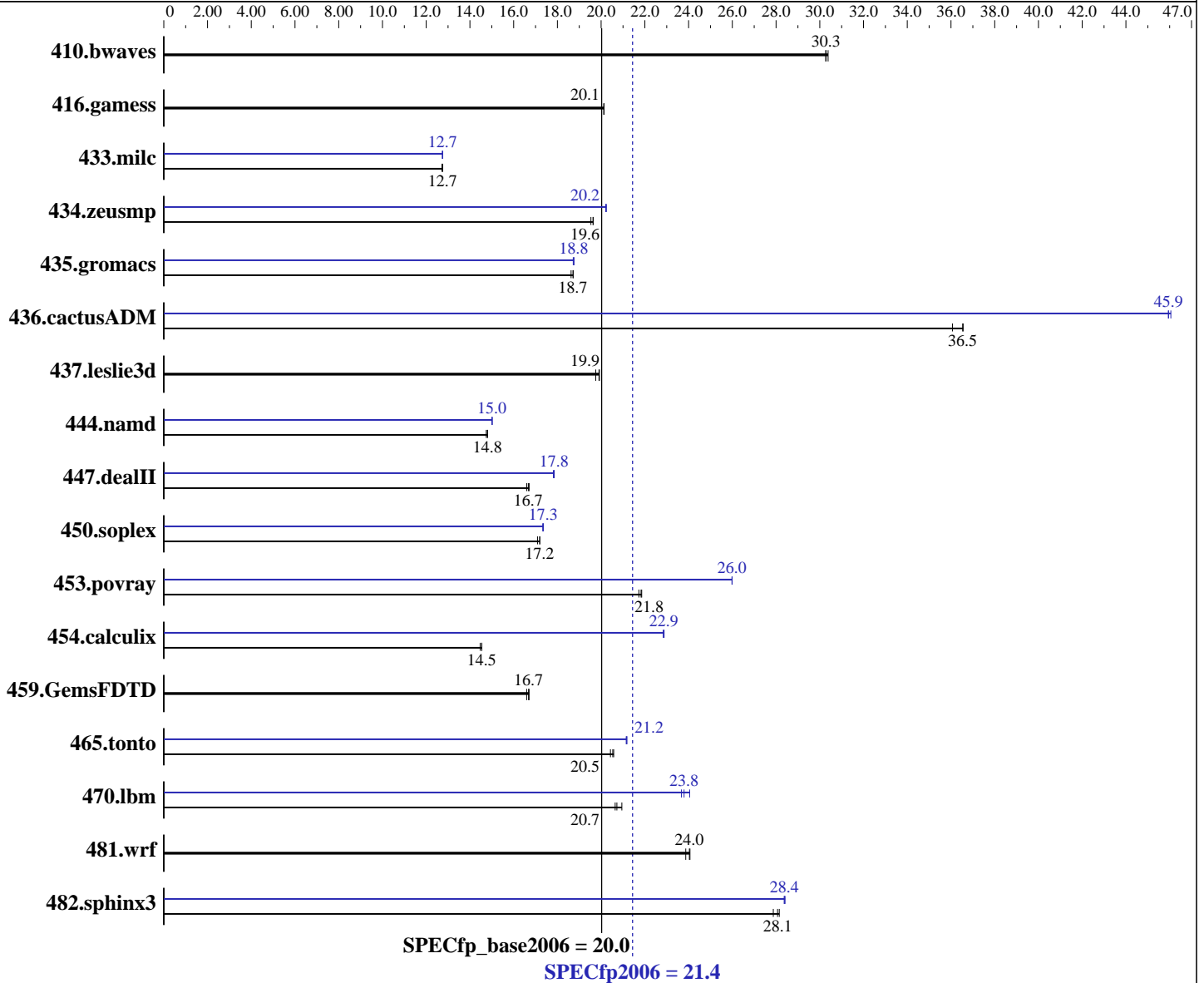
Test date: Mar-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Quad Q9550
 CPU Characteristics:
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: Windows Vista Ultimate, 64 bit Version

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Hardware (Continued)

L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM
 Other Hardware: None

Software (Continued)

Compiler: Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913
 Intel Visual Fortran Compiler for applications running on Intel 64, Version 10.1, Build 20070913
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: MicroQuill SmartHeap Library 8.0 (64 bit)

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	447	30.4	449	30.3	449	30.3	447	30.4	449	30.3	449	30.3
416.gamess	973	20.1	973	20.1	973	20.1	973	20.1	973	20.1	973	20.1
433.milc	720	12.8	720	12.7	722	12.7	721	12.7	720	12.8	720	12.7
434.zeusmp	463	19.6	464	19.6	466	19.5	450	20.2	450	20.2	450	20.2
435.gromacs	381	18.7	382	18.7	383	18.6	381	18.8	381	18.8	381	18.7
436.cactusADM	327	36.5	327	36.6	331	36.1	260	45.9	259	46.1	260	45.9
437.leslie3d	472	19.9	472	19.9	476	19.8	472	19.9	472	19.9	476	19.8
444.namd	542	14.8	542	14.8	544	14.7	534	15.0	534	15.0	534	15.0
447.dealII	686	16.7	685	16.7	689	16.6	641	17.9	642	17.8	641	17.8
450.soplex	485	17.2	485	17.2	488	17.1	481	17.3	480	17.4	481	17.3
453.povray	243	21.9	244	21.8	245	21.7	205	26.0	205	26.0	205	26.0
454.calculix	567	14.5	567	14.5	570	14.5	361	22.9	361	22.9	361	22.8
459.GemsFDTD	635	16.7	636	16.7	639	16.6	635	16.7	636	16.7	639	16.6
465.tonto	479	20.5	478	20.6	482	20.4	465	21.1	465	21.2	465	21.2
470.lbm	656	20.9	663	20.7	666	20.6	571	24.0	577	23.8	580	23.7
481.wrf	465	24.0	464	24.1	468	23.9	465	24.0	464	24.1	468	23.9
482.sphinx3	692	28.1	694	28.1	699	27.9	687	28.4	687	28.4	686	28.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.



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General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com/>

Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -Qparallel -F1000000000 libguide40.lib

C++ benchmarks:
-fast -Qparallel -Qcxx-features -F1000000000 libguide40.lib
shlW64M.lib -link -FORCE:MULTIPLE

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Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -Qparallel -F1000000000 libguide40.lib

Benchmarks using both Fortran and C:

-fast -Qparallel -F1000000000 libguide40.lib

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch -F1000000000 libguide40.lib

482.sphinx3: -fast -Qunroll2 -F1000000000 libguide40.lib

C++ benchmarks:

444.namd: -fast -Qcxx-features -Oa -F1000000000 libguide40.lib shlw64M.lib -link -FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F1000000000 libguide40.lib shlw64M.lib -link -FORCE:MULTIPLE

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -O2 -Qunroll10 -QxT -Qscalar-rep- -Qprec-div- -F1000000000 libguide40.lib

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -fast -Qunroll14 -Qauto -F1000000000 libguide40.lib

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

436.cactusADM: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel -Qprefetch -Qunroll2 -F1000000000 libguide40.lib

454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll-aggressive -F1000000000 libguide40.lib

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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