



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 24.3

CELSIUS M460, Intel Core 2 Duo E8500

SPECint_base2006 = 22.1

CPU2006 license: 22

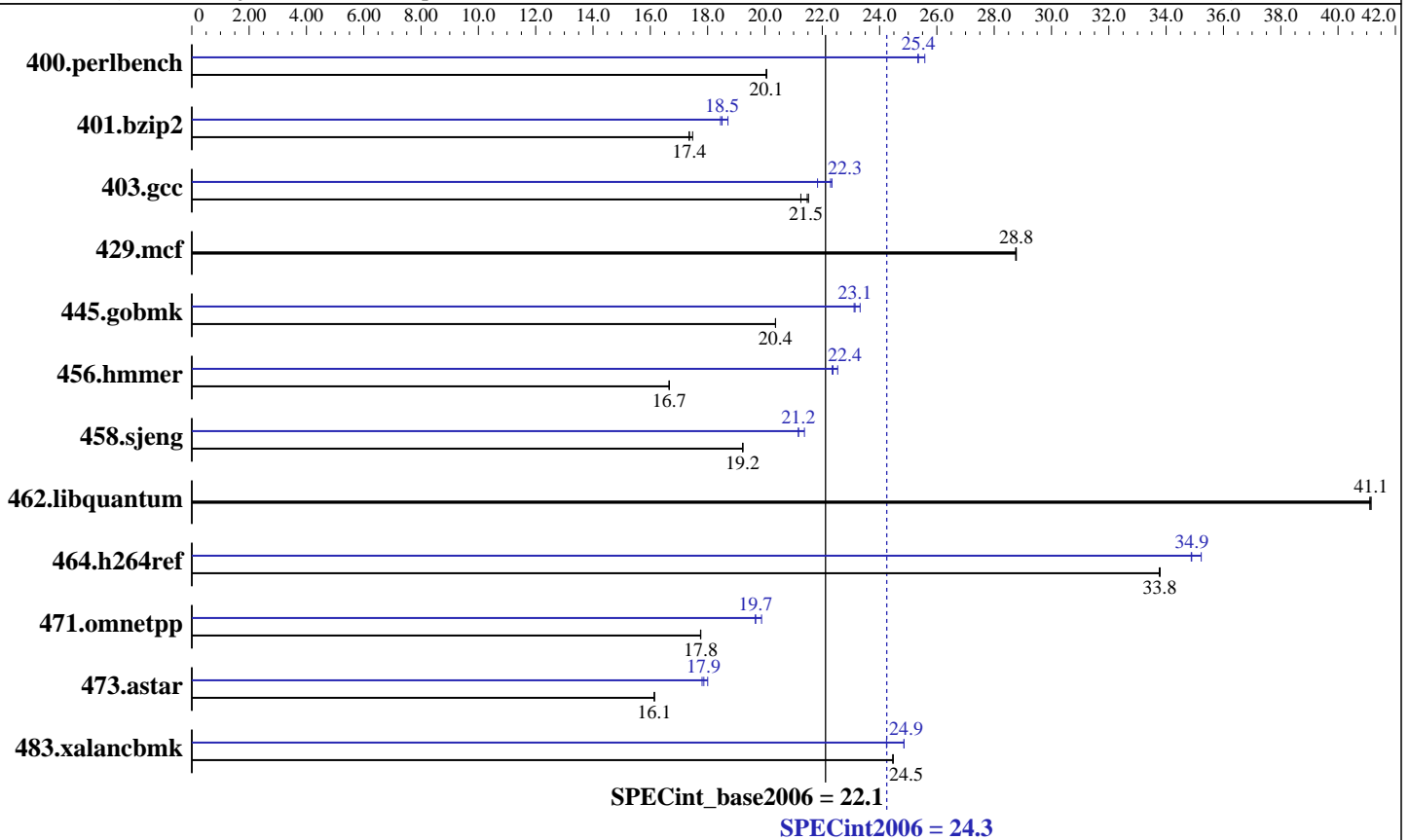
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8500
 CPU Characteristics:
 CPU MHz: 3167
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate, 64 bit Version
 Compiler: Intel C++ Compiler for applications running on IA-32, Version 10.1, Build 20070913
 Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	487	20.0	487	20.1	487	20.1	382	25.6	385	25.4	386	25.3
401.bzip2	556	17.4	556	17.4	552	17.5	516	18.7	522	18.5	523	18.5
403.gcc	375	21.5	379	21.3	374	21.5	361	22.3	369	21.8	360	22.3
429.mcf	317	28.7	317	28.8	317	28.8	317	28.7	317	28.8	317	28.8
445.gobmk	515	20.4	515	20.4	515	20.4	450	23.3	454	23.1	453	23.1
456.hmmmer	560	16.7	560	16.7	560	16.7	414	22.5	417	22.4	418	22.3
458.sjeng	629	19.2	629	19.2	629	19.2	566	21.4	572	21.2	572	21.2
462.libquantum	504	41.1	504	41.1	504	41.1	504	41.1	504	41.1	504	41.1
464.h264ref	655	33.8	656	33.8	655	33.8	628	35.2	634	34.9	635	34.9
471.omnetpp	352	17.8	352	17.8	352	17.8	314	19.9	318	19.7	318	19.7
473.astar	435	16.1	435	16.1	435	16.1	390	18.0	394	17.8	393	17.9
483.xalancbmk	282	24.5	282	24.5	282	24.5	278	24.9	285	24.2	278	24.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com/>

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8



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Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx-features -F512000000 libguide40.lib shlw32M.lib
-link -FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icl -Qvc8 -Qc99
```

```
401.bzip2: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
```

```
456.hmmr: C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
```

C++ benchmarks:

```
icl -Qvc8
```



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Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
 403.gcc: -DSPEC_CPU_WIN32
 456.hmmr: -DSPEC_CPU_P64
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel
 -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
 -F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
 libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -O2 -Qipo -QxT
 -Qprec-div- -Qansi-alias -F512000000

456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
 -Qansi-alias -Qopt-multi-version-aggressive -F512000000
 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
 -F512000000 libguide40.lib

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
 -Qansi-alias -F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
 -Qansi-alias -Qopt-ra-region-strategy=block -F512000000
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
 -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000
 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

Continued on next page



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Peak Optimization Flags (Continued)

```
483.xalanbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
              -Qansi-alias -F512000000 libguide40.lib shlW32M.lib
              -link -FORCE:MULTIPLE
```

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.02.xml

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For other inquiries, please contact webmaster@spec.org.

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