



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®_rate2006 = 33.0

CELSIUS M460, Intel Core 2 Duo E8500

SPECfp_rate_base2006 = 30.7

CPU2006 license: 22

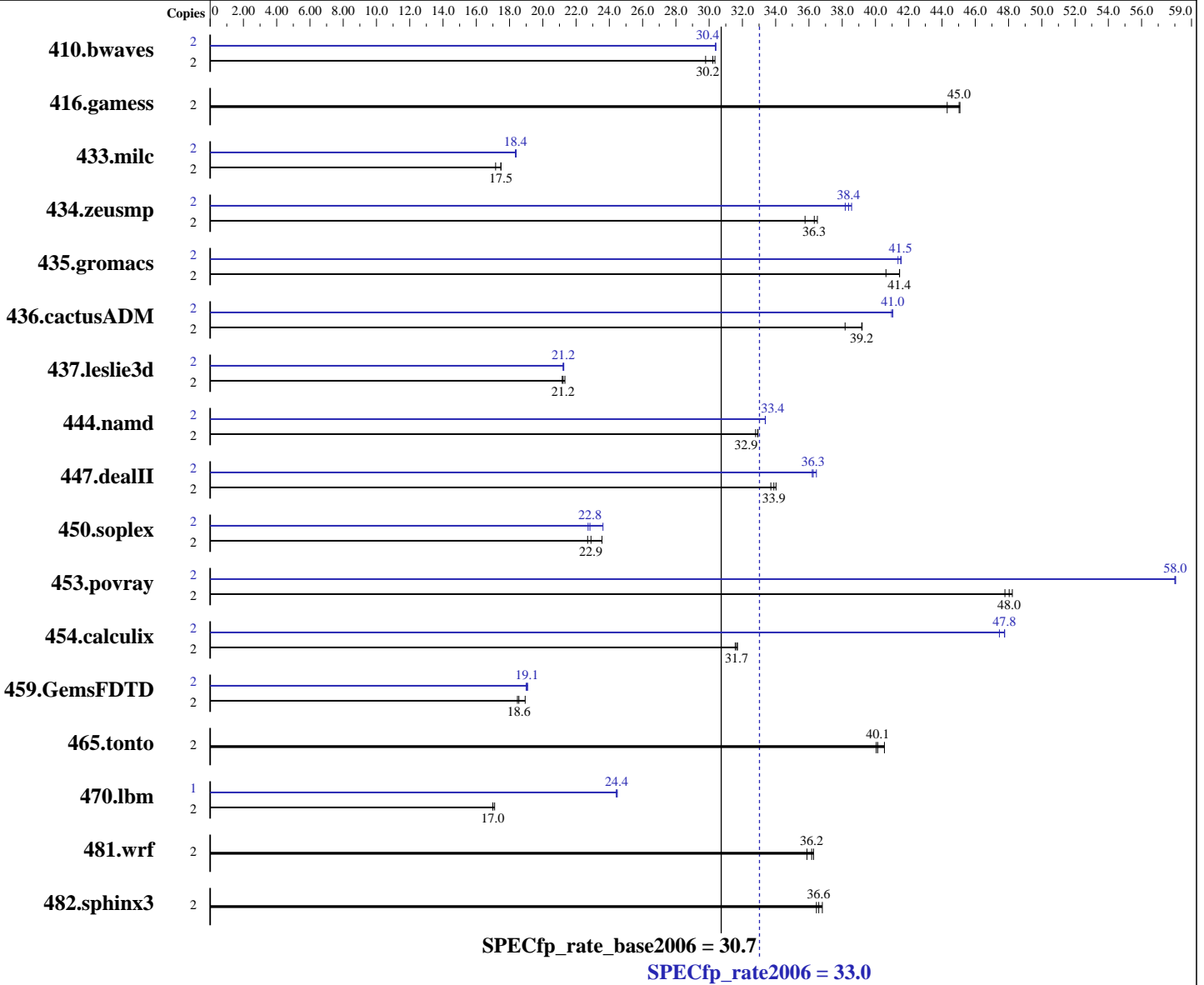
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Core 2 Duo E8500
 CPU Characteristics:
 CPU MHz: 3167
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

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Software

Operating System: Windows Vista Ultimate, 64 bit Version

Continued on next page



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Hardware (Continued)

L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
 Disk Subsystem: 1 x 400 GB SATA 7200 RPM
 Other Hardware: None

Software (Continued)

Compiler: Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913
 Intel Visual Fortran Compiler for applications running on Intel 64, Version 10.1, Build 20070913
 Microsoft Visual Studio 2005 with SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: SmartHeap Library Version 8.0 (64 bit) from <http://www.microquill.com/>

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	2	896	30.4	899	30.2	912	29.8	2	894	30.4	894	30.4	895	30.4		
416.gamess	2	869	45.1	870	45.0	884	44.3	2	869	45.1	870	45.0	884	44.3		
433.milc	2	1050	17.5	1050	17.5	1070	17.2	2	1000	18.4	999	18.4	998	18.4		
434.zeusmp	2	499	36.5	501	36.3	509	35.8	2	474	38.4	472	38.6	477	38.2		
435.gromacs	2	345	41.4	345	41.4	351	40.6	2	345	41.4	344	41.5	344	41.5		
436.cactusADM	2	610	39.2	610	39.2	626	38.2	2	583	41.0	582	41.0	582	41.0		
437.leslie3d	2	888	21.2	881	21.3	886	21.2	2	886	21.2	884	21.3	885	21.2		
444.namd	2	487	32.9	487	32.9	489	32.8	2	481	33.4	481	33.4	481	33.4		
447.dealII	2	675	33.9	673	34.0	679	33.7	2	631	36.3	632	36.2	628	36.4		
450.soplex	2	708	23.6	729	22.9	735	22.7	2	734	22.7	730	22.8	706	23.6		
453.povray	2	221	48.2	221	48.0	223	47.8	2	183	58.0	183	58.0	183	58.0		
454.calculix	2	520	31.7	523	31.6	521	31.7	2	345	47.8	345	47.8	348	47.5		
459.GemsFDTD	2	1120	18.9	1149	18.5	1143	18.6	2	1117	19.0	1114	19.1	1112	19.1		
465.tonto	2	485	40.5	490	40.1	491	40.0	2	485	40.5	490	40.1	491	40.0		
470.lbm	2	1607	17.1	1616	17.0	1617	17.0	1	561	24.5	563	24.4	562	24.4		
481.wrf	2	618	36.2	623	35.9	616	36.3	2	618	36.2	623	35.9	616	36.3		
482.sphinx3	2	1059	36.8	1069	36.5	1065	36.6	2	1059	36.8	1069	36.5	1065	36.6		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used.



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General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com/>

Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase -assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 -TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -F950000000 sh1W64M.lib

C++ benchmarks:
-fast -Qcxx-features -F950000000 sh1W64M.lib

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Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -F950000000

Benchmarks using both Fortran and C:

-fast -F950000000

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch -F950000000

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -fast -Oa -F950000000 shlW64M.lib

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F950000000

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

416.gamess: basepeak = yes

434.zeusmp: -QxT -O2 -Qprec-div- -Qunroll10 -Qscalar-rep- -F950000000

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: -fast -Qunroll12 -Ob0 -Qprefetch -F950000000

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000

436.cactusADM: -fast -Qunroll12 -Qprefetch -F950000000

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Peak Optimization Flags (Continued)

454.calculix: -fast -Qunroll-aggressive -F950000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20080124.00.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20080124.00.xml

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For other inquiries, please contact webmaster@spec.org.

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