SPEC® CINT2006 Result

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Itautec

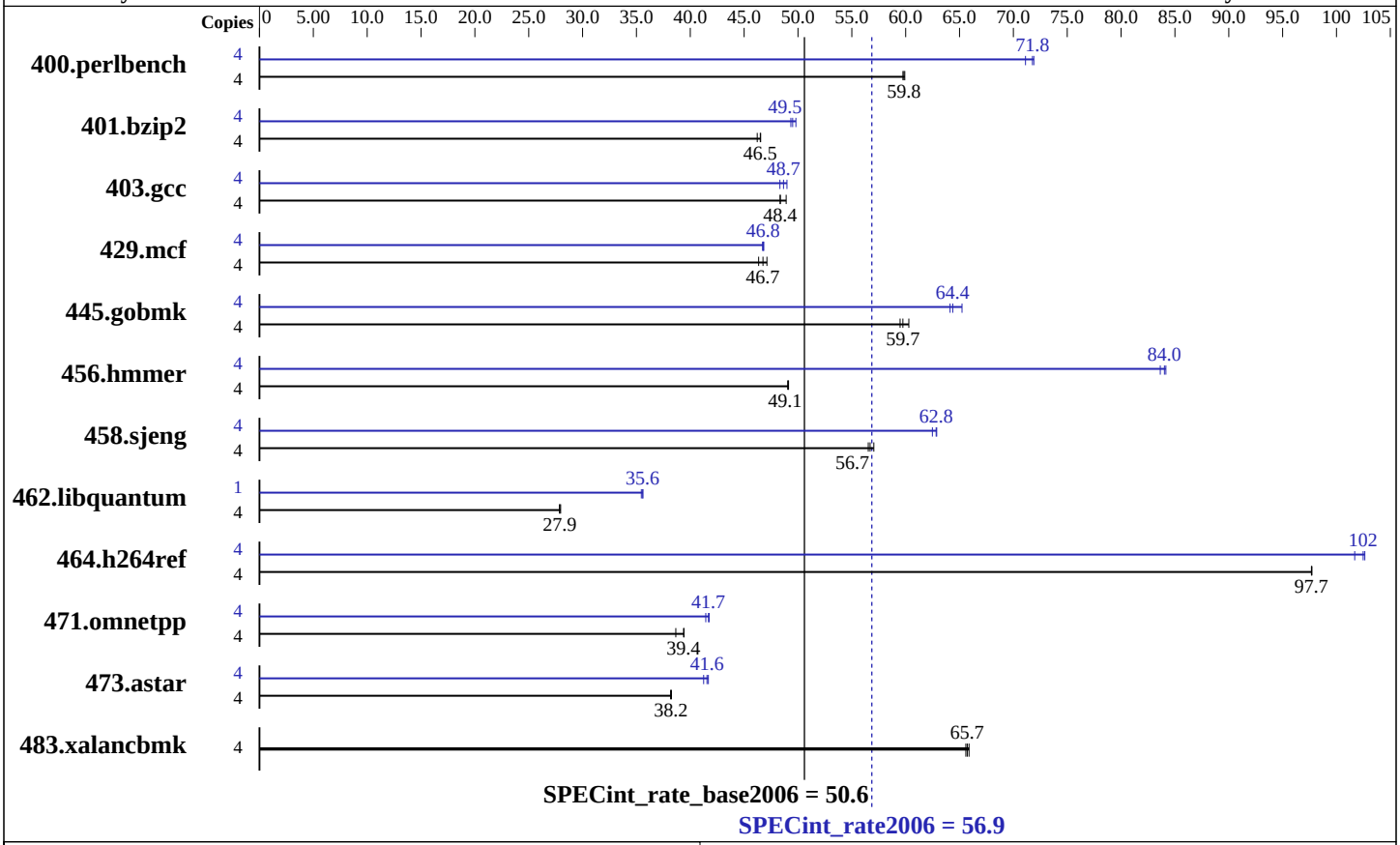
Servidor Itautec LX211 (Intel Xeon E5410)

SPECint®_rate2006 = 56.9

SPECint_rate_base2006 = 50.6

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2008
Hardware Availability: Dec-2007
Software Availability: Jan-2008



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem:	1 x SCSI, 73GB, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run Level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.10.50 MicroQuill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	654	59.7	652	59.9	**653**	**59.8**	4	544	71.9	549	71.1	**545**	**71.8**
401.bzip2	4	829	46.5	835	46.2	**830**	**46.5**	4	782	49.3	**780**	**49.5**	775	49.8
403.gcc	4	666	48.3	658	48.9	**666**	**48.4**	4	657	49.0	**662**	**48.7**	666	48.3
429.mcf	4	774	47.1	**780**	**46.7**	787	46.3	4	781	46.7	**780**	**46.8**	779	46.8
445.gobmk	4	**702**	**59.7**	705	59.5	696	60.3	4	654	64.1	643	65.2	**652**	**64.4**
456.hmmer	4	761	49.1	760	49.1	**760**	**49.1**	4	**444**	**84.0**	446	83.6	443	84.2
458.sjeng	4	**854**	**56.7**	856	56.5	848	57.0	4	**770**	**62.8**	775	62.5	770	62.9
462.libquantum	4	2976	27.9	2964	28.0	**2972**	**27.9**	1	**582**	**35.6**	582	35.6	584	35.5
464.h264ref	4	906	97.7	906	97.7	**906**	**97.7**	4	870	102	**864**	**102**	863	103
471.omnetpp	4	634	39.4	647	38.7	**635**	**39.4**	4	603	41.4	**600**	**41.7**	599	41.8
473.astar	4	734	38.3	735	38.2	**735**	**38.2**	4	674	41.7	**676**	**41.6**	681	41.2
483.xalancbmk	4	421	65.6	419	65.9	**420**	**65.7**	4	421	65.6	419	65.9	**420**	**65.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page

Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic10.1-INT-intel64-linux-flags.xml`

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