SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

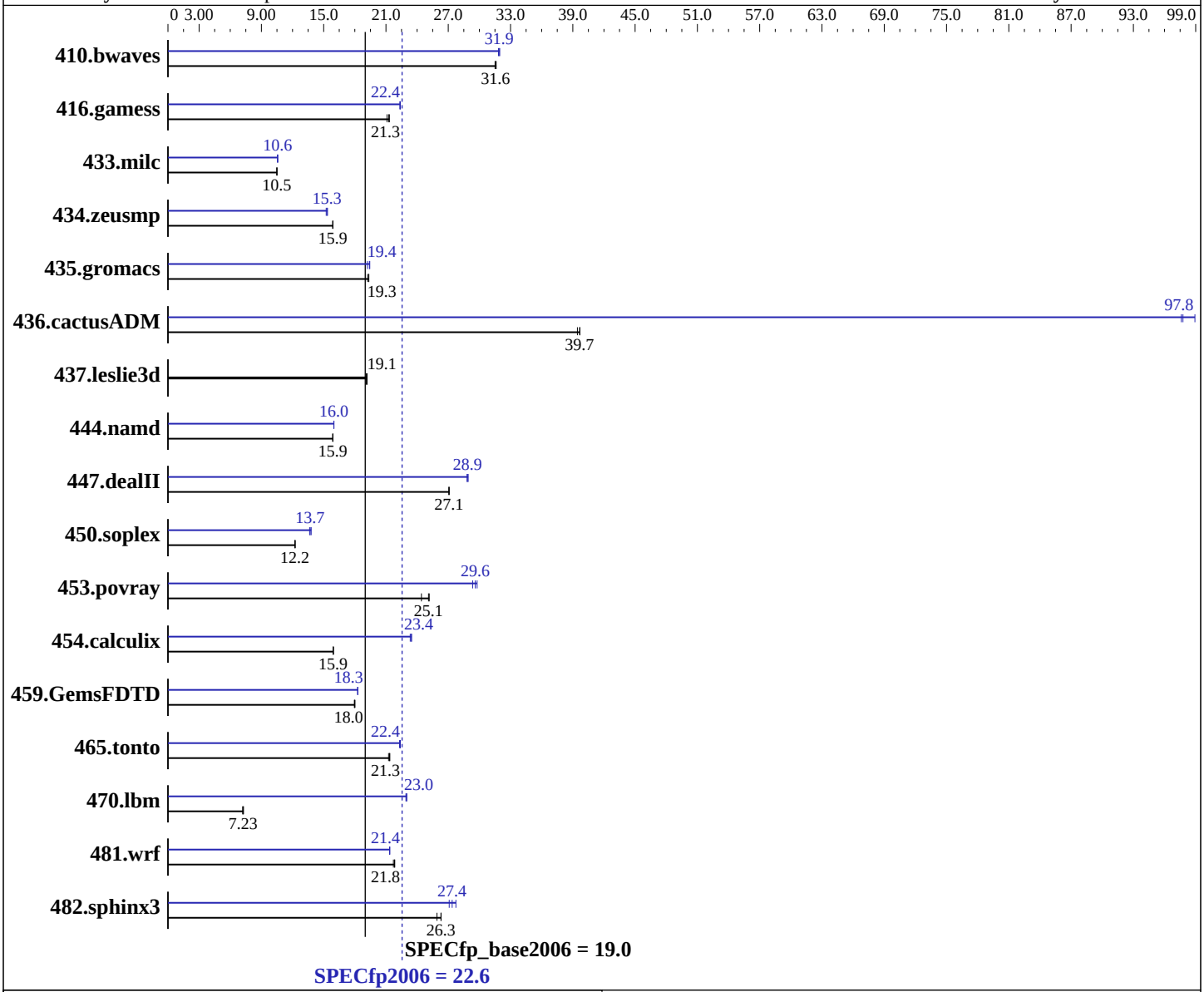
Express5800/120Lj
(Intel Xeon X5450)

SPECfp®2006 = 22.6

SPECfp_base2006 = 19.0

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Mar-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5450
CPU Characteristics:	3.00 GHz, 2x6 MB L2 shared, 1333 MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Lj
(Intel Xeon X5450)

SPECfp2006 = 22.6

SPECfp_base2006 = 19.0

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Mar-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: binutils-2.17.tar.gz, Version 2.17

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	431	31.5	430	31.6	**431**	**31.6**	**426**	**31.9**	425	32.0	427	31.8
416.gamess	**921**	**21.3**	918	21.3	928	21.1	**876**	**22.4**	874	22.4	876	22.4
433.milc	**874**	**10.5**	874	10.5	876	10.5	870	10.6	**869**	**10.6**	869	10.6
434.zeusmp	573	15.9	**573**	**15.9**	573	15.9	**594**	**15.3**	592	15.4	597	15.2
435.gromacs	**369**	**19.3**	369	19.3	371	19.2	367	19.4	372	19.2	**368**	**19.4**
436.cactusADM	**301**	**39.7**	303	39.4	301	39.7	122	97.6	121	98.9	**122**	**97.8**
437.leslie3d	**491**	**19.1**	490	19.2	492	19.1	**491**	**19.1**	490	19.2	492	19.1
444.namd	505	15.9	**505**	**15.9**	505	15.9	502	16.0	**502**	**16.0**	502	16.0
447.dealII	**423**	**27.1**	422	27.1	423	27.1	**396**	**28.9**	397	28.8	396	28.9
450.soplex	682	12.2	**682**	**12.2**	680	12.3	611	13.6	**609**	**13.7**	604	13.8
453.povray	218	24.4	211	25.2	**212**	**25.1**	**180**	**29.6**	179	29.8	181	29.4
454.calculix	517	15.9	519	15.9	**518**	**15.9**	354	23.3	**352**	**23.4**	352	23.5
459.GemsFDTD	591	18.0	589	18.0	**590**	**18.0**	580	18.3	581	18.3	**580**	**18.3**
465.tonto	463	21.3	460	21.4	**461**	**21.3**	440	22.4	**440**	**22.4**	441	22.3
470.lbm	1888	7.28	**1901**	**7.23**	1909	7.20	597	23.0	**598**	**23.0**	599	22.9
481.wrf	514	21.7	**512**	**21.8**	511	21.8	523	21.4	523	21.3	**523**	**21.4**
482.sphinx3	752	25.9	741	26.3	**741**	**26.3**	**712**	**27.4**	719	27.1	703	27.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Intel SpeedStep Technology: Disabled
```

General Notes

```
All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

Fortran benchmarks:
-fast -parallel

Benchmarks using both Fortran and C:
-fast -parallel

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: `-fast -unroll2`

C++ benchmarks:

444.namd: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

447.dealII: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias -scalar-rep-`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-fast -prefetch -parallel`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -Ob0 -prefetch -parallel`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -prefetch -parallel -auto-ilp32`

454.calculix: `-fast -unroll-aggressive -auto-ilp32`

481.wrf: `-fast -parallel -prefetch -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:34:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 15 April 2008.