SPEC® CINT2006 Result

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Bull SAS

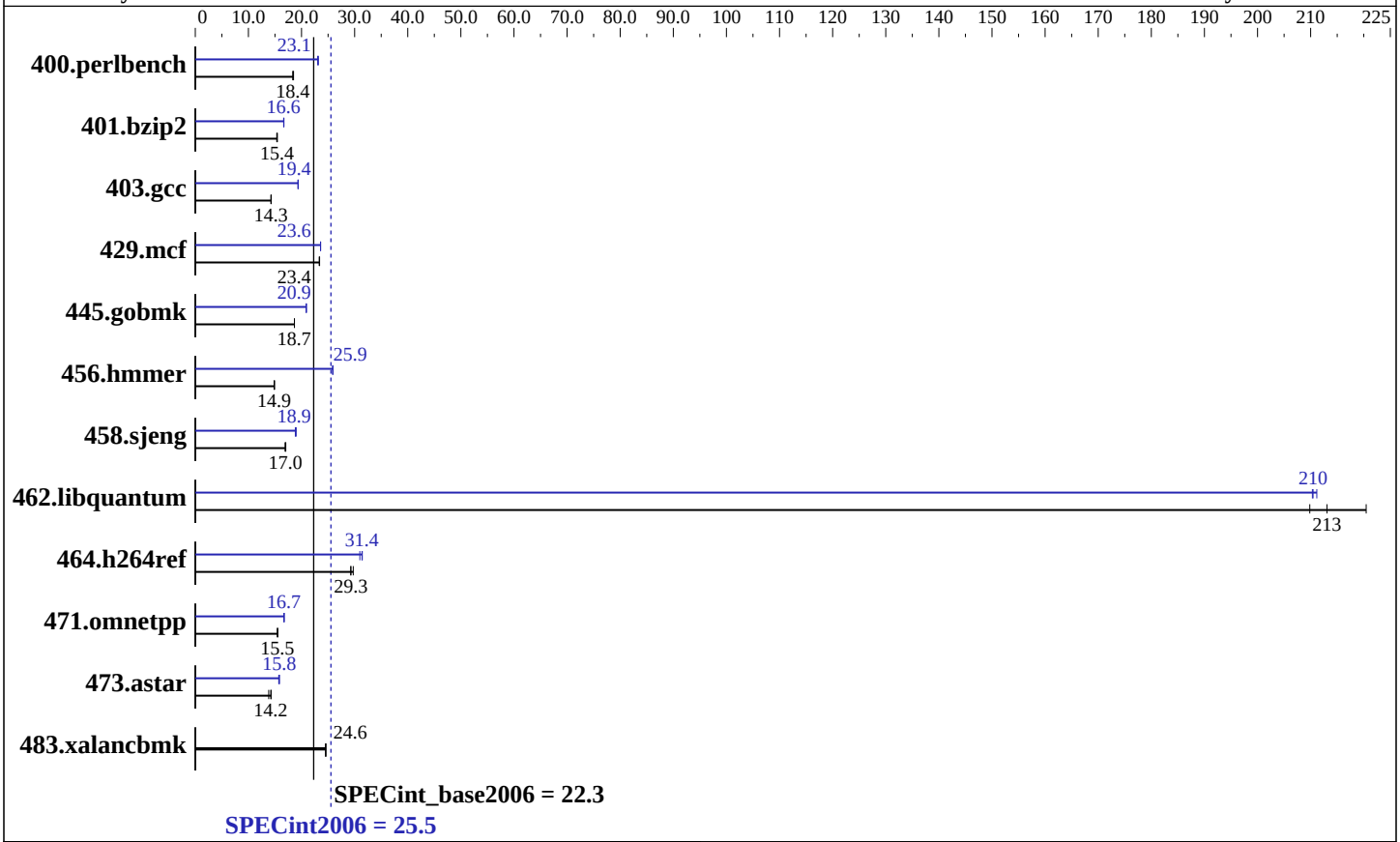
NovaScale R460 E1
(Intel Xeon E5440,2.83GHz)

SPECint®2006 = 25.5

SPECint_base2006 = 22.3

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Mar-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5440
CPU Characteristics:	2.83 GHz, 12 MB L2, 1333 MHz system bus
CPU MHz:	2833
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem:	1x73 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 Kernel 2.6.16.46-0.12-smp for x86_64
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.17.50.0.15 SmartHeap library V8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	528	18.5	**531**	**18.4**	532	18.4	424	23.1	**423**	**23.1**	422	23.2
401.bzip2	629	15.3	625	15.4	**627**	**15.4**	578	16.7	**580**	**16.6**	580	16.6
403.gcc	565	14.3	563	14.3	**565**	**14.3**	416	19.4	416	19.3	**416**	**19.4**
429.mcf	**390**	**23.4**	391	23.4	390	23.4	387	23.6	387	23.6	**387**	**23.6**
445.gobmk	**562**	**18.7**	562	18.7	562	18.7	502	20.9	503	20.9	**502**	**20.9**
456.hmmer	**627**	**14.9**	627	14.9	627	14.9	360	25.9	360	25.9	**360**	**25.9**
458.sjeng	715	16.9	**713**	**17.0**	710	17.0	642	18.8	637	19.0	**640**	**18.9**
462.libquantum	**97.2**	**213**	98.7	210	94.0	220	98.5	210	98.1	211	**98.5**	**210**
464.h264ref	756	29.3	744	29.7	**755**	**29.3**	704	31.4	**705**	**31.4**	714	31.0
471.omnetpp	**403**	**15.5**	405	15.5	403	15.5	374	16.7	374	16.7	**374**	**16.7**
473.astar	**494**	**14.2**	492	14.3	508	13.8	**444**	**15.8**	447	15.7	444	15.8
483.xalancbmk	281	24.5	**281**	**24.6**	281	24.6	281	24.5	**281**	**24.6**	281	24.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
OMP_NUM_THREADS set to number of cores (default)
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
The taskset utility was used to bind processes to cores

 The Bull NovaScale R440 E1 (Intel Xeon E5440,2.83GHz) and
 the Bull NovaScale R460 E1 (Intel Xeon E5440,2.83GHz) models are electronically equivalent.
 The results have been measured on a Bull NovaScale R460 E1 (Intel Xeon E5440,2.83GHz) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.03.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.03.xml`

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