# SPEC® CINT2006 Result



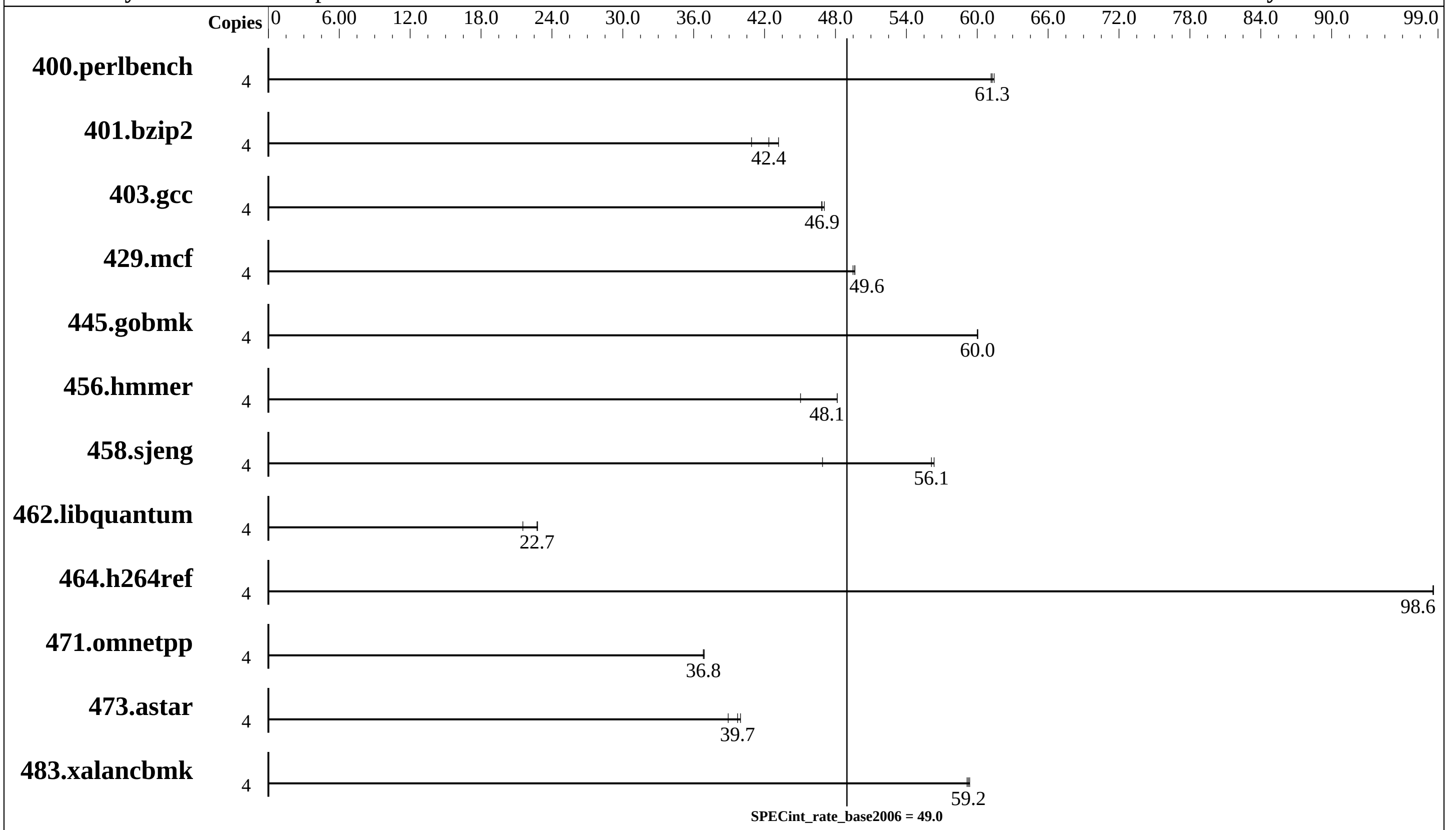
## IBM Corporation

**SPECint®_rate2006 = Not Run**

IBM System x3850 M2 (Intel Xeon E7210)

**SPECint_rate_base2006 = 49.0**

| | | | |
|---|---|---|---|
| **CPU2006 license:** | 11 | **Test date:** | Mar-2008 |
| **Test sponsor:** | IBM Corporation | **Hardware Availability:** | Nov-2007 |
| **Tested by:** | IBM Corporation | **Software Availability:** | Nov-2007 |




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7210 |
| CPU Characteristics: | Dual Core, 2.4 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1,2,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 4 MB I+D on chip per core |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 64 GB (32 x 2 GB DDR2-5300 ECC) |
| Disk Subsystem: | 1 x 146 GB SAS, 10000 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp |
| Compiler: | Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Multi-user, run level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | Not Applicable |
| Other Software: | MicroQuill SmartHeap 8.1<br>Binutils 2.17.50.0.15 |

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## Results Table

| | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **Benchmark** | **Copies** | **Seconds** | **Ratio** | **Seconds** | **Ratio** | **Seconds** | **Ratio** | **Copies** | **Seconds** | **Ratio** | **Seconds** | **Ratio** | **Seconds** | **Ratio** |
| 400.perlbench | 4 | **638** | **61.3** | 636 | 61.4 | 639 | 61.2 | | | | | | | |
| 401.bzip2 | 4 | 894 | 43.2 | **911** | **42.4** | 944 | 40.9 | | | | | | | |
| 403.gcc | 4 | 688 | 46.8 | **687** | **46.9** | 684 | 47.1 | | | | | | | |
| 429.mcf | 4 | 737 | 49.5 | 735 | 49.6 | **735** | **49.6** | | | | | | | |
| 445.gobmk | 4 | 699 | 60.0 | **699** | **60.0** | 699 | 60.1 | | | | | | | |
| 456.hmmer | 4 | 828 | 45.0 | **775** | **48.1** | 775 | 48.2 | | | | | | | |
| 458.sjeng | 4 | 1032 | 46.9 | **862** | **56.1** | 859 | 56.3 | | | | | | | |
| 462.libquantum | 4 | 3848 | 21.5 | **3645** | **22.7** | 3637 | 22.8 | | | | | | | |
| 464.h264ref | 4 | 898 | 98.6 | **898** | **98.6** | 898 | 98.6 | | | | | | | |
| 471.omnetpp | 4 | 679 | 36.8 | 678 | 36.9 | **679** | **36.8** | | | | | | | |
| 473.astar | 4 | **707** | **39.7** | 721 | 38.9 | 703 | 40.0 | | | | | | | |
| 483.xalancbmk | 4 | 467 | 59.1 | **466** | **59.2** | 465 | 59.4 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
All benchmarks compiled in 32-bit mode
taskset utility used to bind CPU(s) processes
```

## Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
`-fast -inline-calloc -opt-malloc-options=3`

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.xml



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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