SPEC® CINT2006 Result

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IBM Corporation

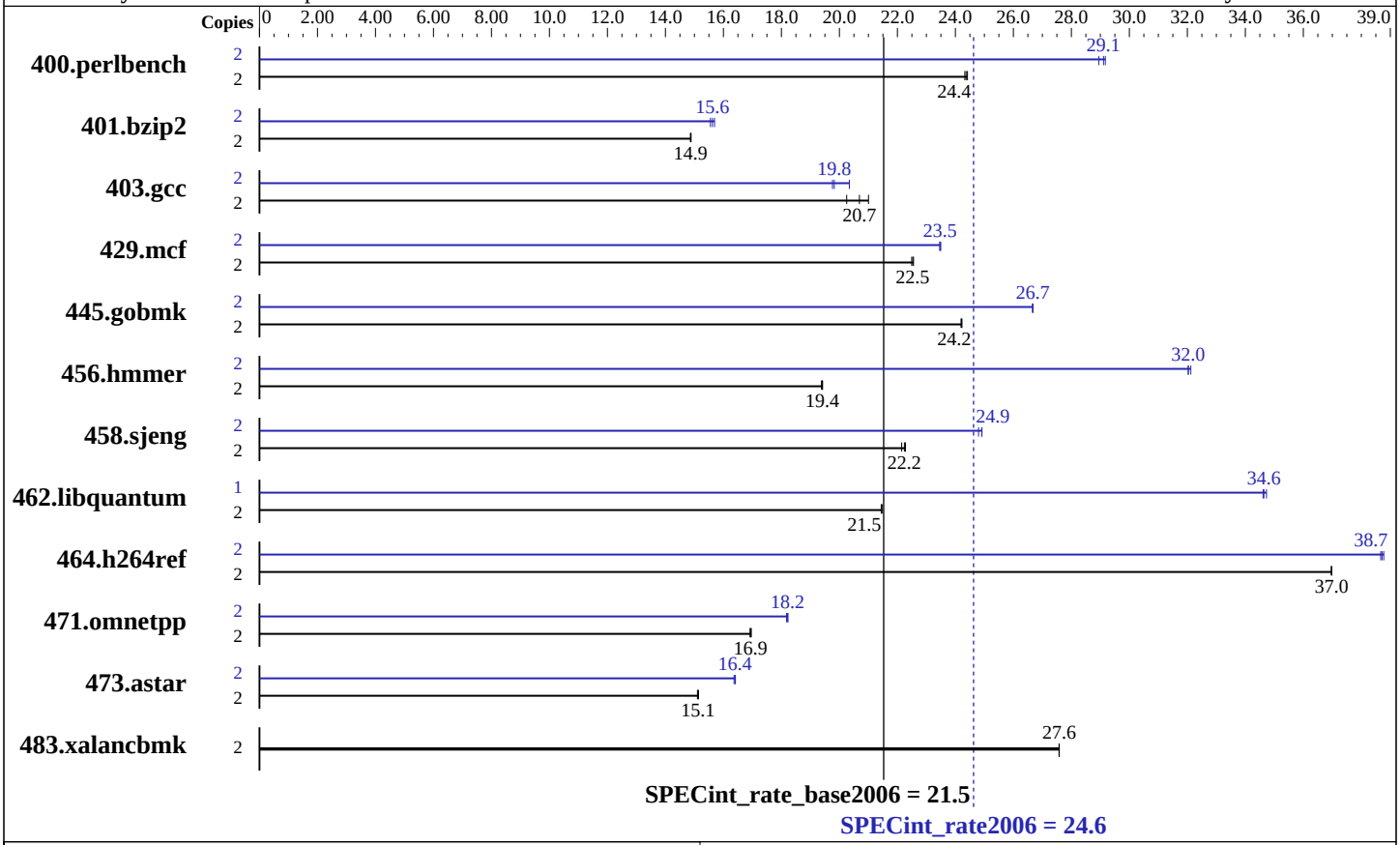
IBM BladeCenter HS12 (Intel Core 2 Duo E6305)

SPECint®_rate2006 = 24.6

SPECint_rate_base2006 = 21.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Apr-2008
Hardware Availability: May-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E6305
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2 GB DDR2-5300 ECC)
Disk Subsystem:	1 x 73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	803	24.3	800	24.4	**802**	**24.4**	2	670	29.2	**671**	**29.1**	675	28.9
401.bzip2	2	**1298**	**14.9**	1297	14.9	1299	14.9	2	1230	15.7	**1236**	**15.6**	1241	15.6
403.gcc	2	795	20.3	767	21.0	**778**	**20.7**	2	815	19.8	791	20.3	**812**	**19.8**
429.mcf	2	**809**	**22.5**	811	22.5	809	22.6	2	776	23.5	777	23.5	**777**	**23.5**
445.gobmk	2	**866**	**24.2**	866	24.2	867	24.2	2	**787**	**26.7**	787	26.7	786	26.7
456.hmmer	2	963	19.4	**961**	**19.4**	961	19.4	2	581	32.1	**582**	**32.0**	583	32.0
458.sjeng	2	**1088**	**22.2**	1086	22.3	1093	22.1	2	**971**	**24.9**	971	24.9	976	24.8
462.libquantum	2	1933	21.4	**1931**	**21.5**	1930	21.5	1	599	34.6	597	34.7	**598**	**34.6**
464.h264ref	2	1197	37.0	1197	37.0	**1197**	**37.0**	2	1145	38.7	1141	38.8	**1143**	**38.7**
471.omnetpp	2	739	16.9	**738**	**16.9**	737	17.0	2	**686**	**18.2**	686	18.2	688	18.2
473.astar	2	**929**	**15.1**	930	15.1	927	15.1	2	855	16.4	858	16.4	**856**	**16.4**
483.xalancbmk	2	500	27.6	**500**	**27.6**	500	27.6	2	500	27.6	**500**	**27.6**	500	27.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
taskset utility used to bind CPU(s) to processes
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

456.hmmer:
```
/opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml`

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