SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

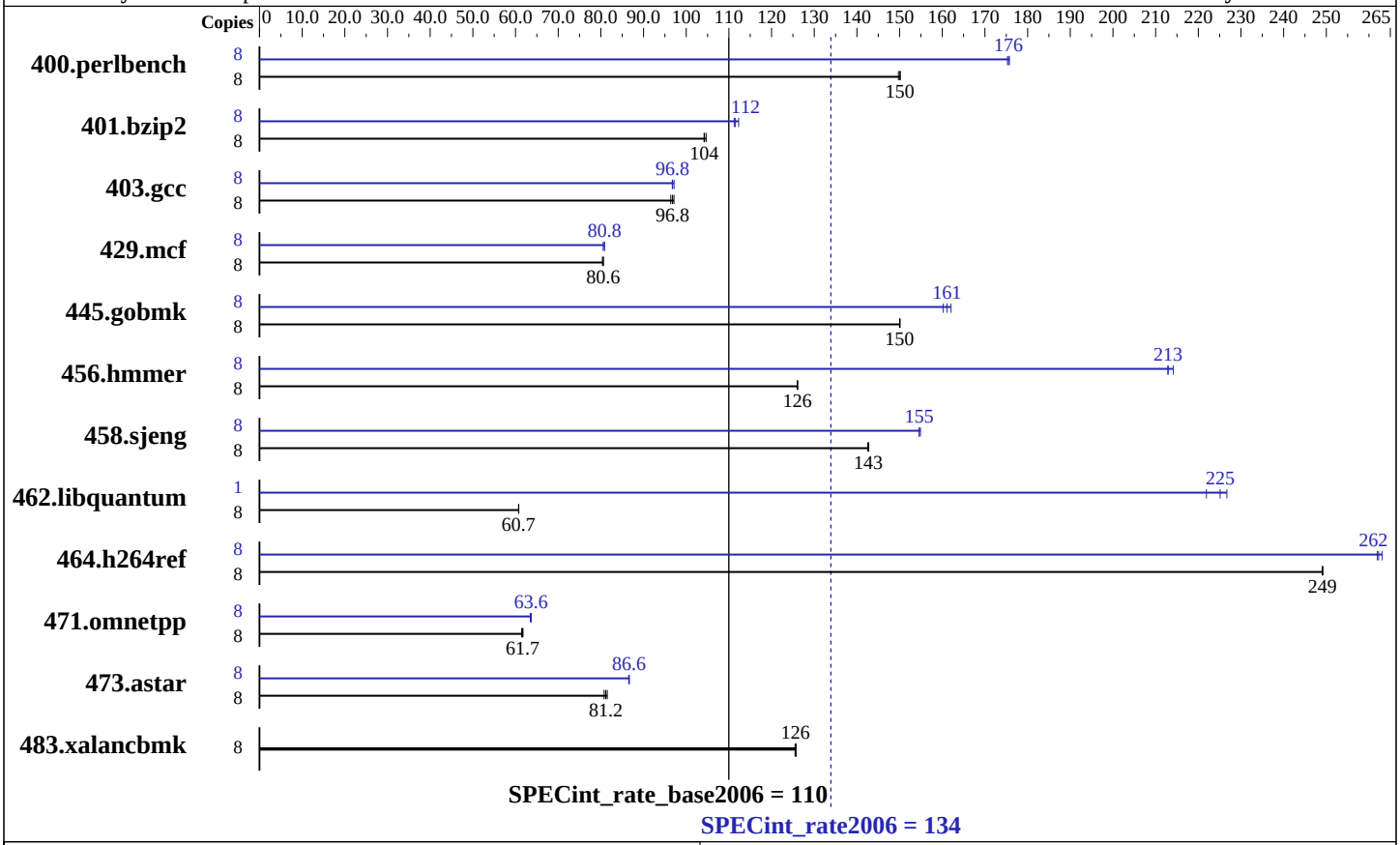
Supermicro Motherboard X7DWT

SPECint®_rate2006 = 134

SPECint_rate_base2006 = 110

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5450
CPU Characteristics:	Quad Core, 3.00 GHz, 1333 MHz System Bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 X 2 GB ECC PC2-5300, CL5, FBDIMM)
Disk Subsystem:	SAMSUNG HD501LJ 500 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	64-Bit SUSE Linux Enterprise Server 10 SP1
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap Library 8.1 Binutils 2.17.50.0.15

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro Motherboard X7DWT

SPECint_rate2006 = 134

SPECint_rate_base2006 = 110

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	521	150	**521**	**150**	522	150	8	**445**	**176**	446	175	445	176
401.bzip2	8	738	105	**740**	**104**	741	104	8	**692**	**112**	694	111	688	112
403.gcc	8	**665**	**96.8**	668	96.4	663	97.1	8	**665**	**96.8**	663	97.1	666	96.7
429.mcf	8	905	80.6	**906**	**80.6**	908	80.4	8	902	80.8	906	80.5	**902**	**80.8**
445.gobmk	8	559	150	**559**	**150**	559	150	8	518	162	**521**	**161**	524	160
456.hmmer	8	**592**	**126**	592	126	592	126	8	351	213	349	214	**351**	**213**
458.sjeng	8	**678**	**143**	678	143	679	143	8	625	155	626	155	**626**	**155**
462.libquantum	8	**2729**	**60.7**	2728	60.8	2730	60.7	1	93.4	222	91.4	227	**92.0**	**225**
464.h264ref	8	711	249	**711**	**249**	711	249	8	**675**	**262**	673	263	676	262
471.omnetpp	8	**811**	**61.7**	810	61.7	814	61.4	8	788	63.5	**786**	**63.6**	785	63.7
473.astar	8	695	80.8	689	81.5	**692**	**81.2**	8	649	86.5	**648**	**86.6**	648	86.6
483.xalancbmk	8	439	126	440	126	**439**	**126**	8	439	126	440	126	**439**	**126**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, which are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

456.hmmer:
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090713.00.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.1.
Report generated on Tue Jul 22 17:04:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 May 2008.