SPEC® CINT2006 Result

Fujitsu Siemens Computers

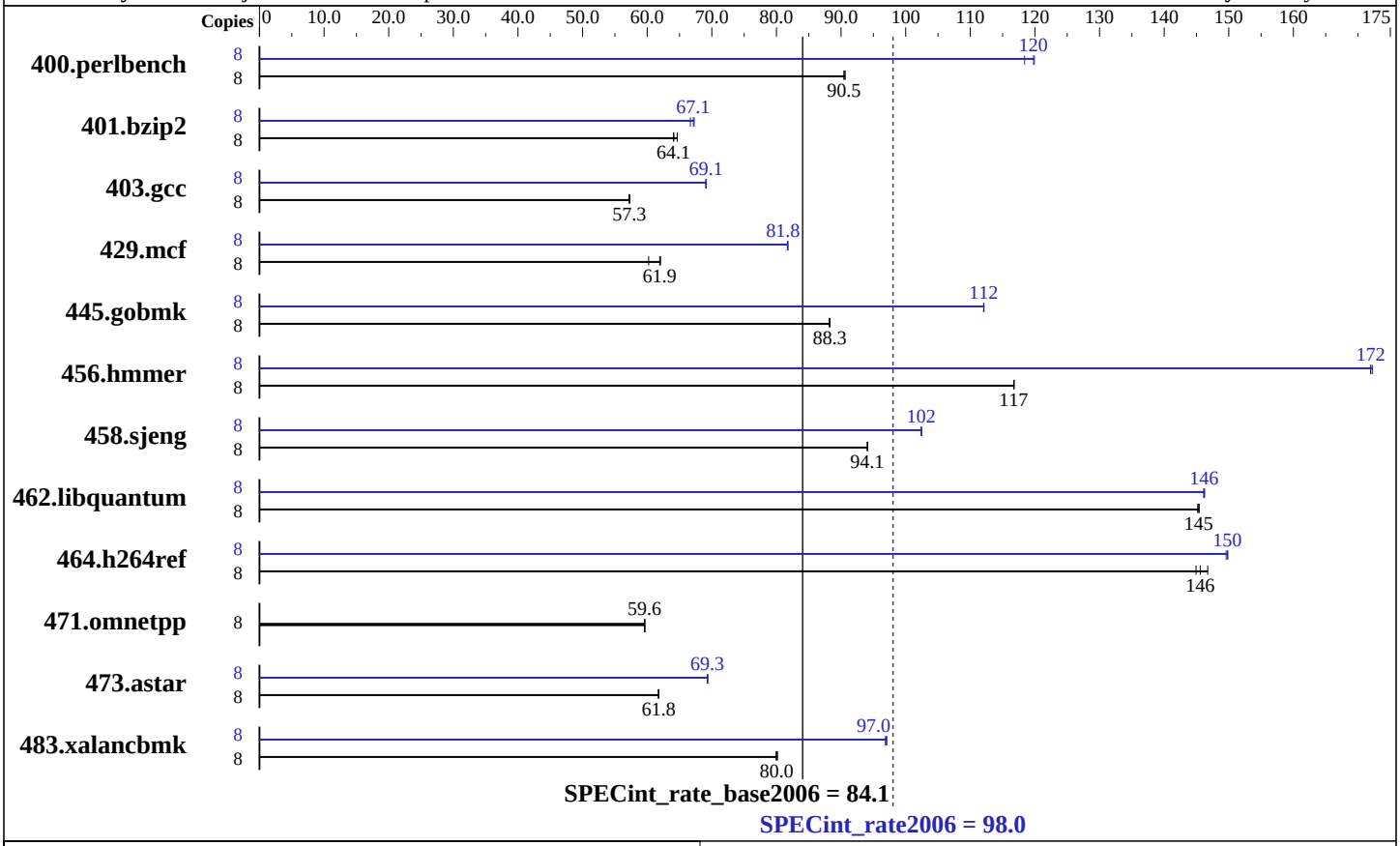
CELSIUS V840, AMD Opteron 2354 (2.2 GHz)

SPECint®_rate2006 = 98.0

SPECint_rate_base2006 = 84.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2008
Hardware Availability: May-2008
Software Availability: May-2008



Hardware

CPU Name:	AMD Opteron 2354
CPU Characteristics:	
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (8x2GB PC2-5300P, CL5, dual rank ECC)
Disk Subsystem:	1 x 400 GB SATA II, 7200 rpm
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	PGI Workstation Complete Version 7.2-1 PathScale Compiler Suite, Release 3.2 Beta
Auto Parallel:	No
File System:	ext3
System State:	Multi-User SuSE Run Level 3
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	binutils 2.18.50 Microquill SmartHeap 8.1 32-bit Library for Linux

SPEC CINT2006 Result

Fujitsu Siemens Computers

CELSIUS V840, AMD Opteron 2354 (2.2 GHz)

SPECint_rate2006 = 98.0

SPECint_rate_base2006 = 84.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2008
Hardware Availability: May-2008
Software Availability: May-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**864**	**90.5**	862	90.6	864	90.5	8	660	118	652	120	**652**	**120**
401.bzip2	8	1194	64.6	1205	64.1	**1204**	**64.1**	8	1158	66.7	**1150**	**67.1**	1147	67.3
403.gcc	8	**1124**	**57.3**	1126	57.2	1124	57.3	8	931	69.2	**932**	**69.1**	933	69.0
429.mcf	8	1211	60.2	**1178**	**61.9**	1176	62.1	8	893	81.7	**892**	**81.8**	892	81.8
445.gobmk	8	**951**	**88.3**	952	88.2	951	88.3	8	749	112	748	112	**749**	**112**
456.hmmer	8	639	117	639	117	**639**	**117**	8	**434**	**172**	433	172	434	172
458.sjeng	8	**1029**	**94.1**	1029	94.1	1029	94.1	8	945	102	**945**	**102**	945	102
462.libquantum	8	1142	145	1140	145	**1141**	**145**	8	1134	146	**1134**	**146**	1133	146
464.h264ref	8	1221	145	**1216**	**146**	1206	147	8	1183	150	**1182**	**150**	1181	150
471.omnetpp	8	839	59.6	838	59.6	**839**	**59.6**	8	839	59.6	838	59.6	**839**	**59.6**
473.astar	8	910	61.7	909	61.8	**909**	**61.8**	8	**810**	**69.3**	810	69.3	809	69.4
483.xalancbmk	8	689	80.2	**690**	**80.0**	690	80.0	8	570	96.9	**569**	**97.0**	569	97.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
powersave -f is applied to set CPU to maximum frequency prior to run
stacksize is set to unlimited prior to run
ulimit -l 2457600
PGI_HUGE_PAGES set to 150
(Total number of huge pages available is 1200)
```

General Notes

```
The command numactl has been used to bind processes to CPUs

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
pgcc

C++ benchmarks:
pgcpp

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fastsse -Msmartalloc=huge:150 -Mfprelaxed -Mipa=jobs:4 -Mipa=fast
-Mipa=inline -tp barcelona-64 -Bstatic_pgi

C++ benchmarks:
-fastsse -Msmartalloc=huge:150 -Mfprelaxed --zc_eh -Mipa=jobs:4
-Mipa=fast -Mipa=inline -tp barcelona -Bstatic_pgi

Peak Compiler Invocation

C benchmarks (except as noted below):
pgcc

400.perlbench: pathcc

403.gcc: pathcc

445.gobmk: pathcc

464.h264ref: pathcc

C++ benchmarks (except as noted below):
pgcpp

483.xalancbmk: pathCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
-WOPT:if_conv=0 -CG:local_sched_alg=1

401.bzip2: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2) -fastsse -O4
-Msmartalloc=huge:150 -Mprefetch=t0 -Mnounroll
-tp barcelona-64 -Bstatic_pgi

403.gcc: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:Ofast -m32

429.mcf: -fastsse -Msmartalloc=huge:150 -Mipa=jobs:4 -Mipa=fast
-Mipa=inline:1 -tp barcelona -Bstatic_pgi

445.gobmk: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
-LNO:prefetch=1 -LNO:ignore_feedback=off -CG:p2align=on

456.hmmer: -fastsse -Mvect=partial -Munroll=n:8 -Msmartalloc=huge:150
-Msafeptr -Mprefetch=t0 -Mfprelaxed -Mipa=jobs:4
-Mipa=const -Mipa=ptr -Mipa=arg -Mipa=inline
-tp barcelona-64 -Bstatic_pgi

458.sjeng: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=jobs:4(pass 2)
-Mipa=fast(pass 2) -Mipa=inline:1(pass 2)
-Mipa=noarg(pass 2) -fastsse -Msmartalloc=huge:150
-Mfprelaxed -tp barcelona-64 -Bstatic_pgi

462.libquantum: -fastsse -Munroll=m:8 -Msmartalloc=huge:150
-Mprefetch=distance:4 -Mfprelaxed -Mipa=jobs:4 -Mipa=fast
-Mipa=inline -Mipa=noarg -tp barcelona-64 -Bstatic_pgi

464.h264ref: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
-CG:push_pop_int_saved_regs=off

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=jobs:4(pass 2) -Mipa=fast(pass 2) -Mipa=inline:6(pass 2) -fastsse -O4 -Msmartalloc=huge:150 -Msafeptr=global -Mfprelaxed --zc_eh -tp barcelona -Bstatic_pgi

483.xalancbmk: -march=barcelona -Ofast -OPT:unroll_times_max=8 -CG:push_pop_int_saved_regs=off -CG:ptr_load_use=0 -m32 -lsmartheap

Peak Other Flags

C++ benchmarks:

483.xalancbmk: -L/opt/SmartHeap_8.1/lib

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/fsc-mix-pgi-path.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/fsc-mix-pgi-path.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Mon Jul 13 17:57:27 2009 by SPEC CPU2006 PS/PDF formatter v6323.