



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 24.8

Dell Precision R5400 (Intel Xeon X5260, 3.33 GHz)

SPECint\_base2006 = 21.7

CPU2006 license: 55

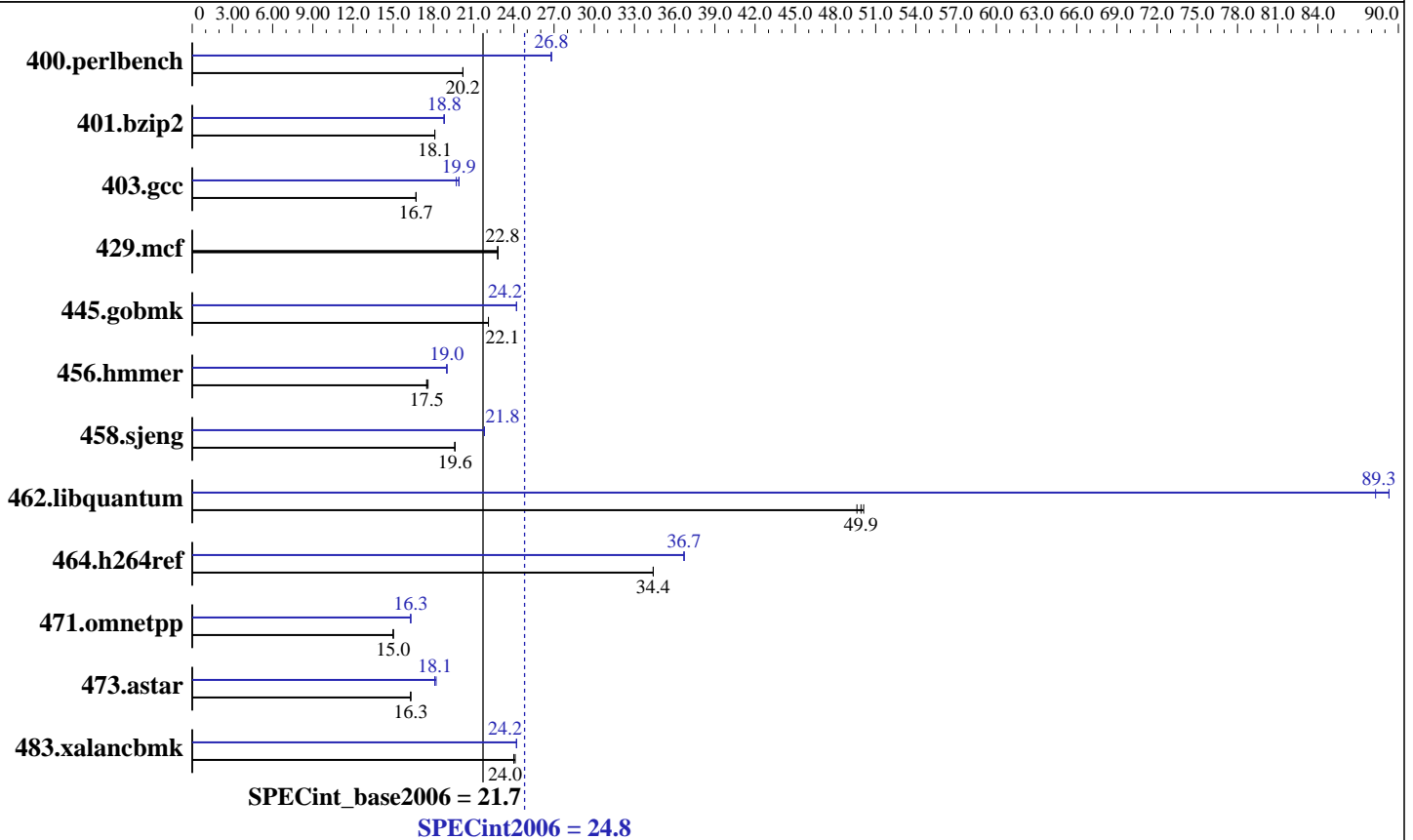
Test date: Jun-2008

Test sponsor: Dell Inc.

Hardware Availability: Jul-2008

Tested by: Dell Inc.

Software Availability: Mar-2008



### Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics: 1333 MHz Bus Speed  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4x4 GB 667 MHz CL5 FB-DIMM)  
 Disk Subsystem: 2 x 320 GB SATA 7200 RPM, RAID0  
 Other Hardware: None

### Software

Operating System: Windows Vista Ultimate 64-bit  
 Compiler: Intel C++ Compiler for IA-32, Version 10.1  
 Build 20080312 Package ID: w\_cc\_p\_10.1.021  
 Microsoft Visual Studio 2005 SP1  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	483	20.2	<b>483</b>	<b>20.2</b>	483	20.2	365	26.8	364	26.8	<b>364</b>	<b>26.8</b>
401.bzip2	532	18.1	532	18.1	<b>532</b>	<b>18.1</b>	514	18.8	514	18.8	<b>514</b>	<b>18.8</b>
403.gcc	481	16.7	481	16.7	<b>481</b>	<b>16.7</b>	404	19.9	408	19.7	<b>404</b>	<b>19.9</b>
429.mcf	400	22.8	<b>401</b>	<b>22.8</b>	401	22.8	400	22.8	<b>401</b>	<b>22.8</b>	401	22.8
445.gobmk	475	22.1	<b>475</b>	<b>22.1</b>	474	22.1	434	24.2	434	24.2	<b>434</b>	<b>24.2</b>
456.hammer	<b>532</b>	<b>17.5</b>	532	17.5	532	17.6	<b>490</b>	<b>19.0</b>	490	19.0	490	19.0
458.sjeng	619	19.6	617	19.6	<b>617</b>	<b>19.6</b>	556	21.8	556	21.8	<b>556</b>	<b>21.8</b>
462.libquantum	418	49.6	413	50.1	<b>415</b>	<b>49.9</b>	235	88.3	232	89.3	<b>232</b>	<b>89.3</b>
464.h264ref	643	34.4	643	34.4	<b>643</b>	<b>34.4</b>	603	36.7	603	36.7	<b>603</b>	<b>36.7</b>
471.omnetpp	418	15.0	416	15.0	<b>417</b>	<b>15.0</b>	383	16.3	<b>384</b>	<b>16.3</b>	384	16.3
473.astar	431	16.3	430	16.3	<b>430</b>	<b>16.3</b>	<b>387</b>	<b>18.1</b>	386	18.2	387	18.1
483.xalancbmk	287	24.0	<b>287</b>	<b>24.0</b>	287	24.1	<b>286</b>	<b>24.2</b>	285	24.2	286	24.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries were built on Windows Vista Ultimate (64-bit)  
BIOS Settings  
Adjacent Cache Line Prefetch set to ON

## Compiler Invocation

C benchmarks:  
icl -Qstd=c99

C++ benchmarks:  
icl

## Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32  
483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword



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## Base Optimization Flags

C benchmarks:

-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000 libguide40.lib

C++ benchmarks:

-fast -Qcxx\_features /F512000000 shlw32m.lib libguide40.lib  
-link /FORCE:MULTIPLE

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qprefetch  
/F512000000 libguide40.lib

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qipo  
-Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qansi-alias -Qopt-multi-version-aggressive /F512000000  
libguide40.lib

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll14  
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll14 -Ob0 -Qprefetch  
-Qopt-streaming-stores:always -Qparallel  
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll12  
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qopt-ra-region-strategy=block -Qcxx\_features /F512000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

473.astar: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qopt-ra-region-strategy=routine -Qcxx\_features /F512000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qcxx\_features /F512000000 shlw32m.lib libguide40.lib  
-link /FORCE:MULTIPLE

## Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090713.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090713.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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