



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 125

ACTINA SOLAR 410 S3 (Intel Xeon processor E5430, 2.66GHz)

SPECint_rate_base2006 = 102

CPU2006 license: 9008

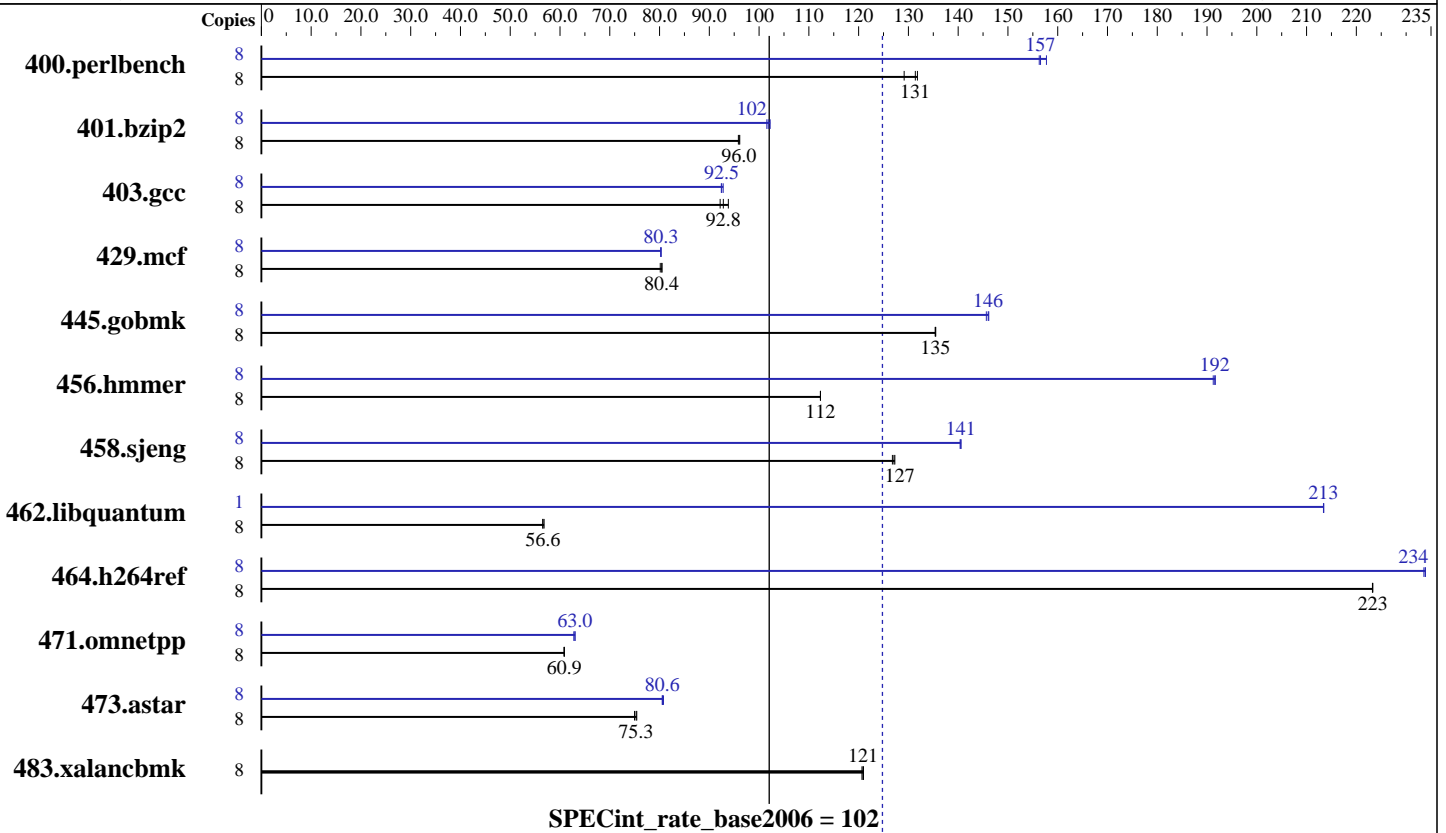
Test date: Jun-2008

Test sponsor: ACTION S.A.

Hardware Availability: Dec-2007

Tested by: ACTION S.A.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 2.66 GHz, 12 MB L2, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x 2 GB PC2-5300 ECC FBDIMM)
 Disk Subsystem: 1x 160 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	605	129	<u>595</u>	<u>131</u>	593	132	8	<u>499</u>	<u>157</u>	500	156	496	158
401.bzip2	8	<u>804</u>	<u>96.0</u>	806	95.8	803	96.1	8	760	102	755	102	<u>758</u>	<u>102</u>
403.gcc	8	686	93.8	<u>694</u>	<u>92.8</u>	699	92.2	8	697	92.4	<u>696</u>	<u>92.5</u>	694	92.8
429.mcf	8	910	80.1	<u>907</u>	<u>80.4</u>	906	80.5	8	909	80.3	<u>909</u>	<u>80.3</u>	909	80.2
445.gobmk	8	<u>620</u>	<u>135</u>	619	135	620	135	8	<u>575</u>	<u>146</u>	574	146	576	146
456.hammer	8	664	112	<u>664</u>	<u>112</u>	664	112	8	389	192	390	191	<u>390</u>	<u>192</u>
458.sjeng	8	763	127	<u>762</u>	<u>127</u>	761	127	8	689	140	<u>689</u>	<u>141</u>	689	141
462.libquantum	8	2918	56.8	2932	56.5	<u>2930</u>	<u>56.6</u>	1	97.1	213	97.1	213	<u>97.1</u>	<u>213</u>
464.h264ref	8	<u>793</u>	<u>223</u>	793	223	793	223	8	758	234	<u>757</u>	<u>234</u>	757	234
471.omnetpp	8	821	60.9	822	60.8	<u>822</u>	<u>60.9</u>	8	797	62.8	793	63.0	<u>794</u>	<u>63.0</u>
473.astar	8	745	75.4	749	75.0	<u>746</u>	<u>75.3</u>	8	697	80.5	<u>697</u>	<u>80.6</u>	695	80.8
483.xalancbmk	8	456	121	<u>457</u>	<u>121</u>	457	121	8	456	121	<u>457</u>	<u>121</u>	457	121

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode.
Taskset command was used to bind processes to CPUs.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:

icpc

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
```

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Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

Same as Base Other Flags

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.xml>



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For other inquiries, please contact webmaster@spec.org.

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